



Radxa Dragon Q8B

schematic

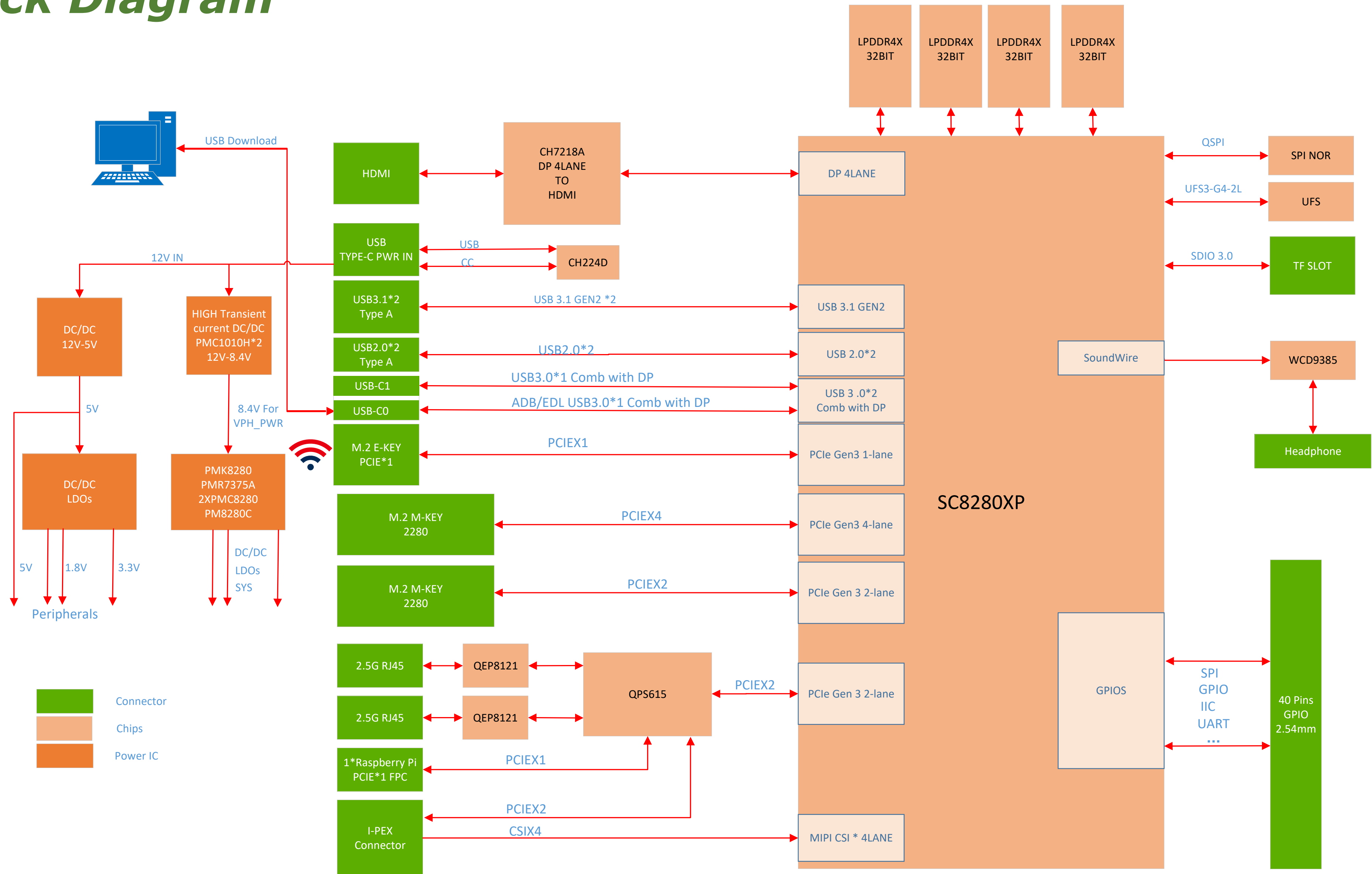
V1.3



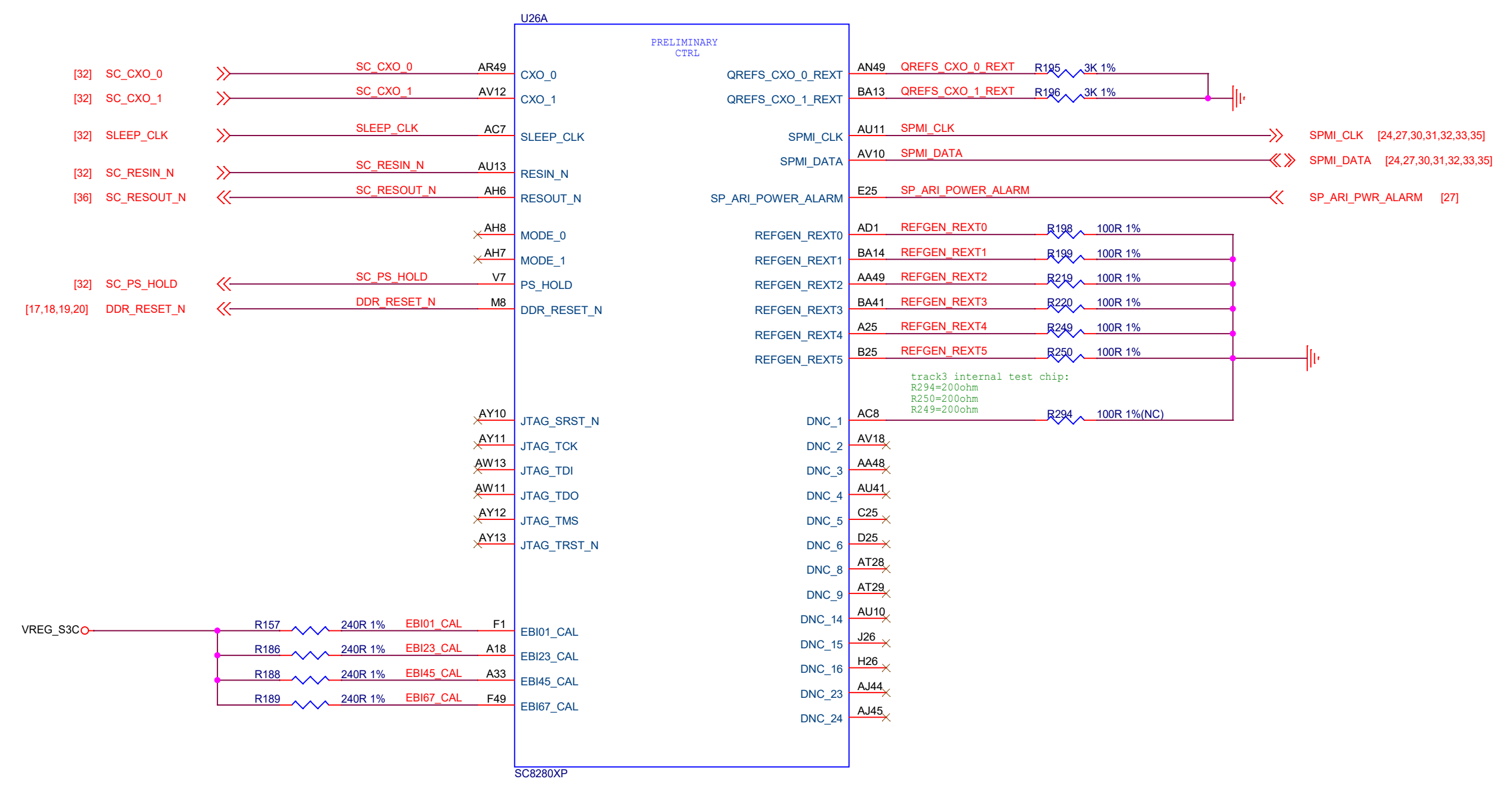
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Block Diagram



SC8280XP_GPIO1

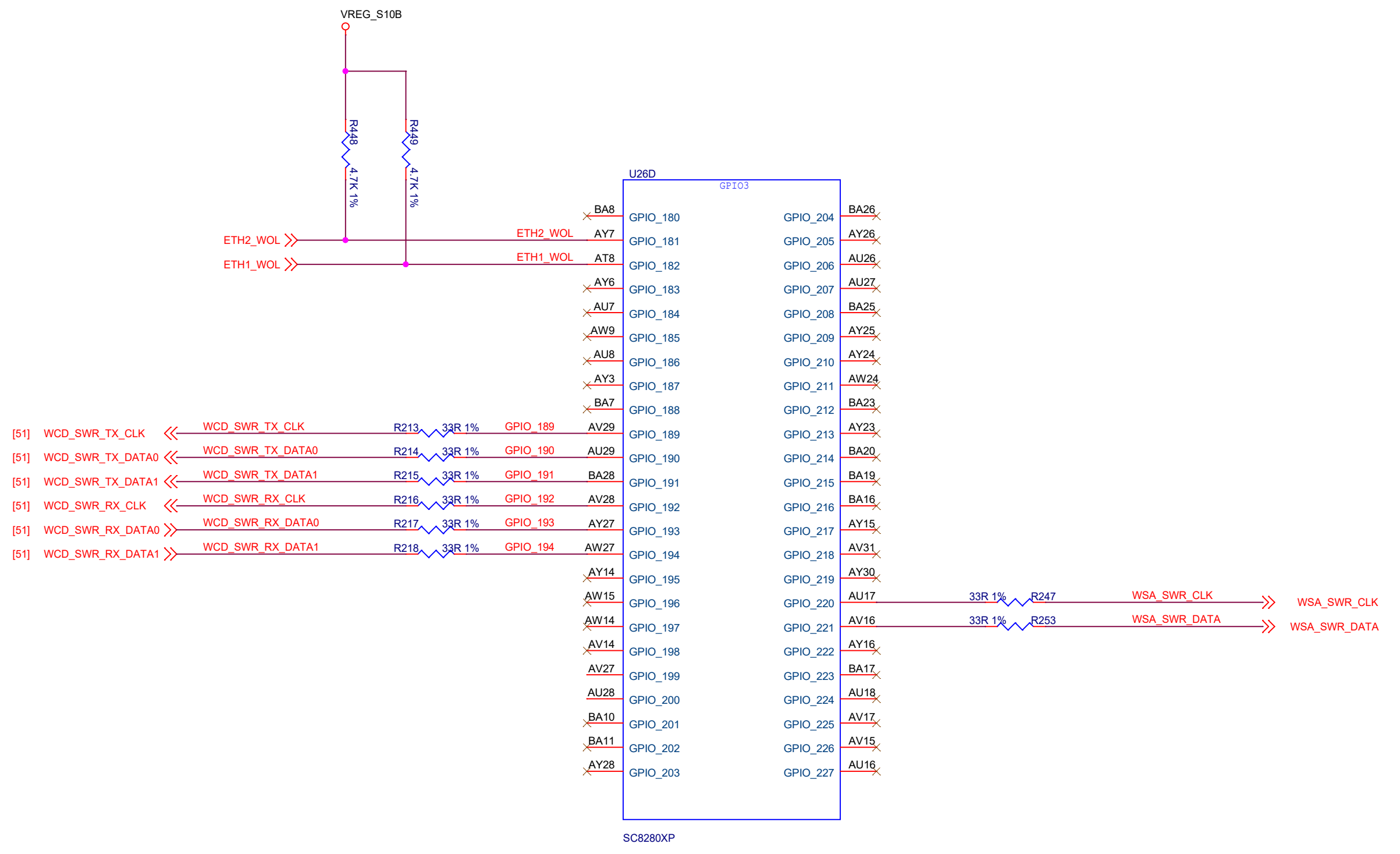


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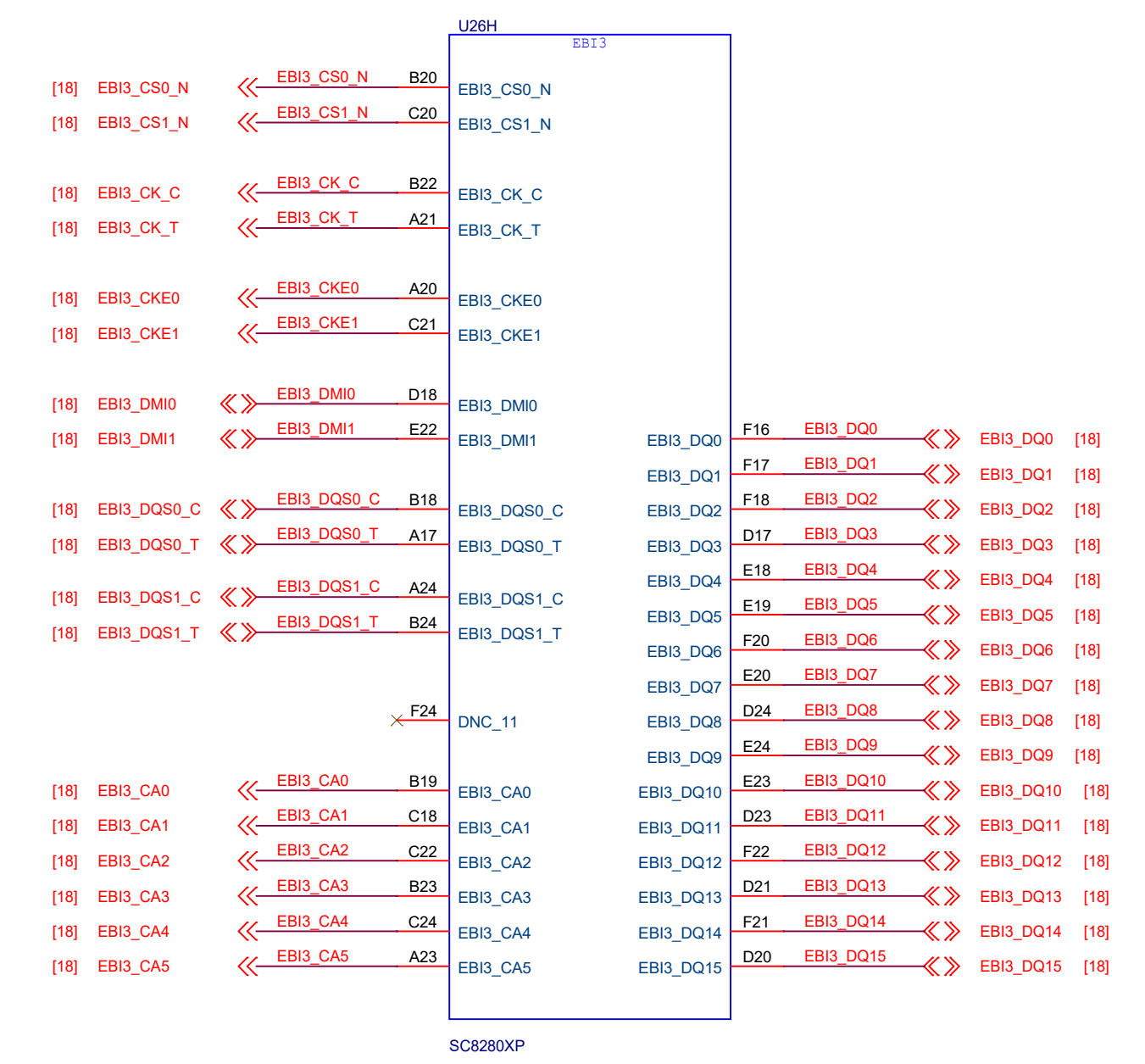
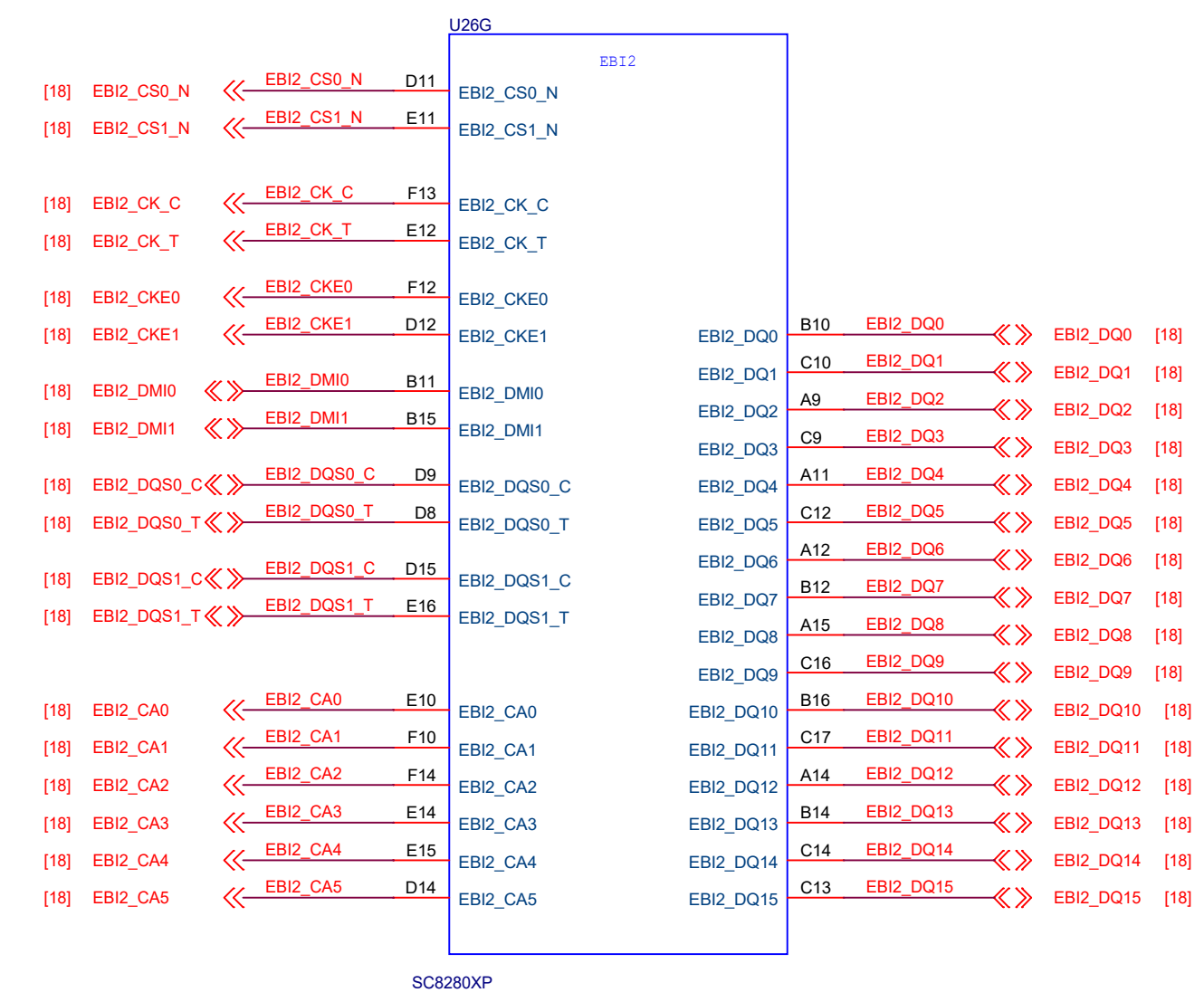
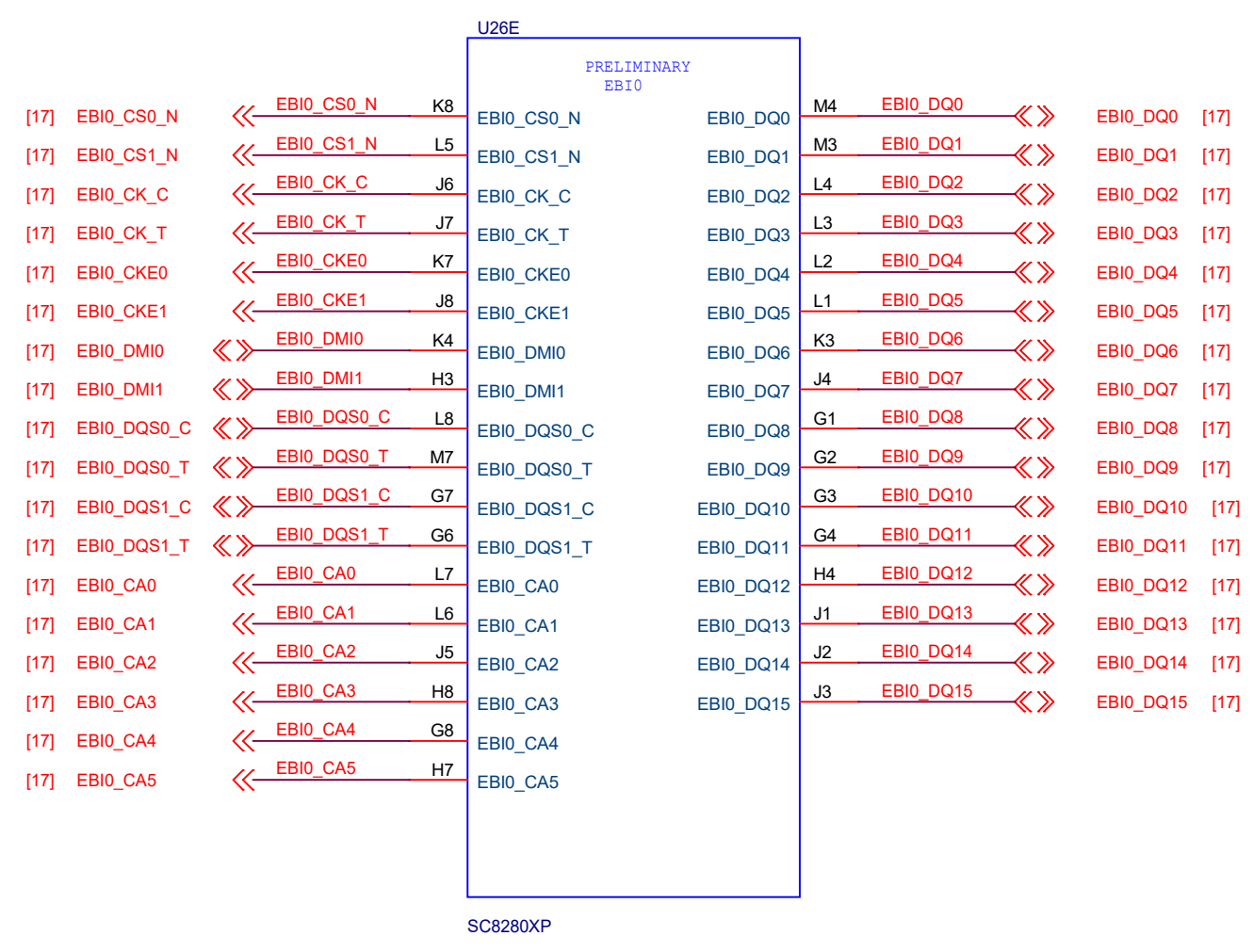
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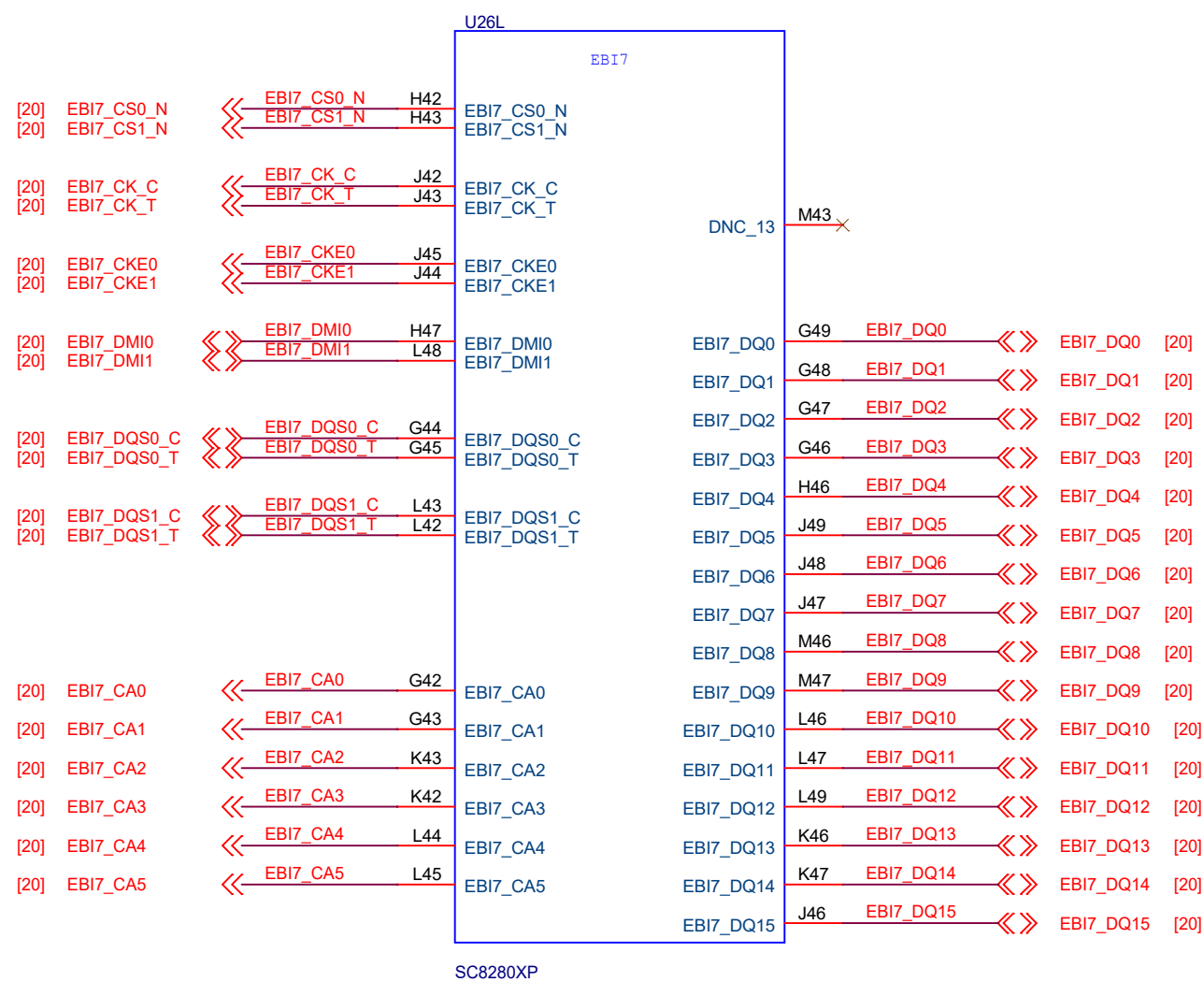
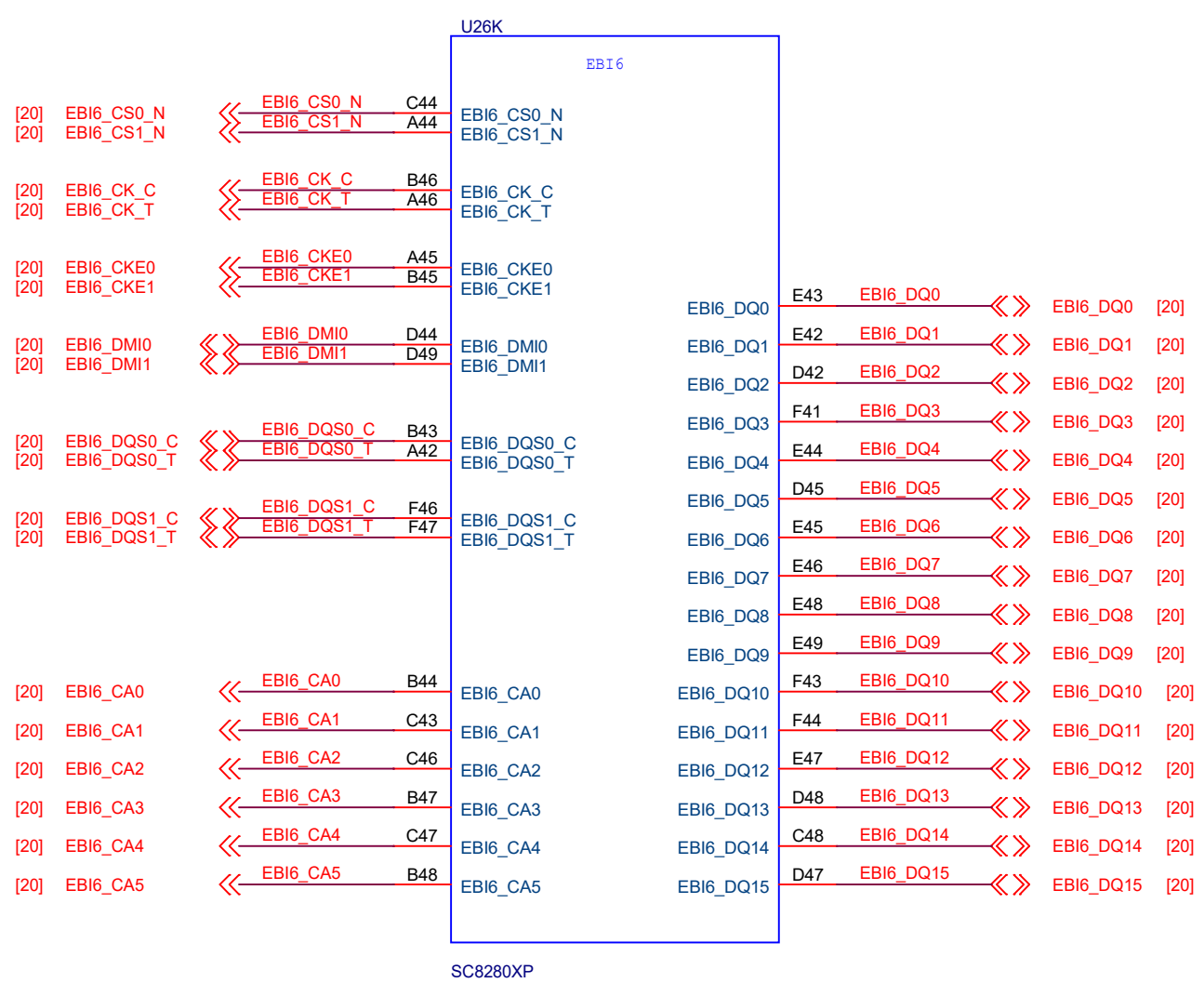
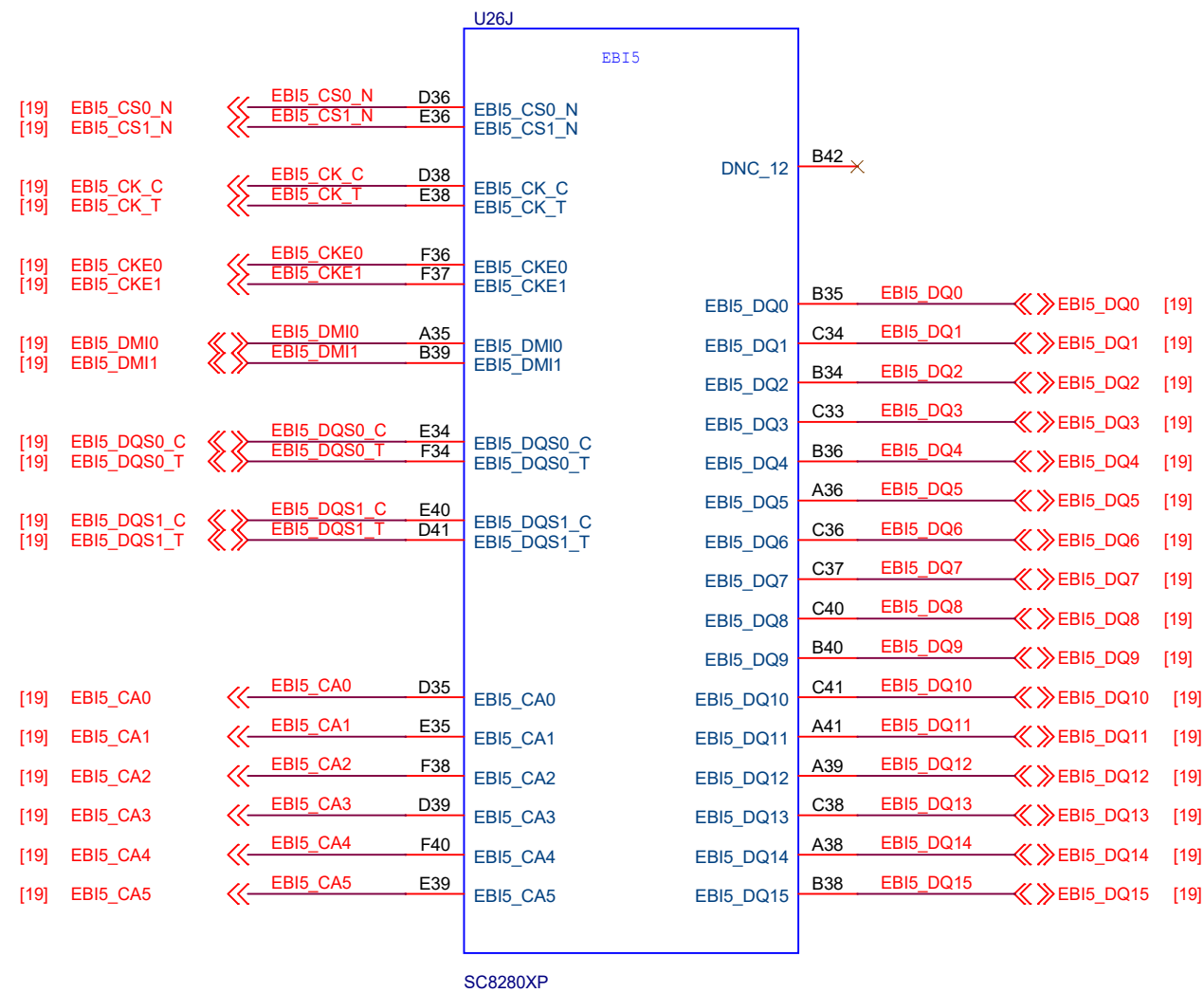
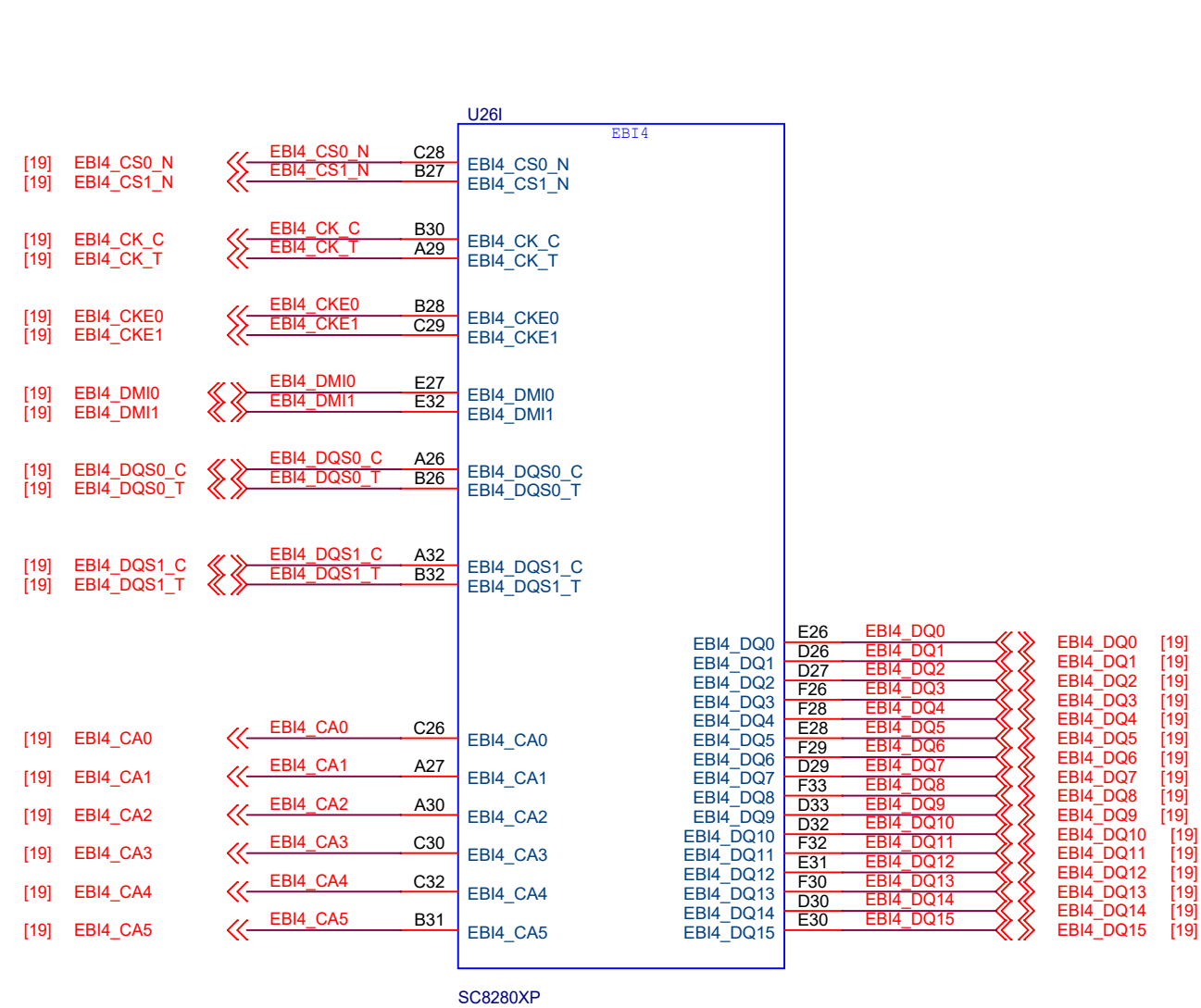
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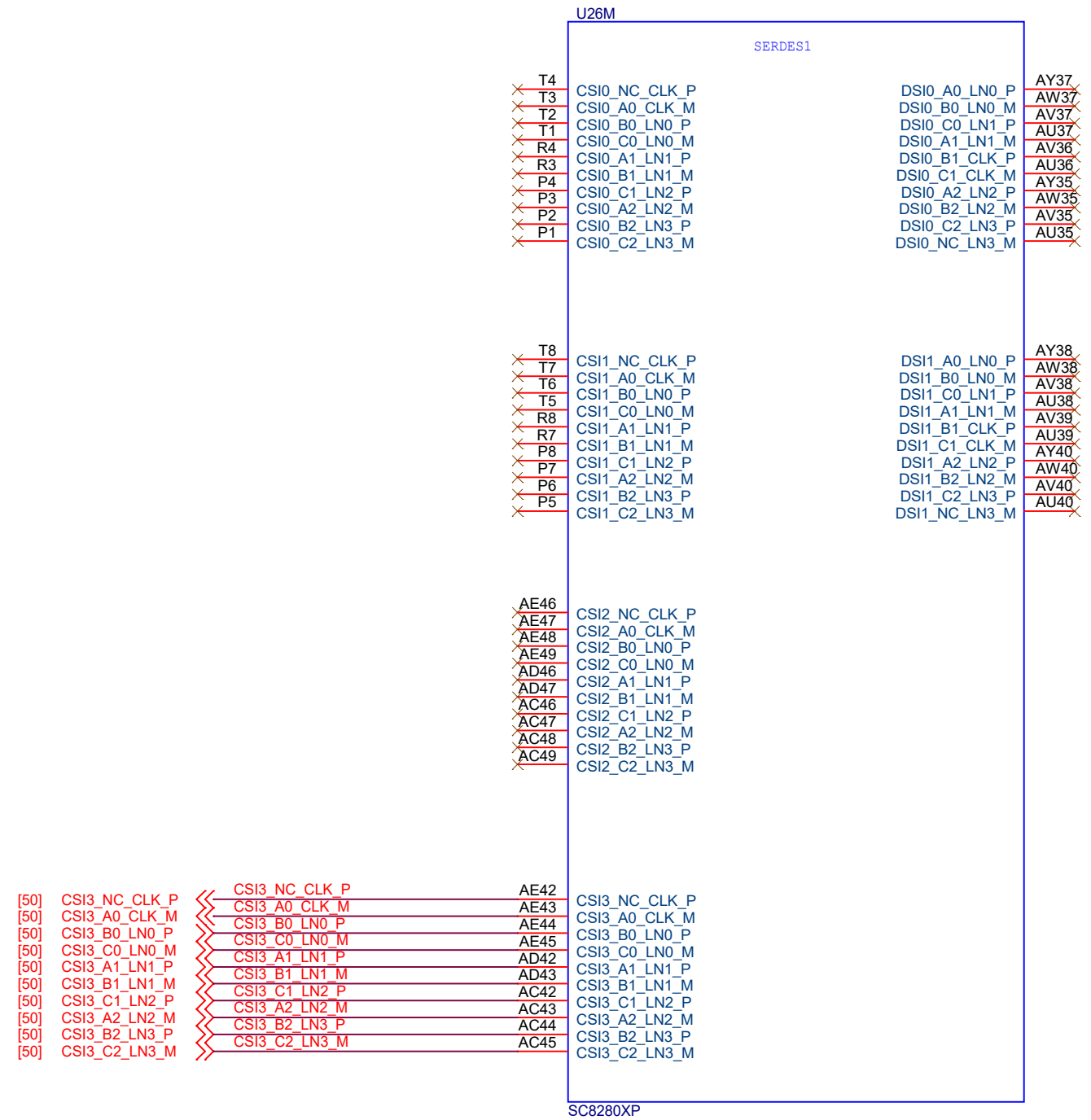
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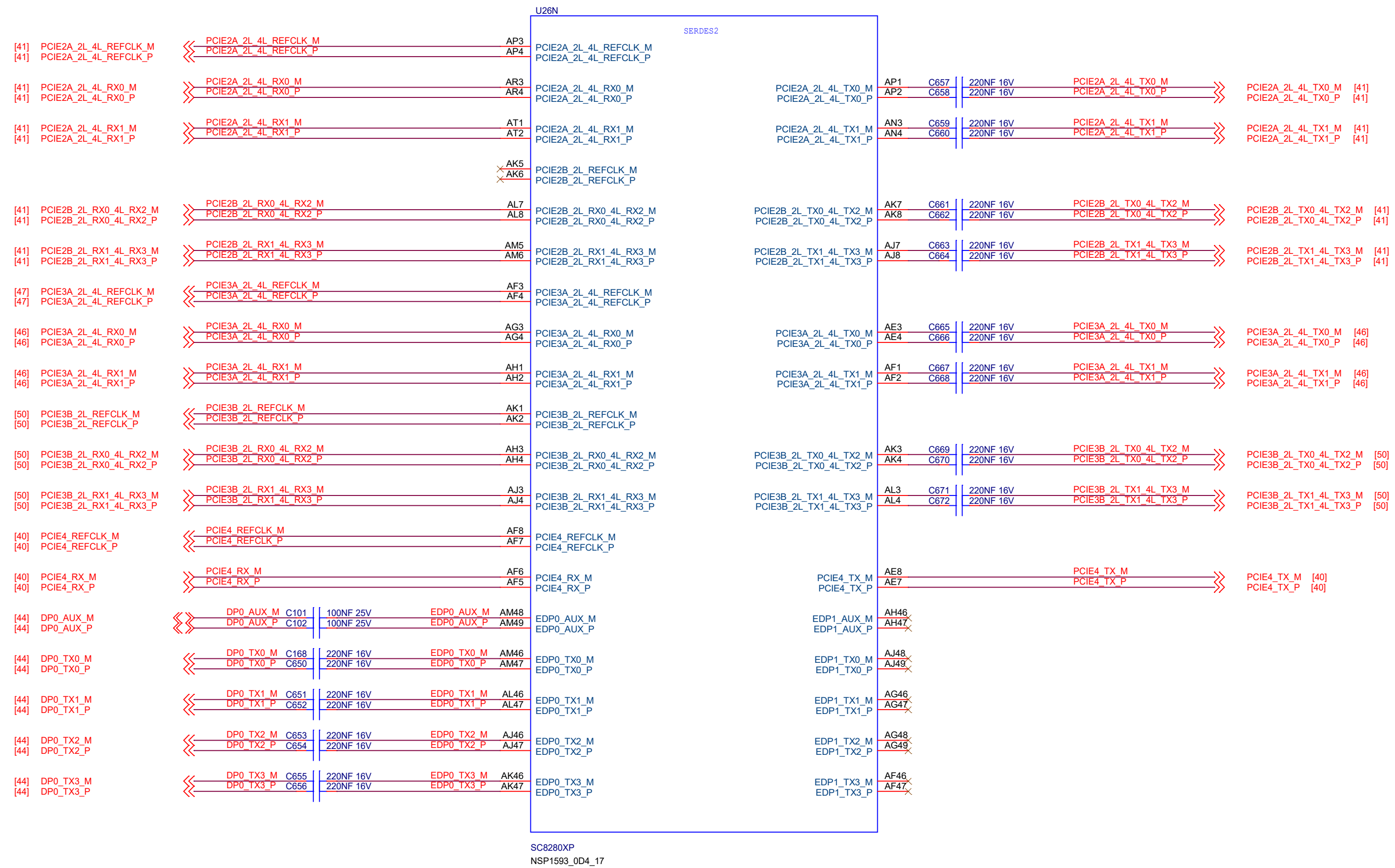
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SC8280XP_SERDES1



SC8280XP_SERDES2

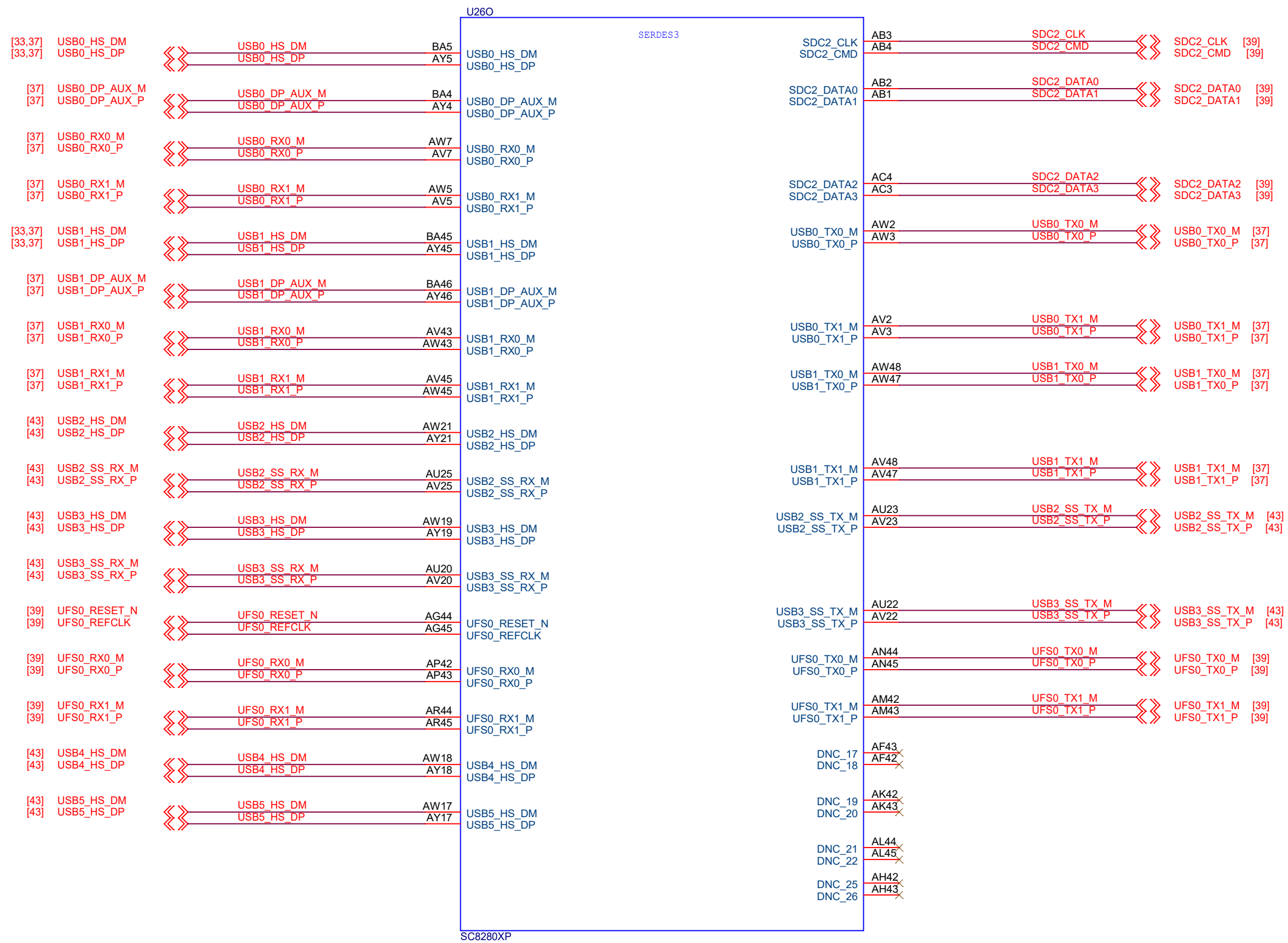


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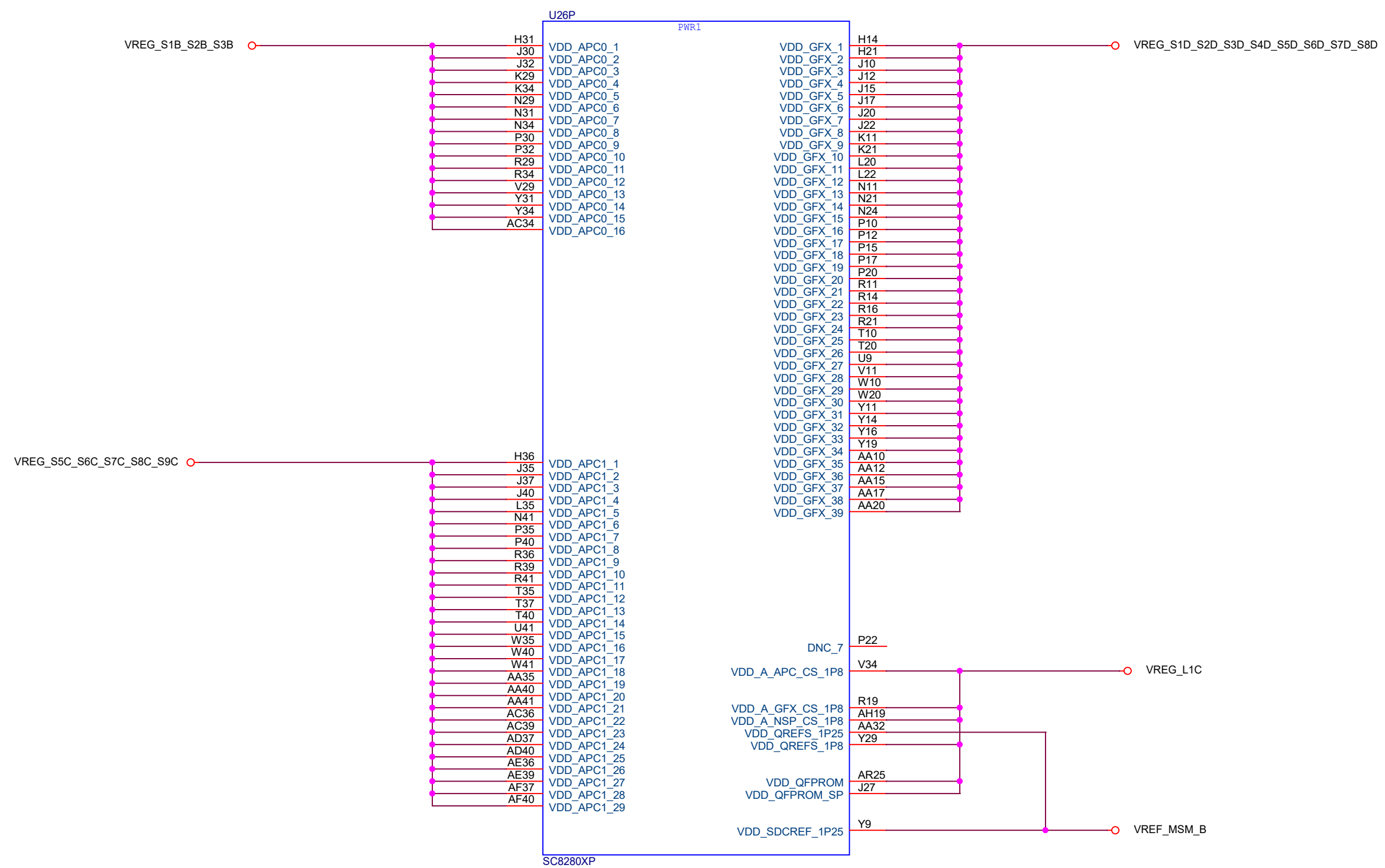


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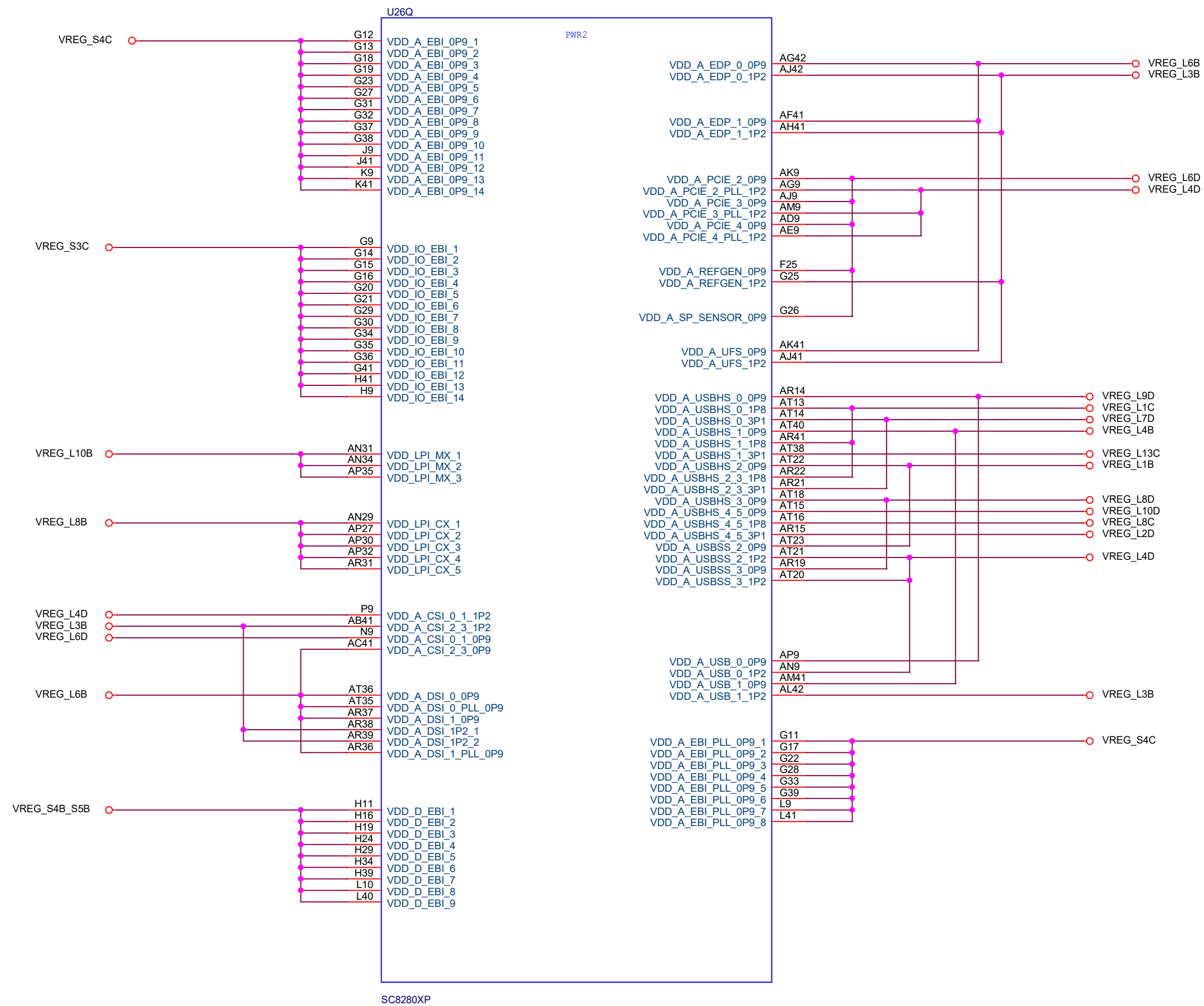


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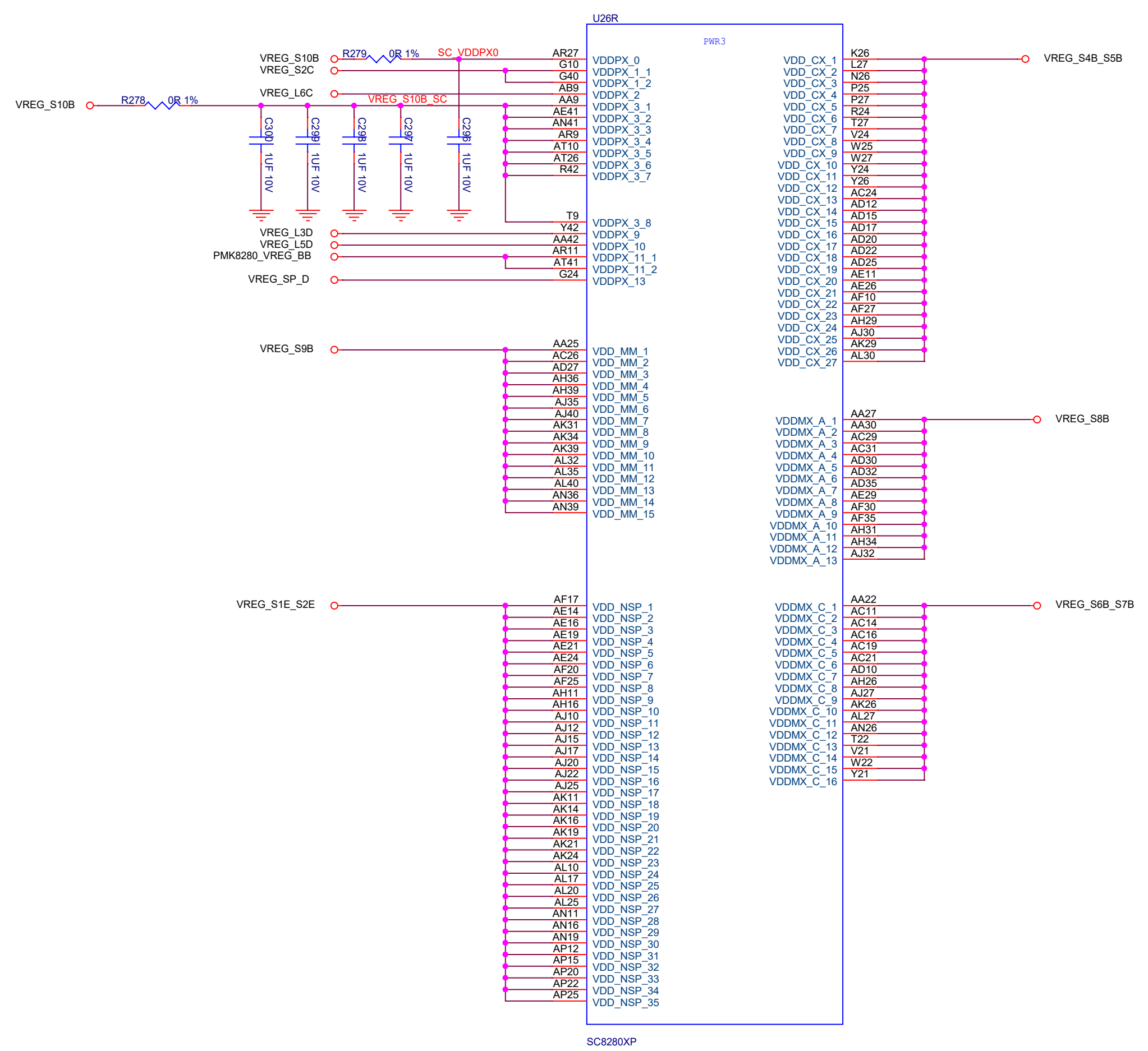
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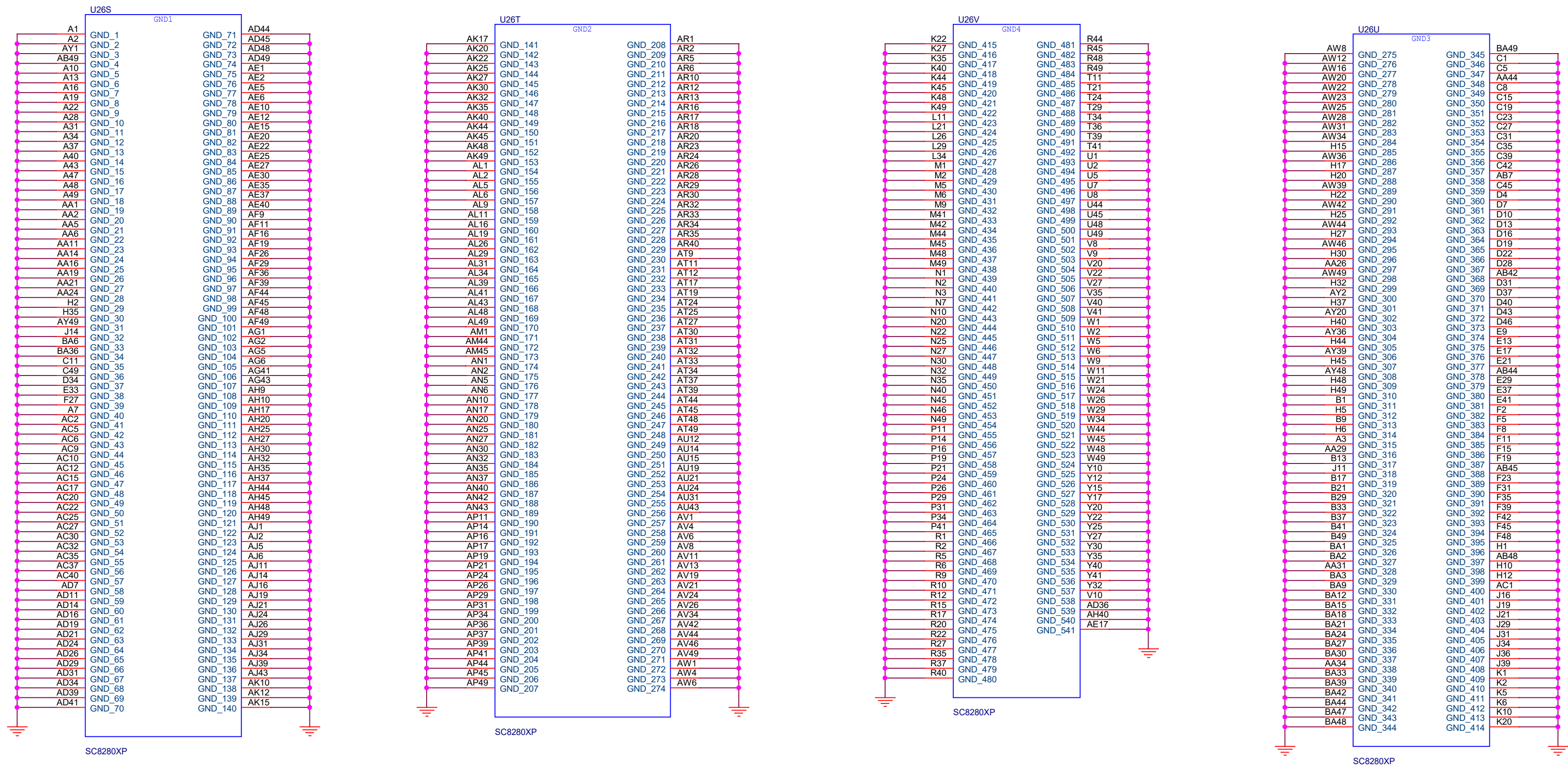


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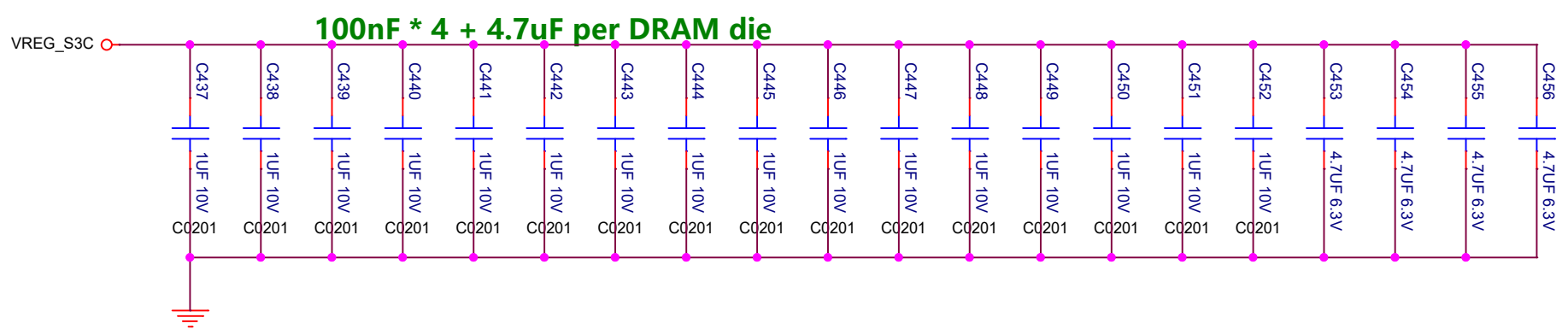
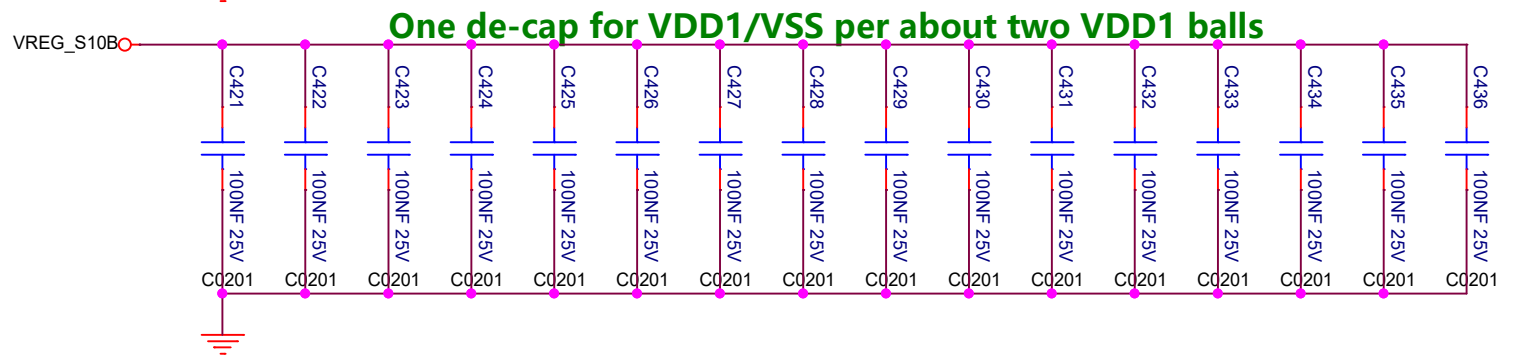
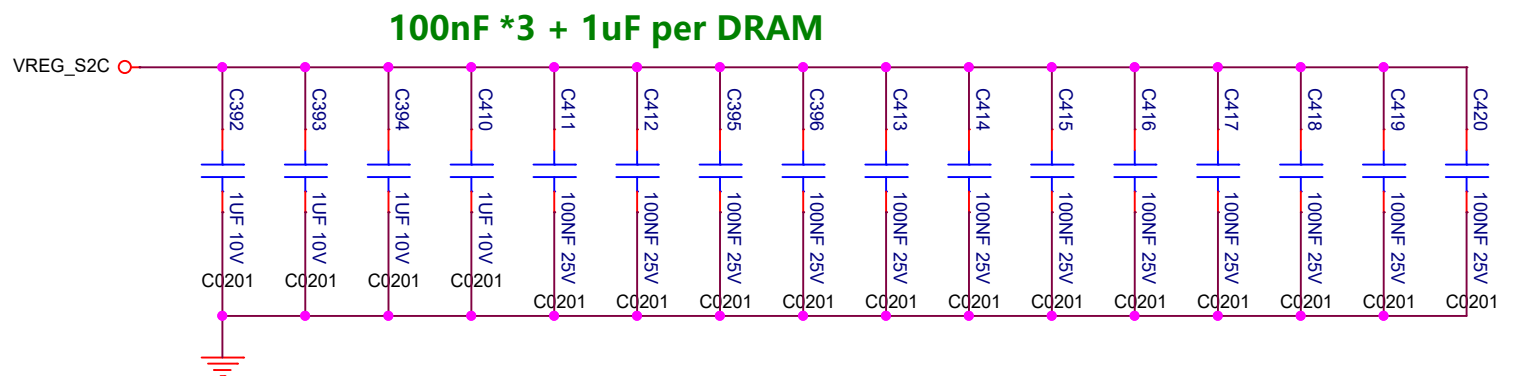
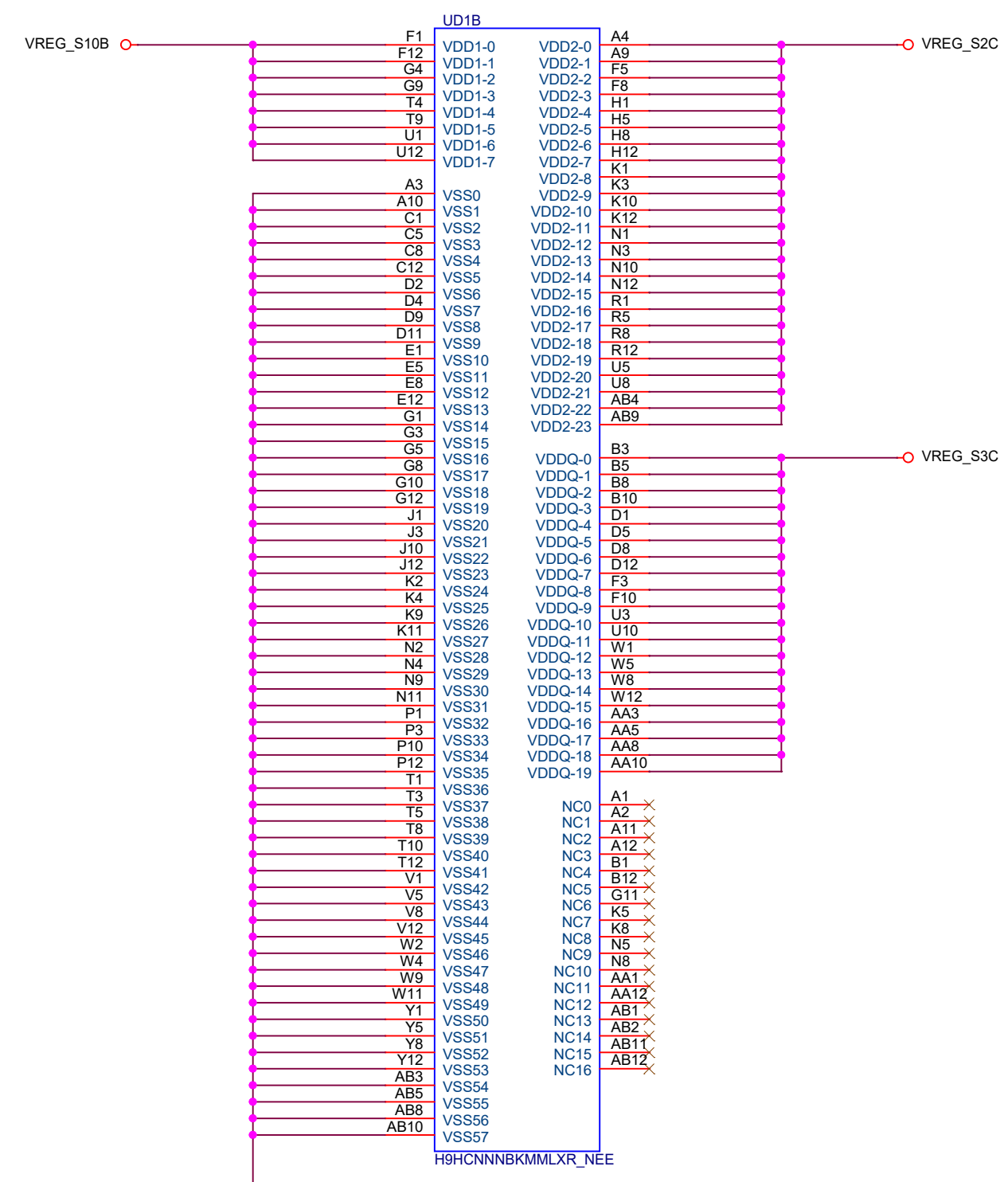
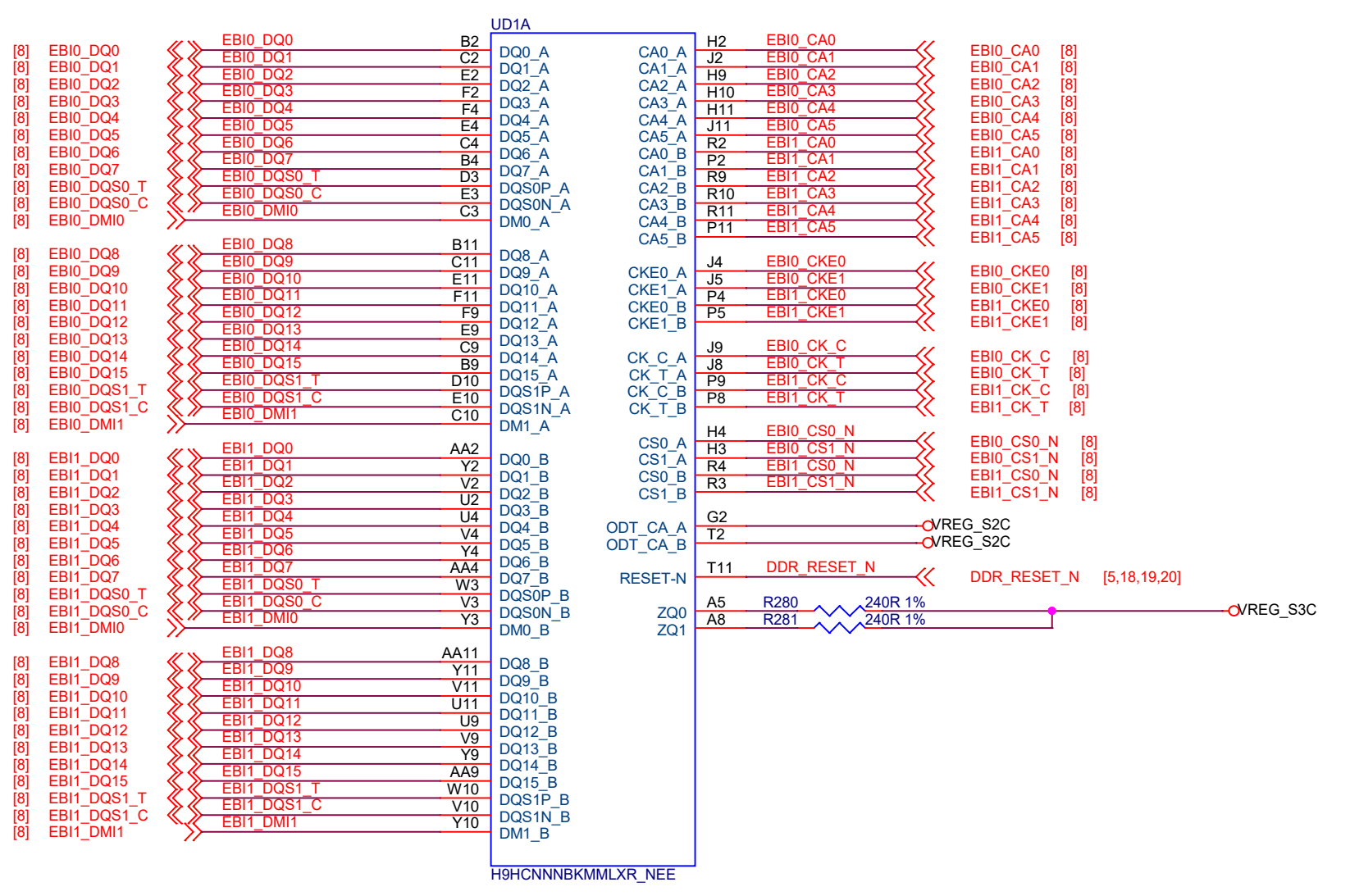
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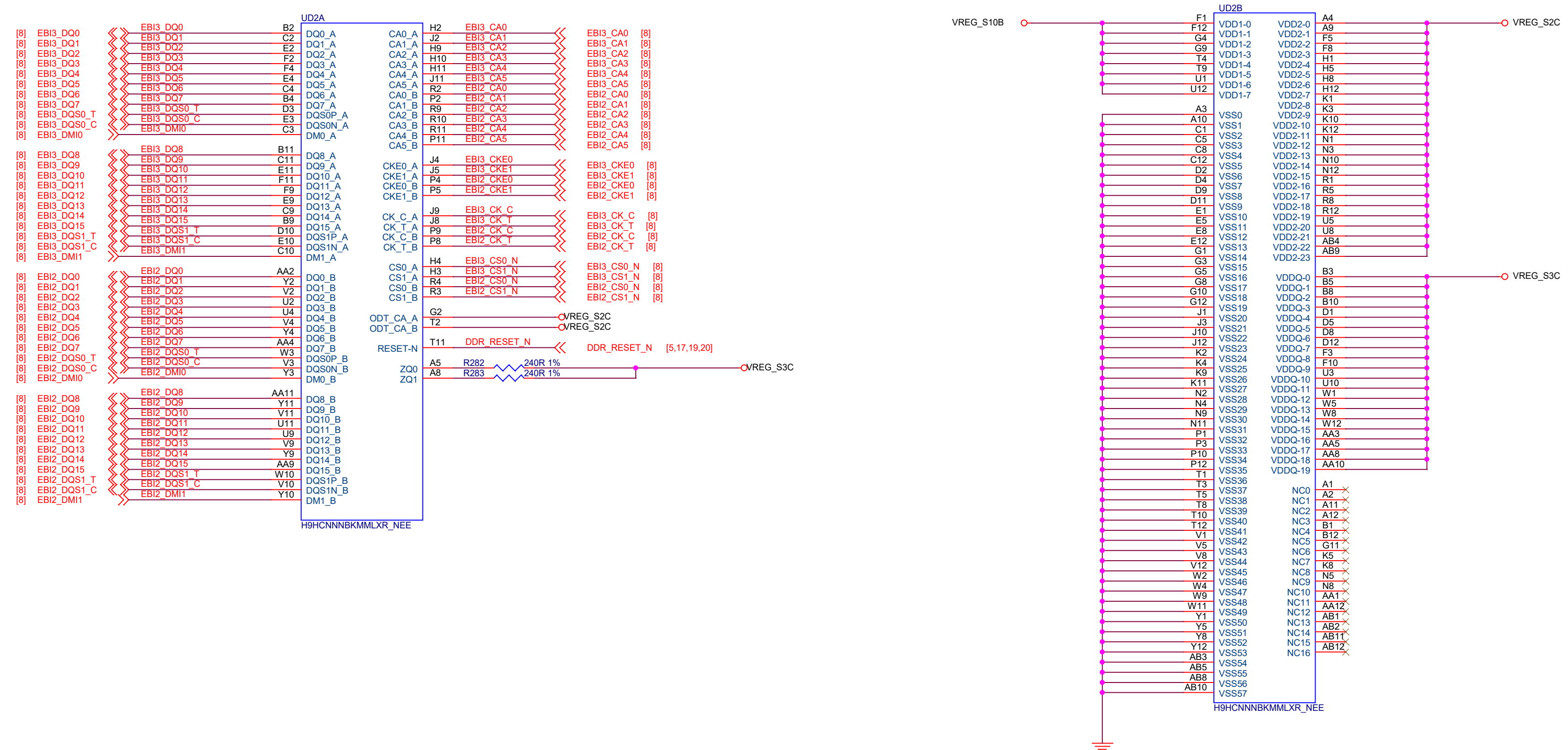
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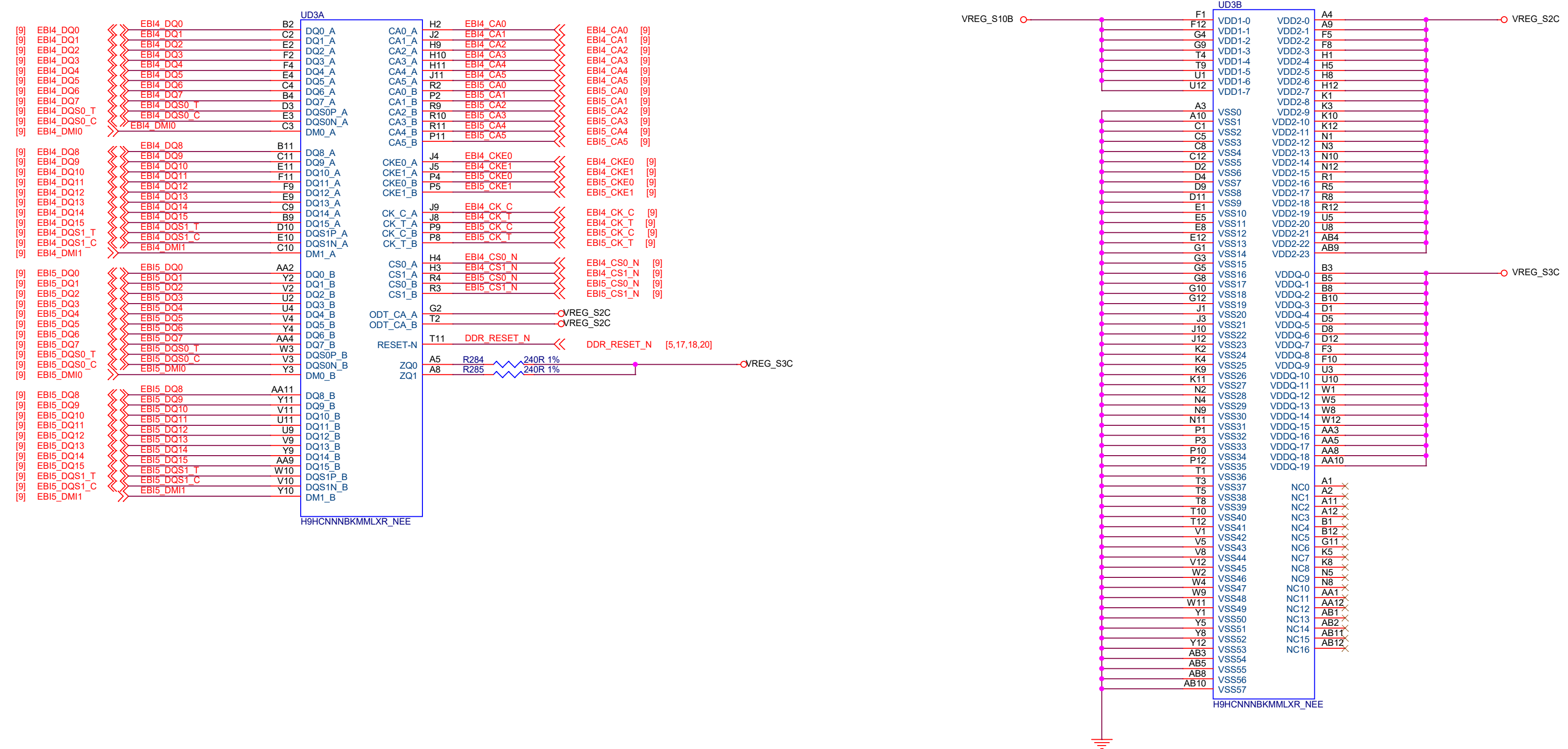
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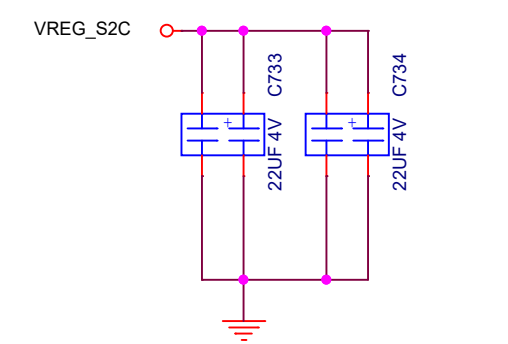
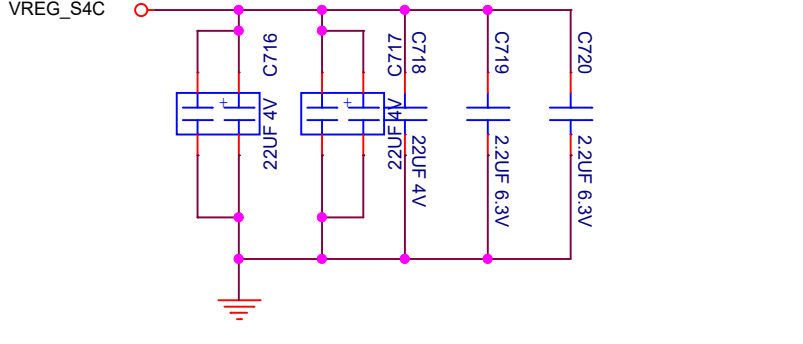
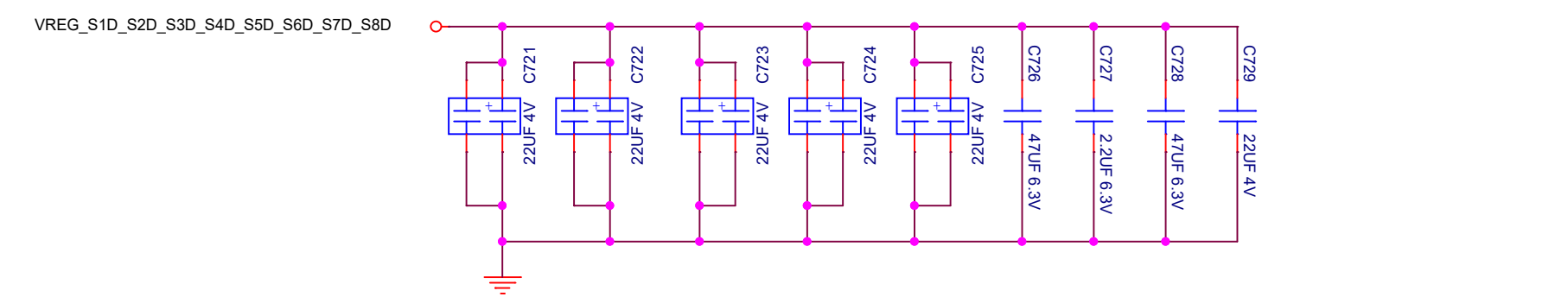
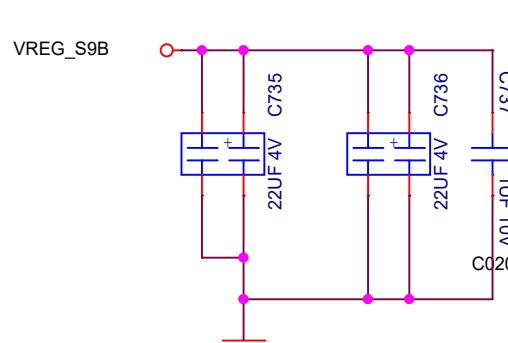
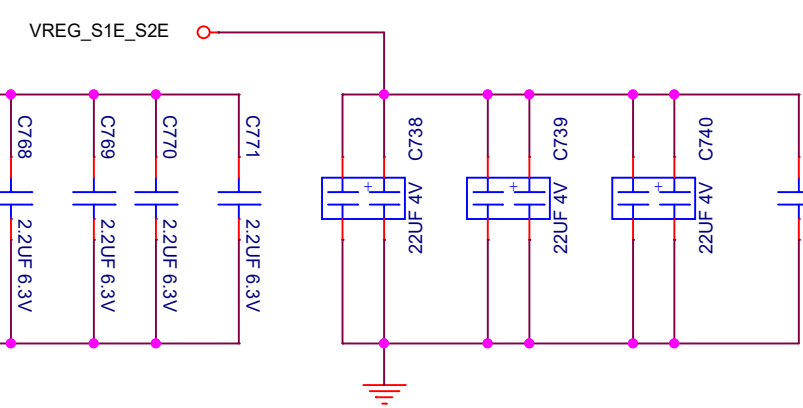
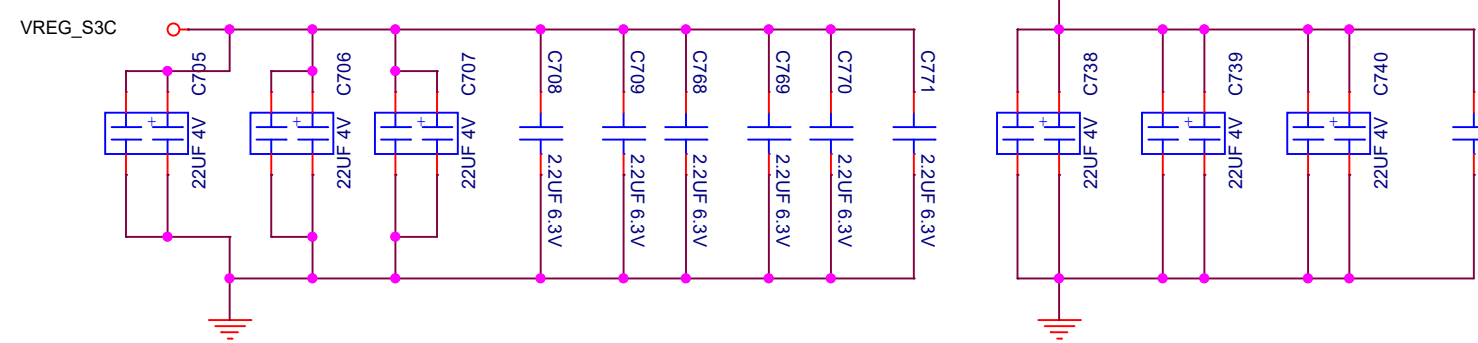
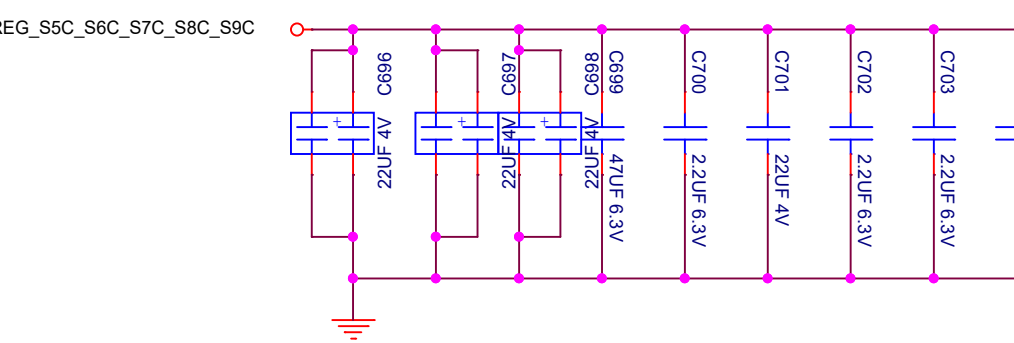
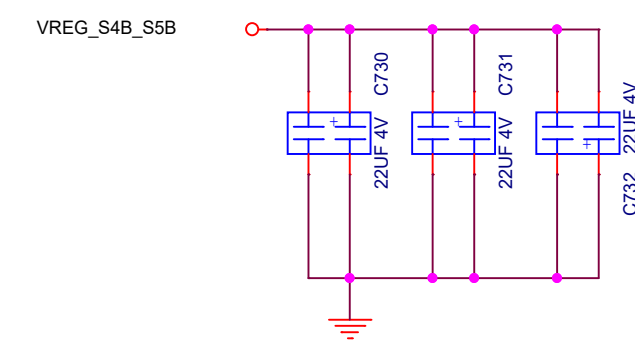
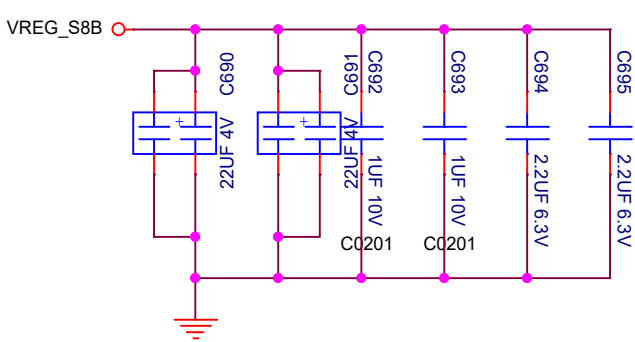
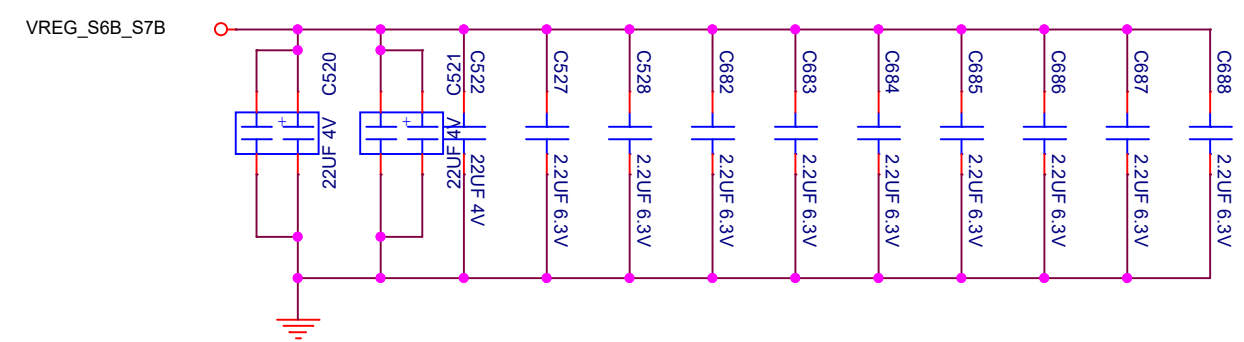
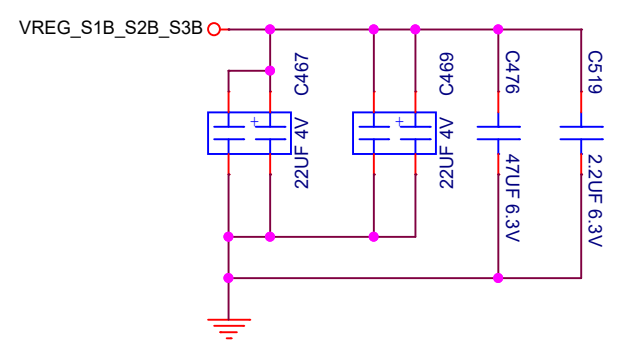
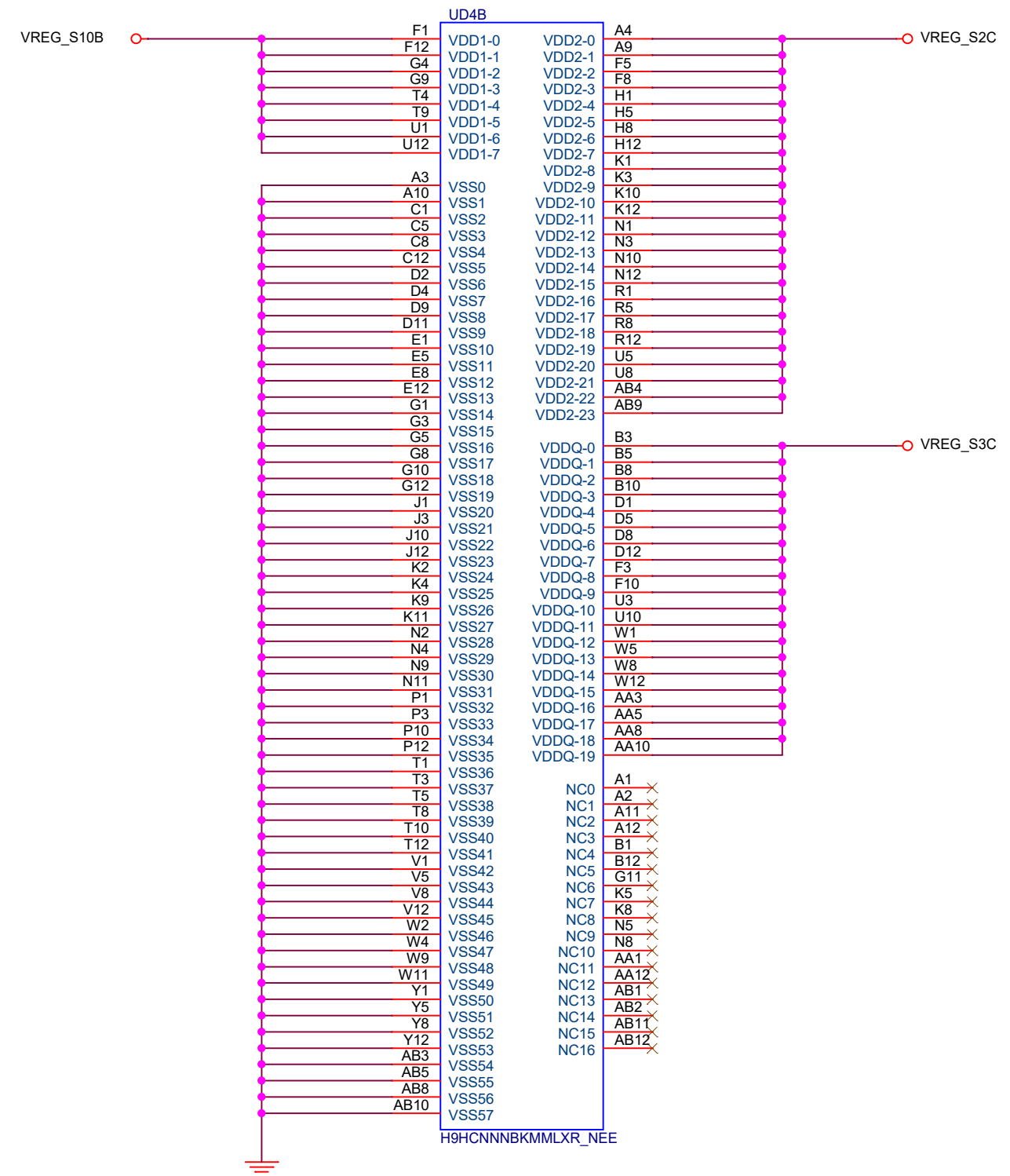
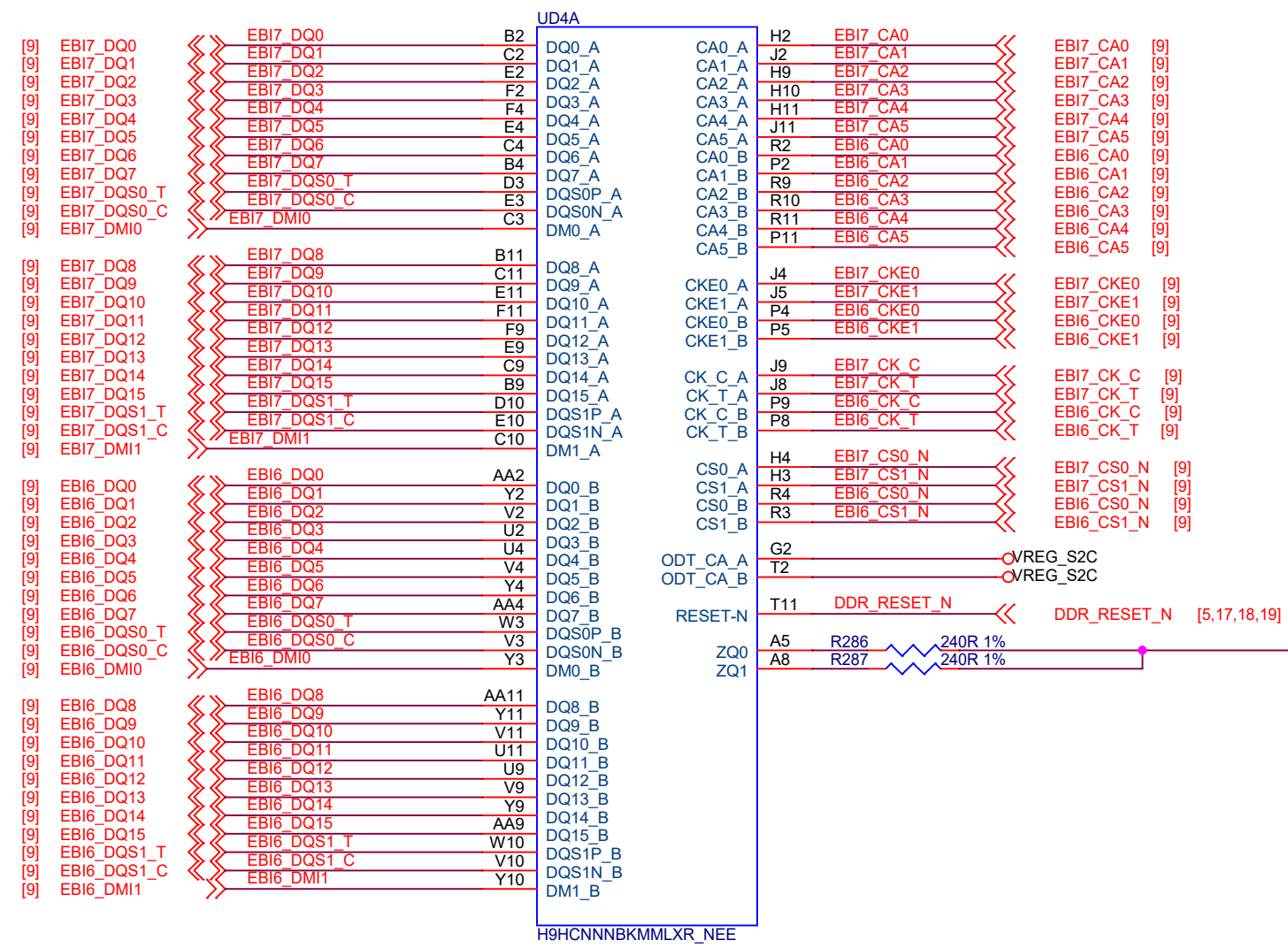
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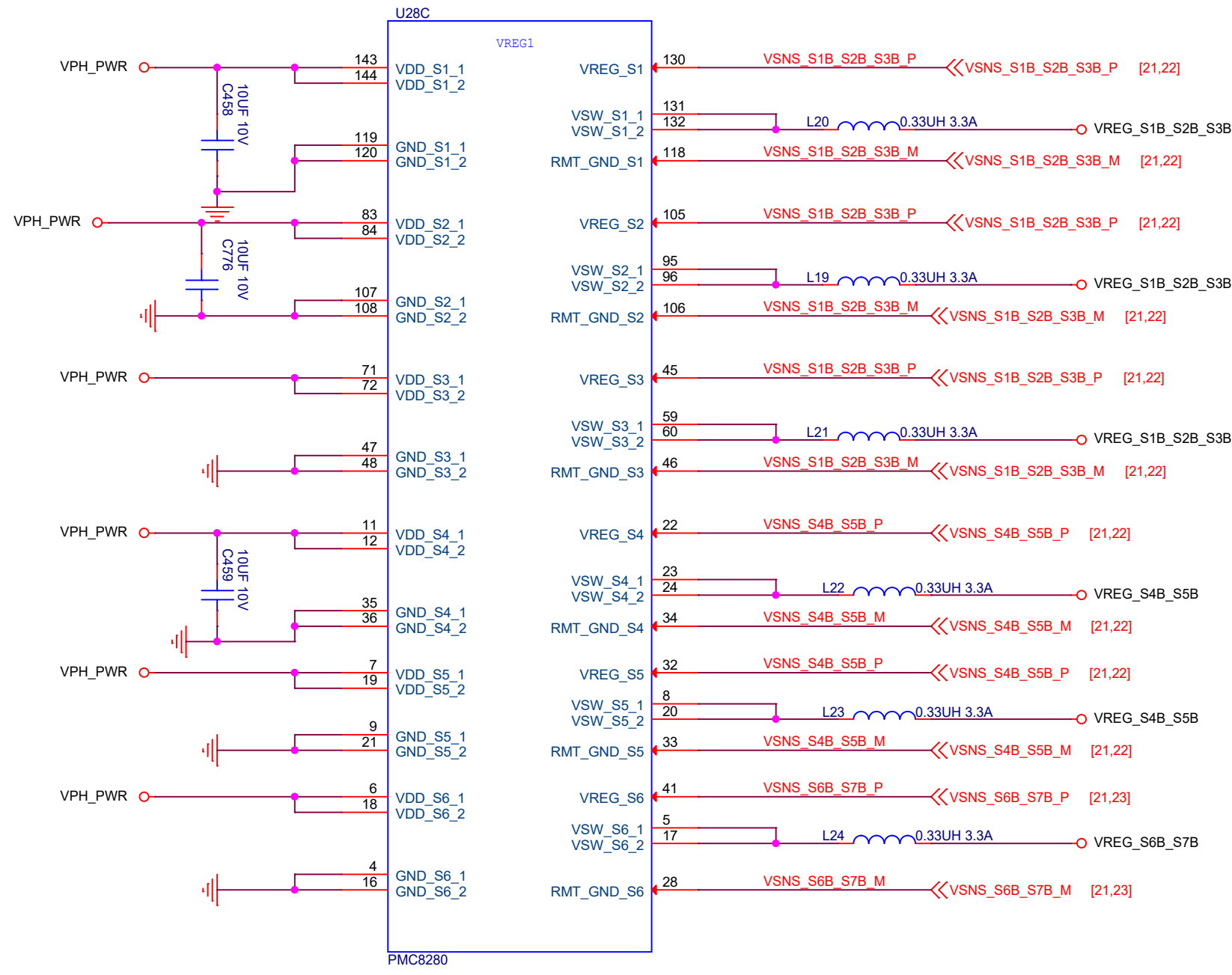
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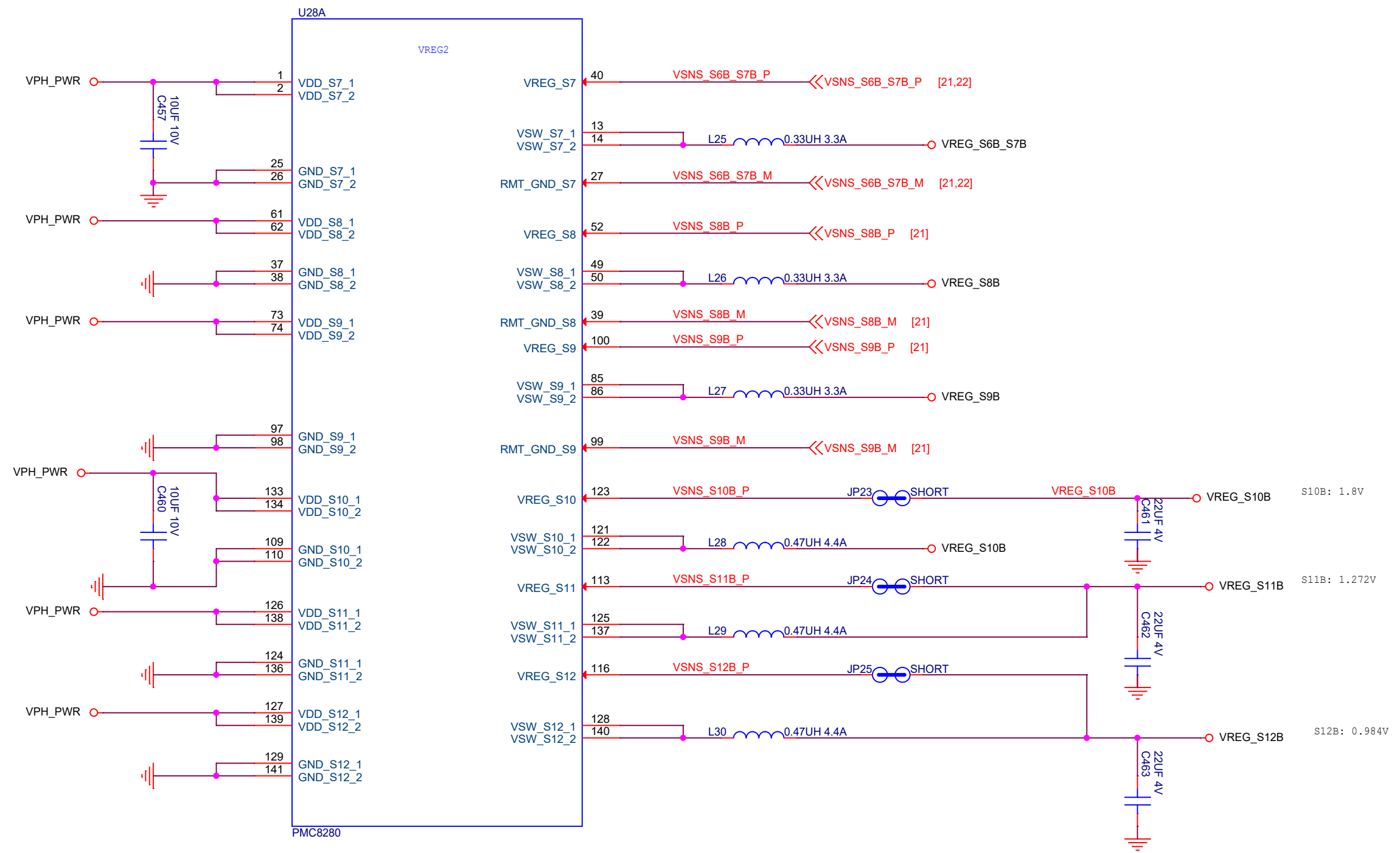
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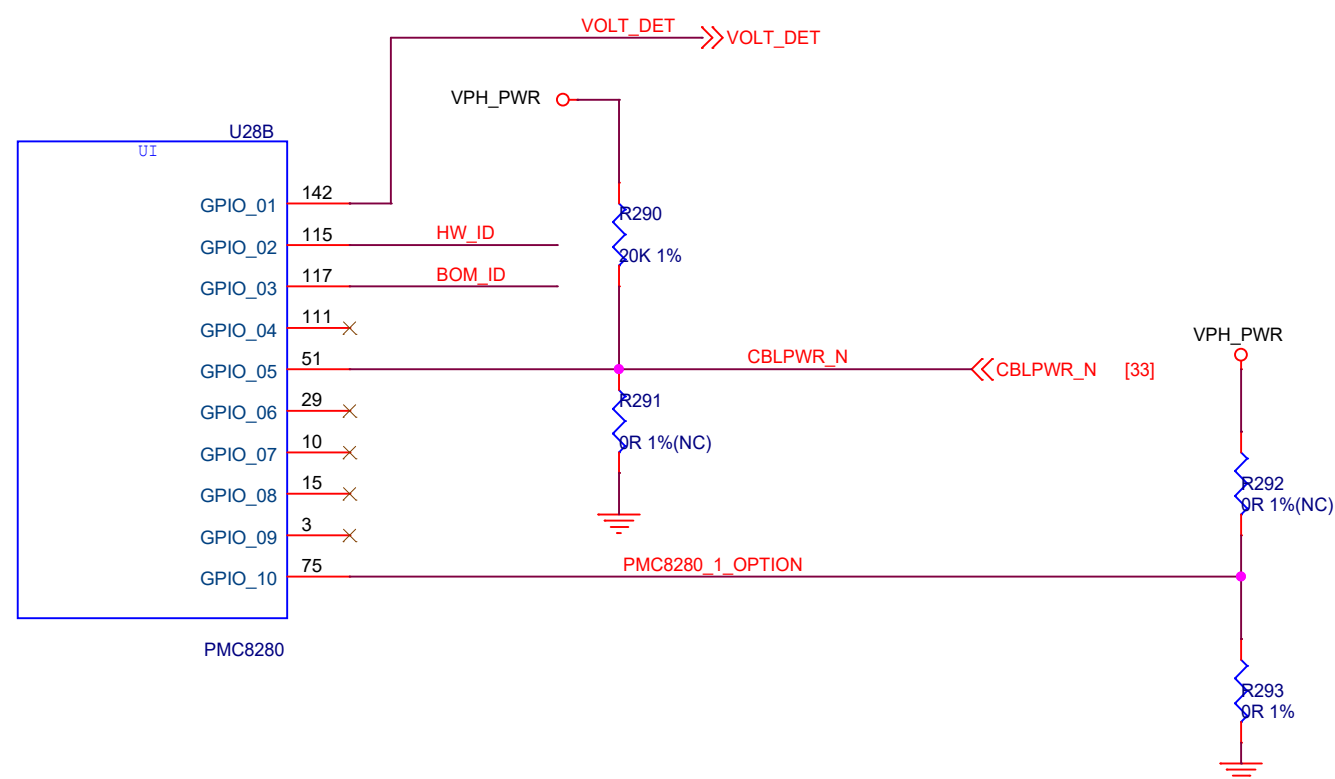
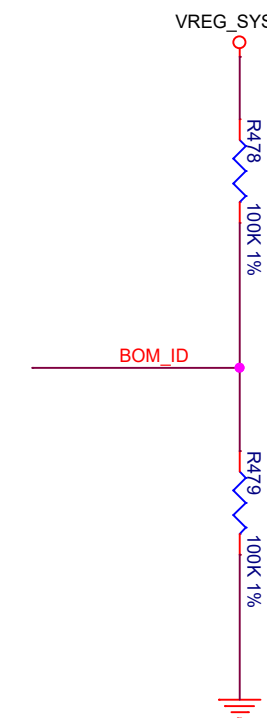
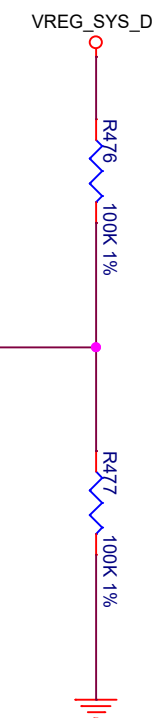
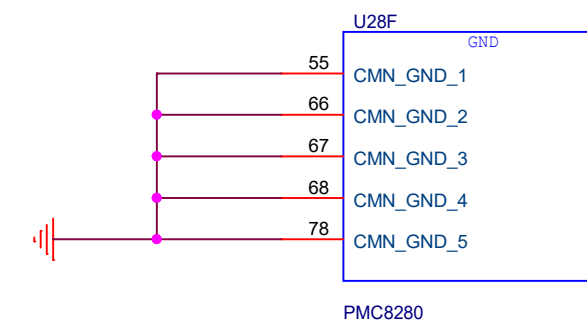
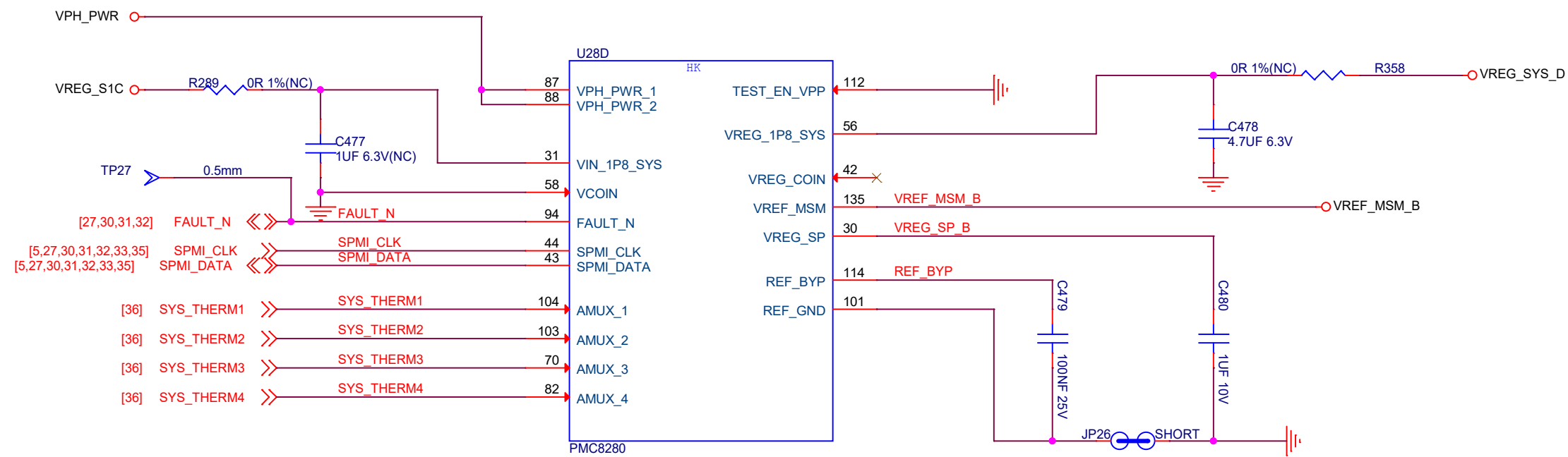
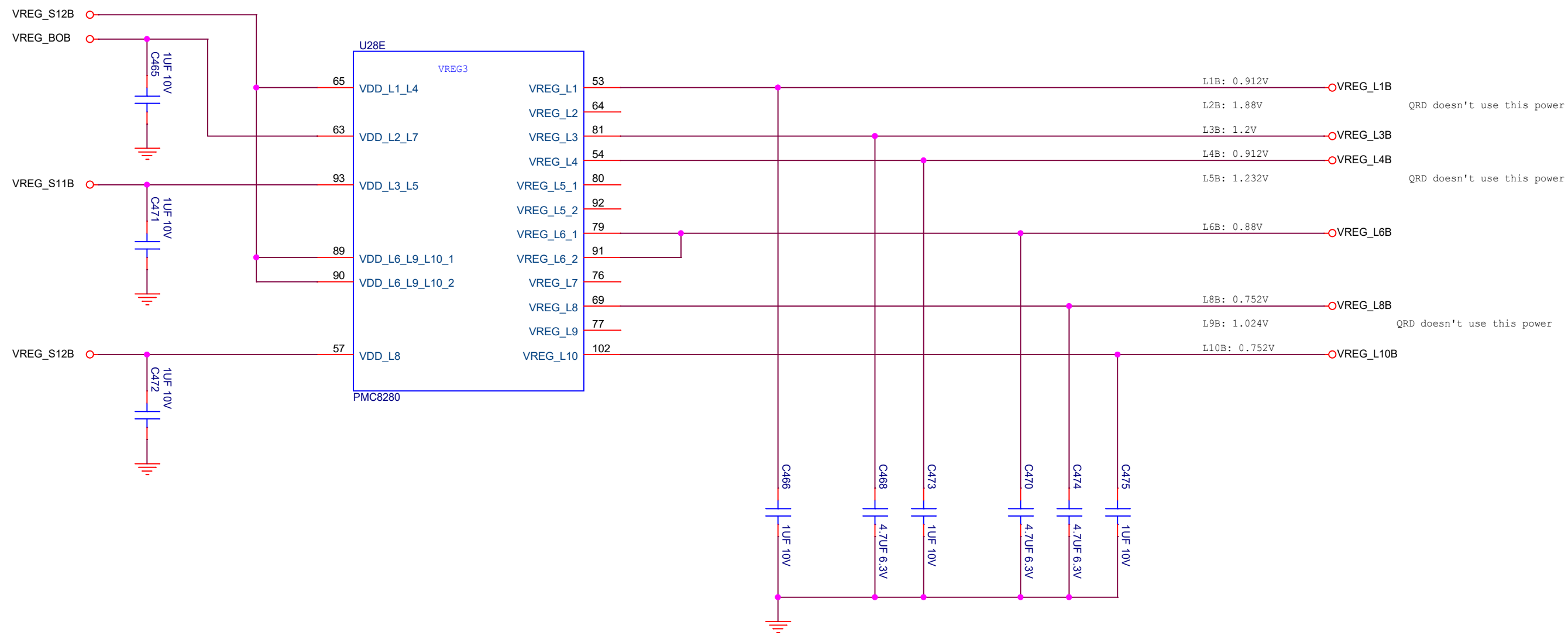


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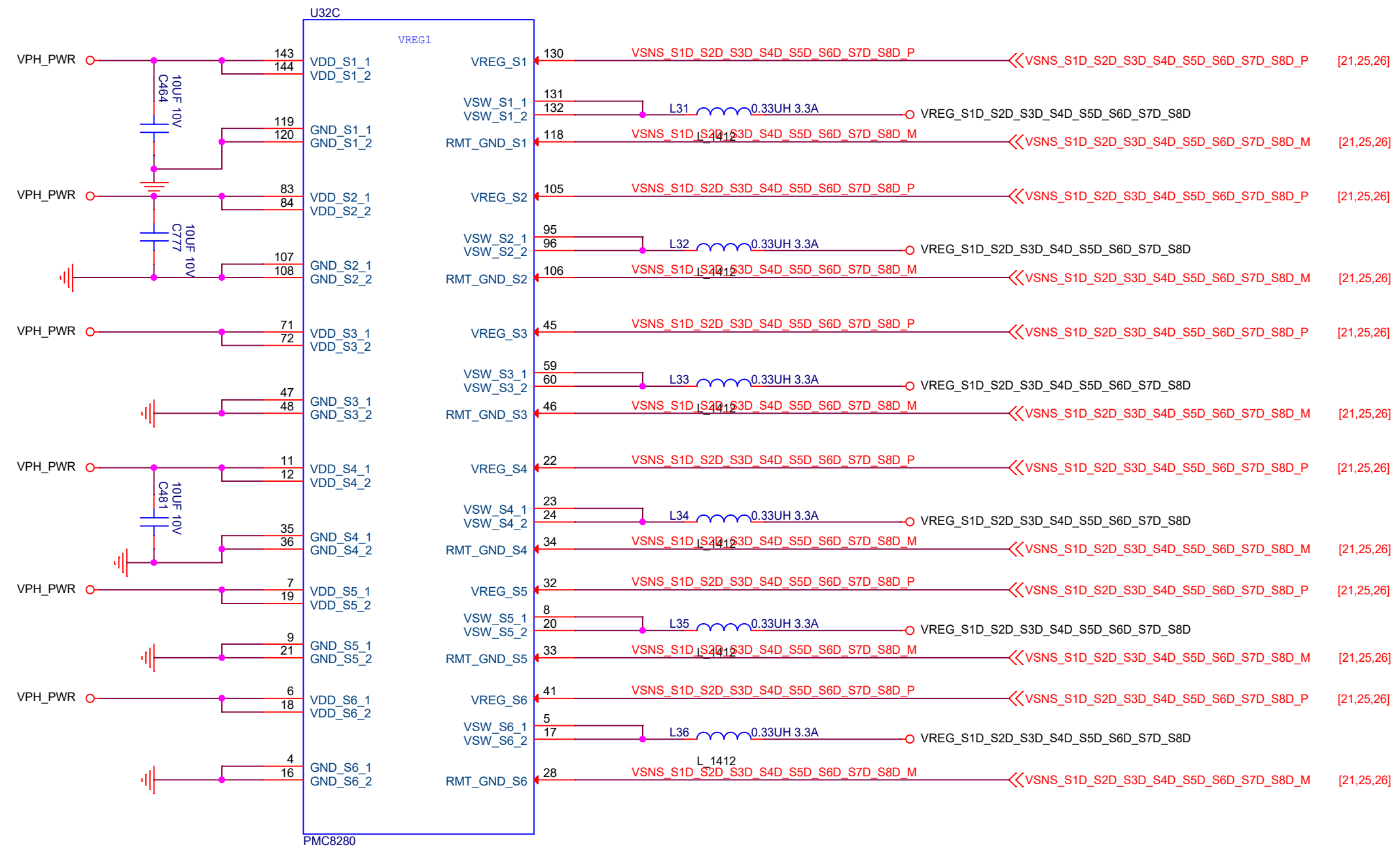
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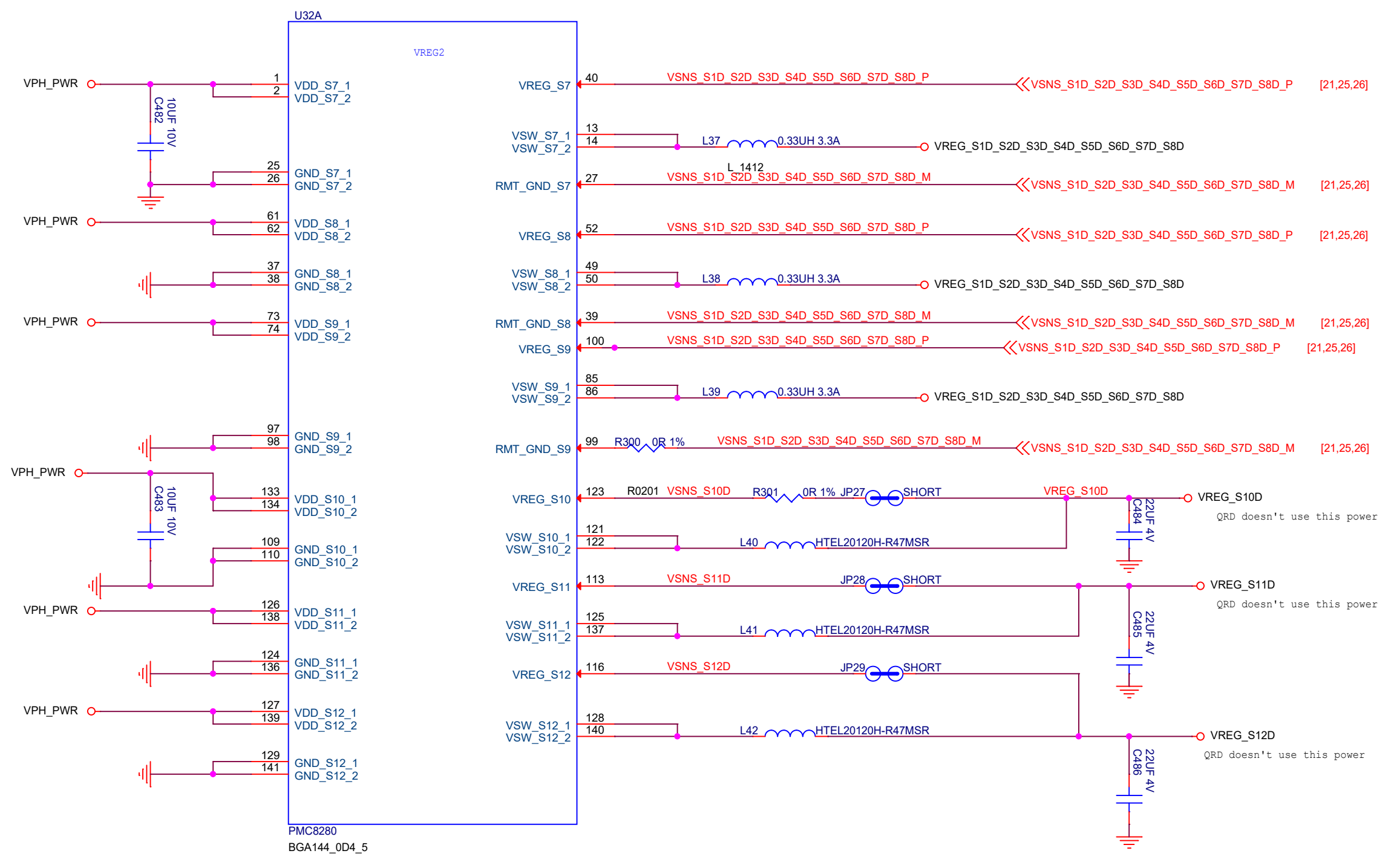
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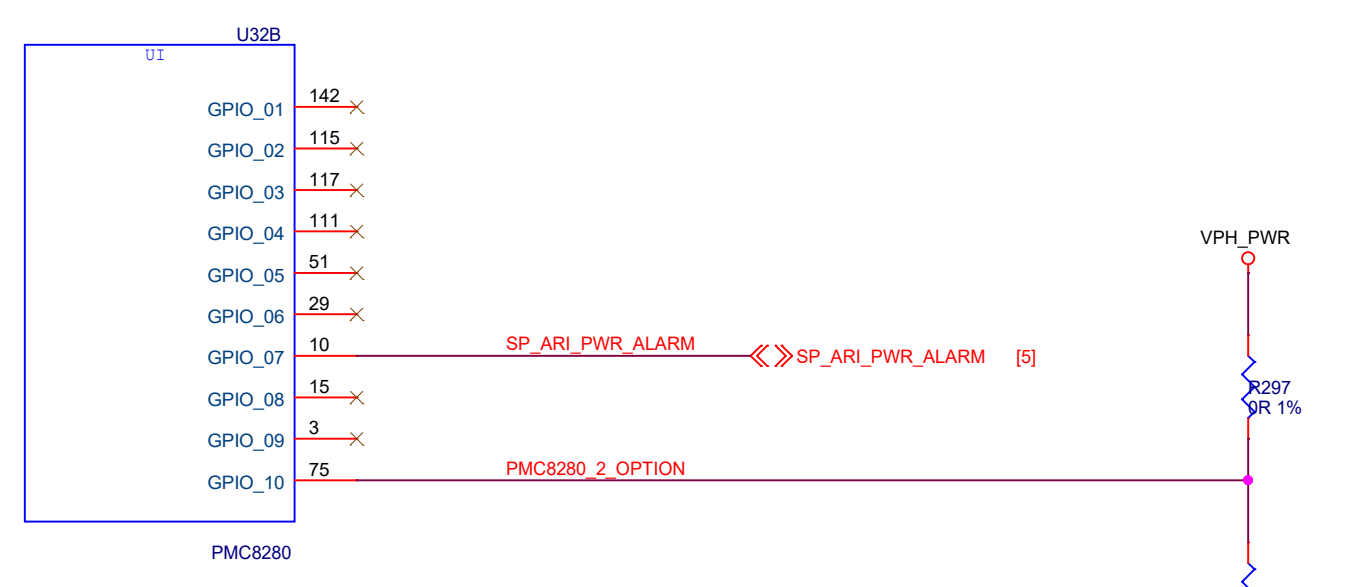
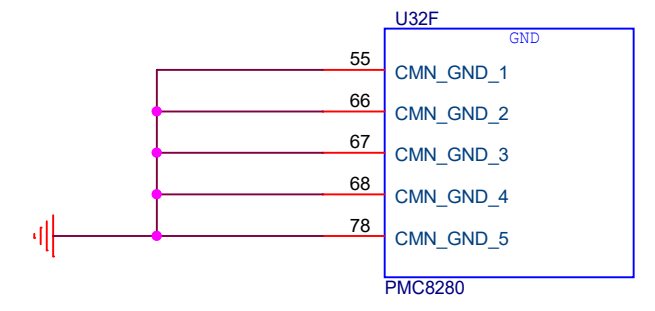
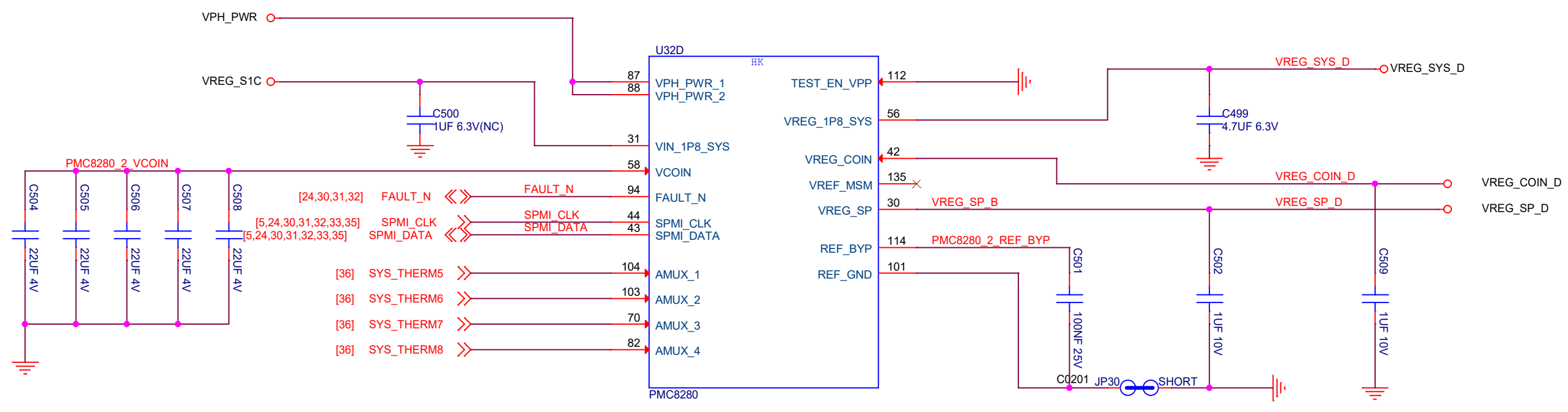
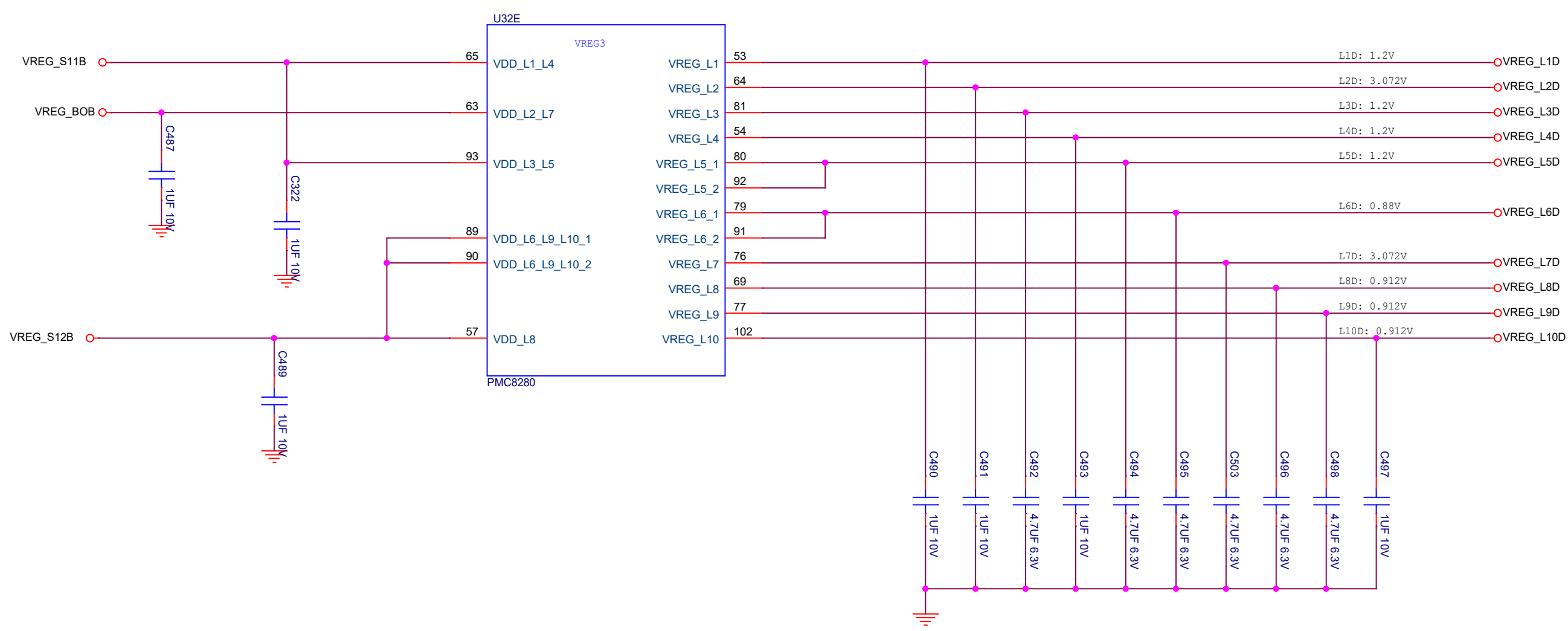
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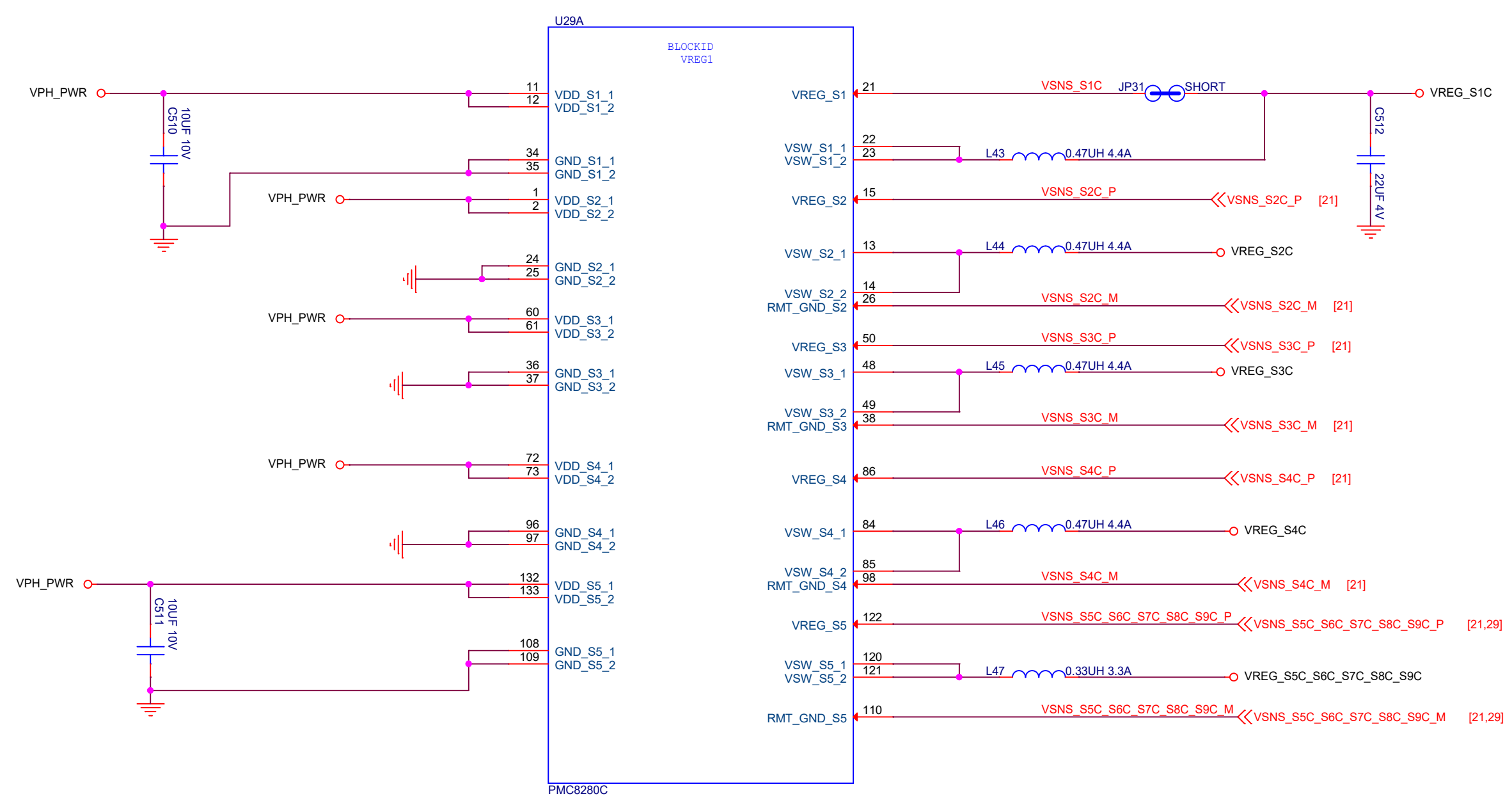


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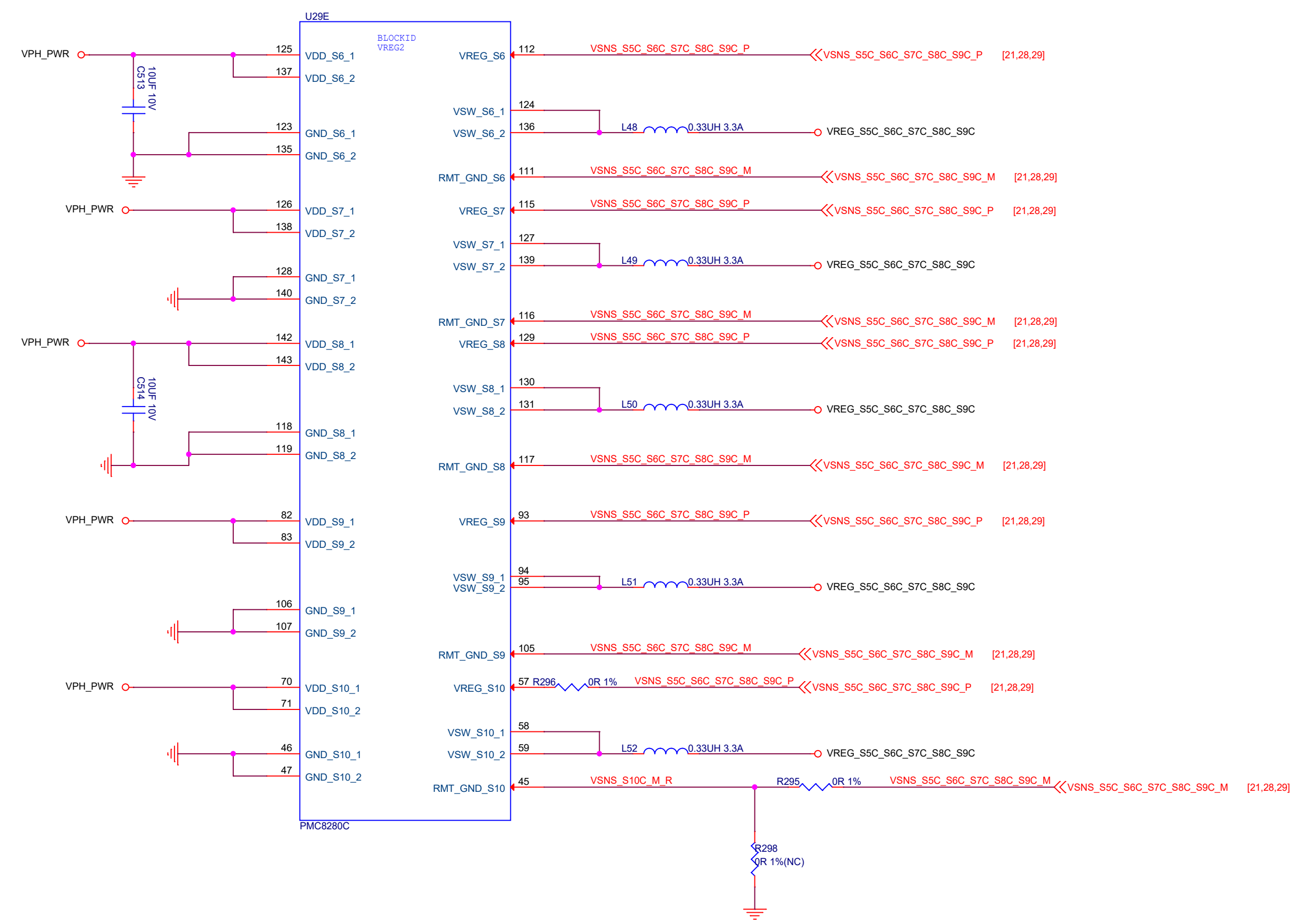


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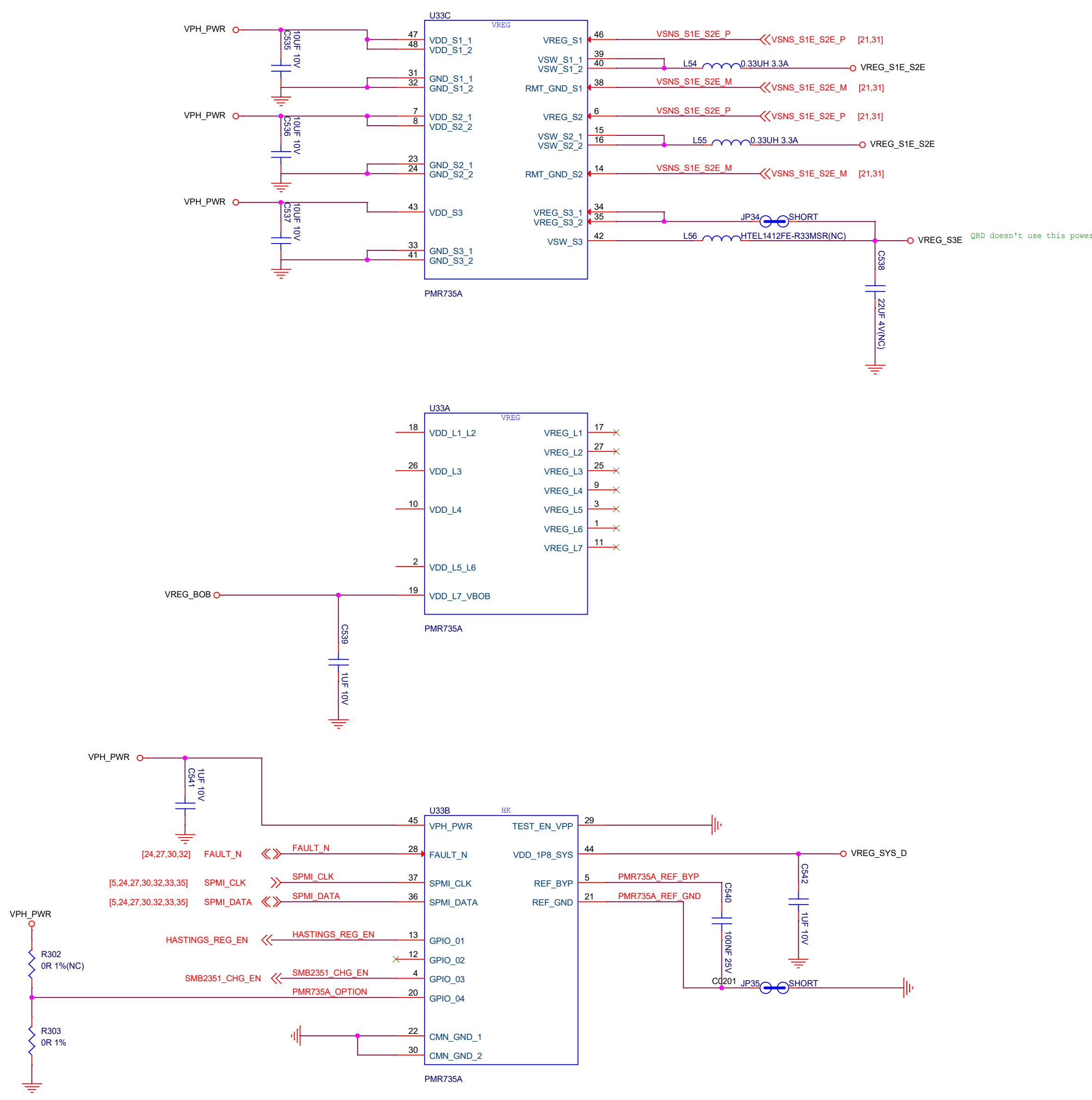
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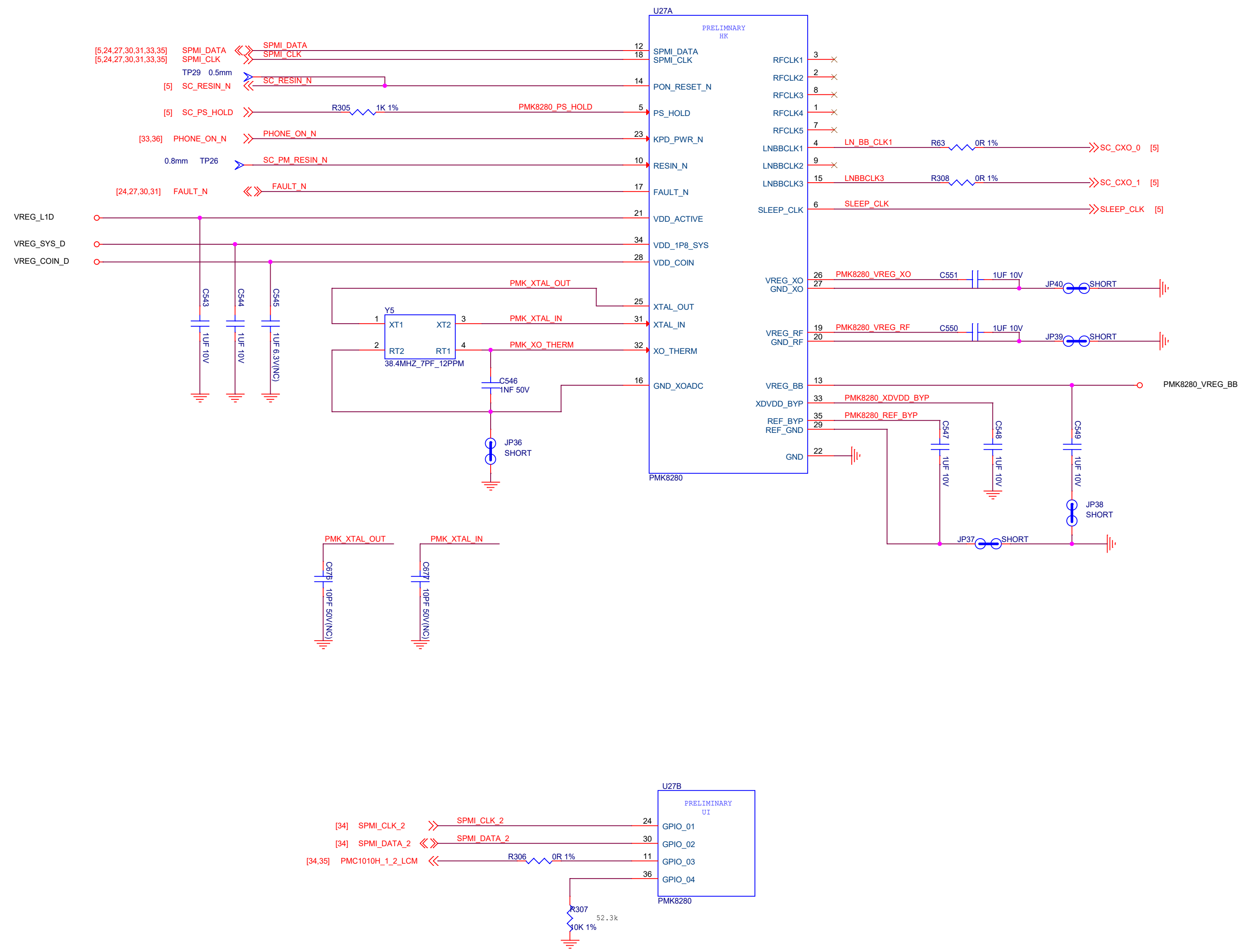
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PMR735A



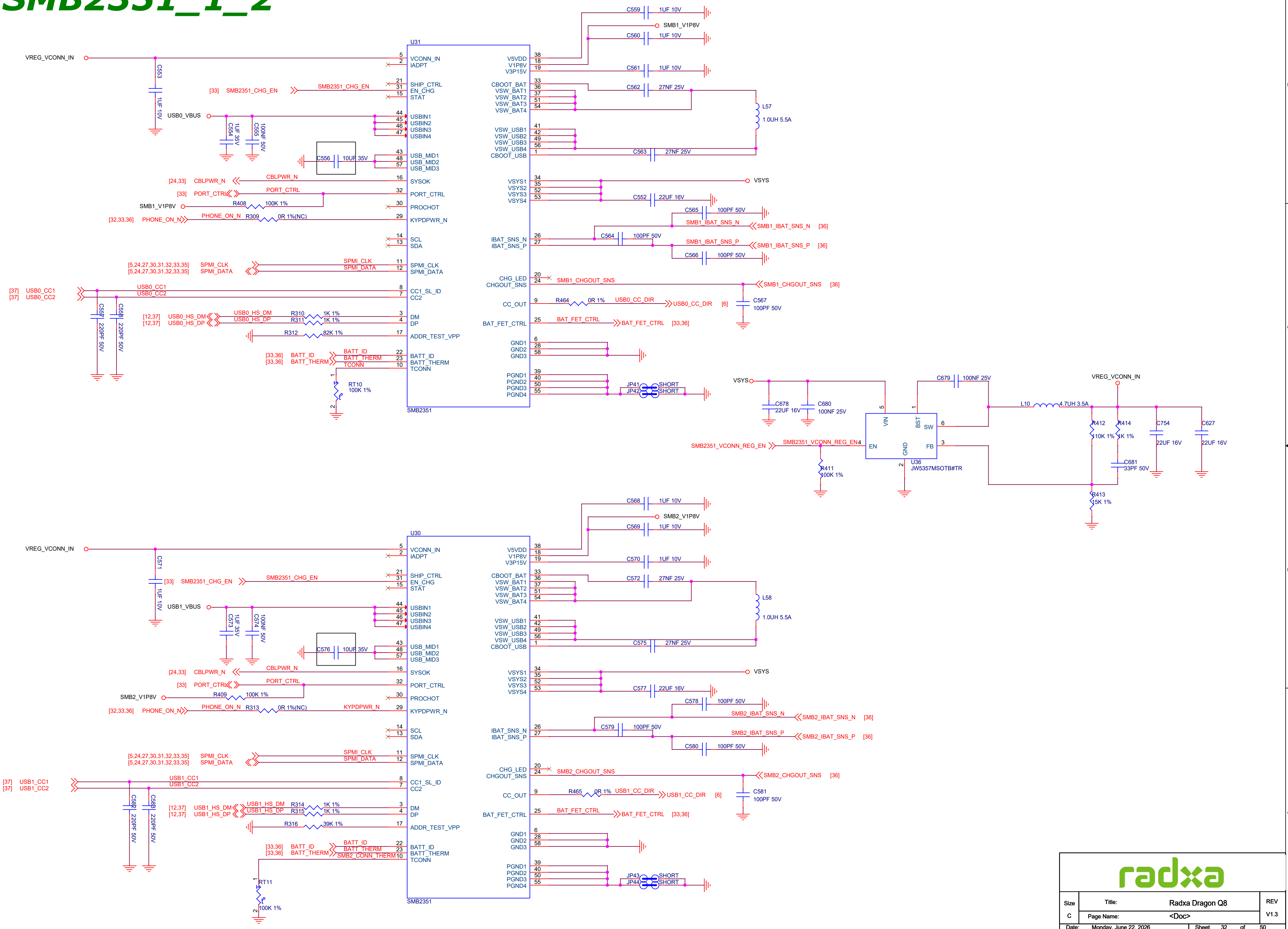
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PMK8280

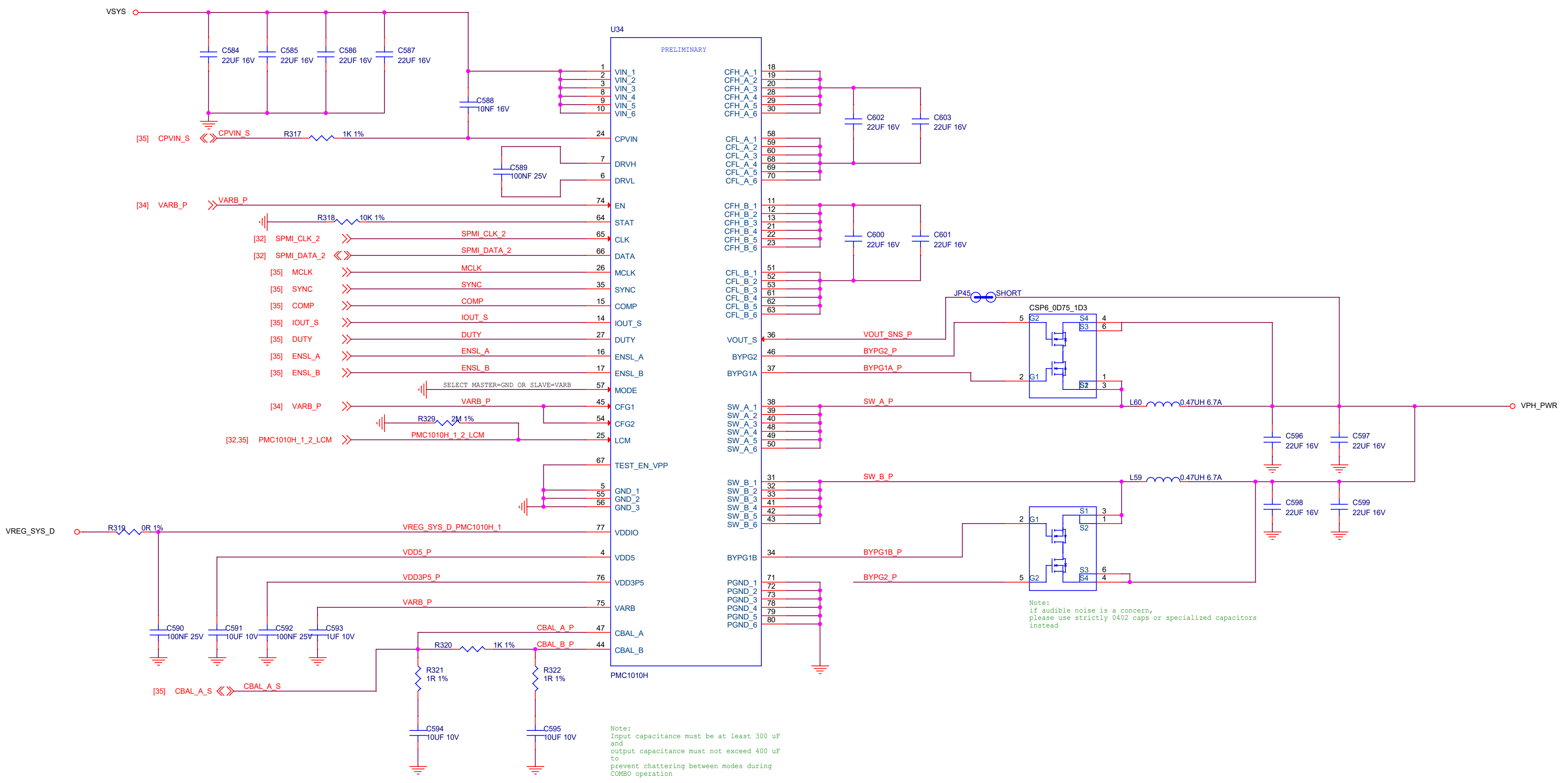


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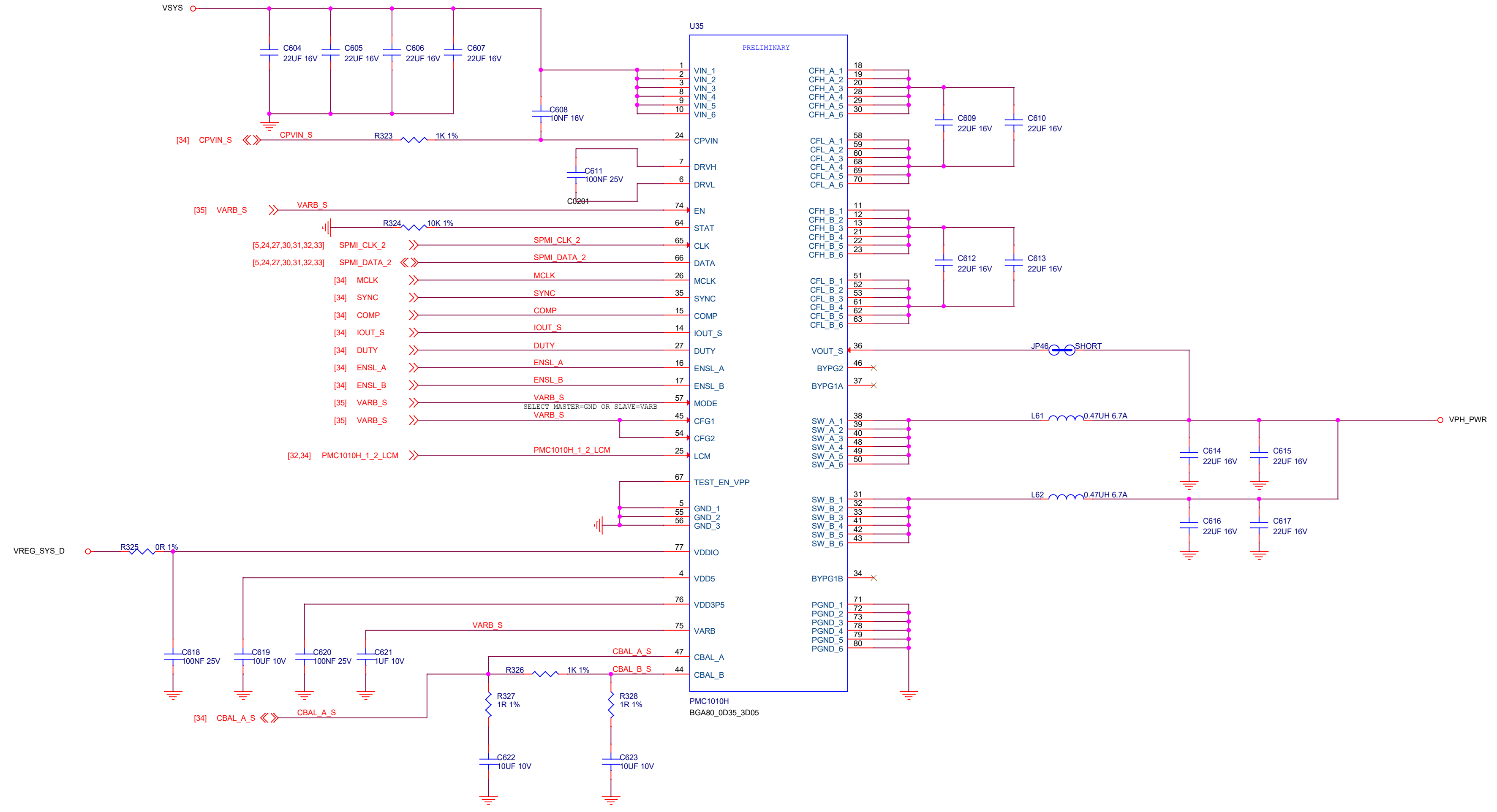
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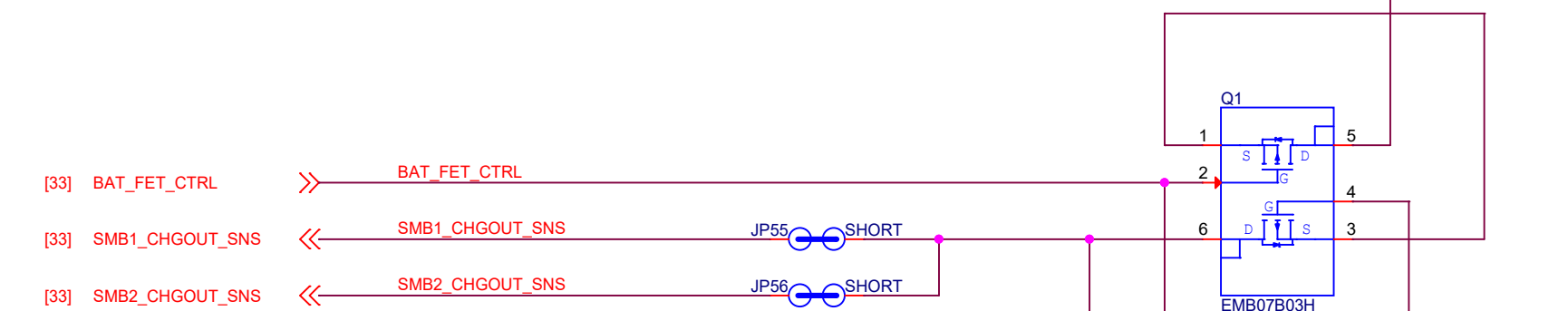
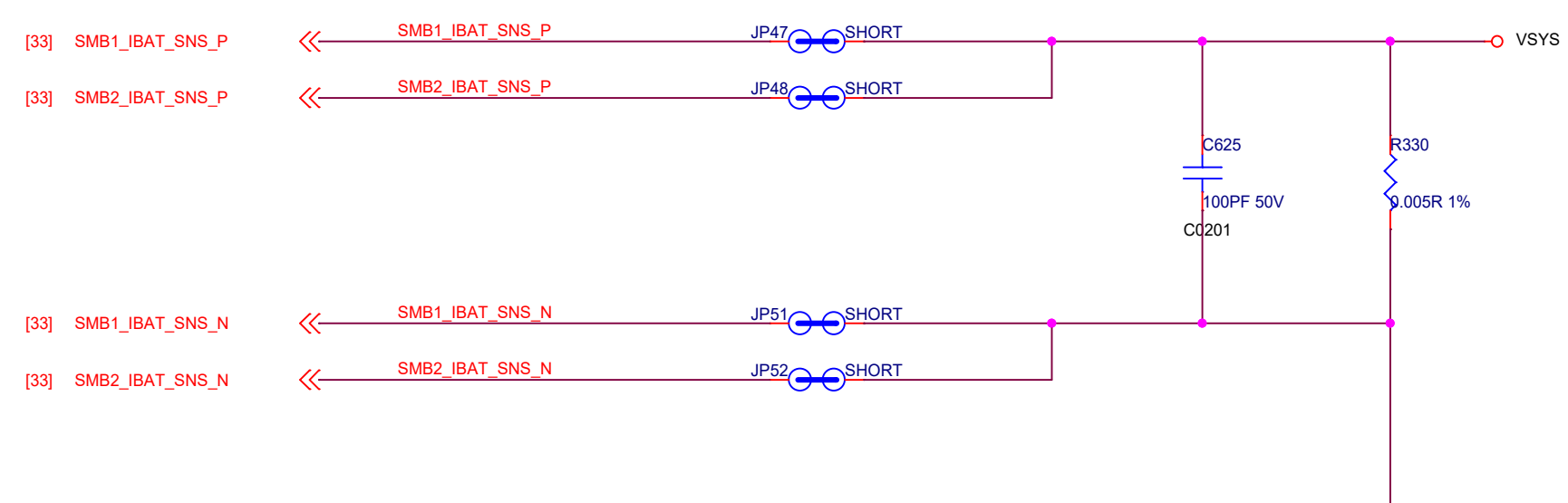


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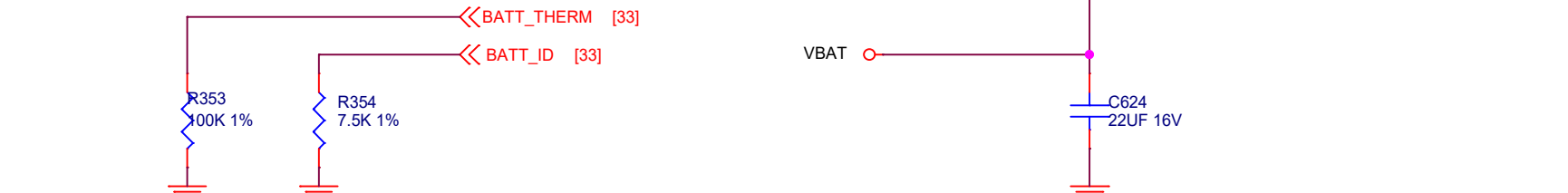


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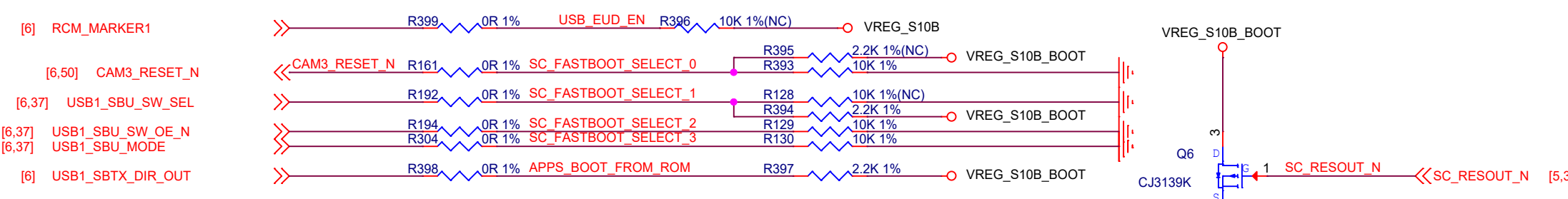
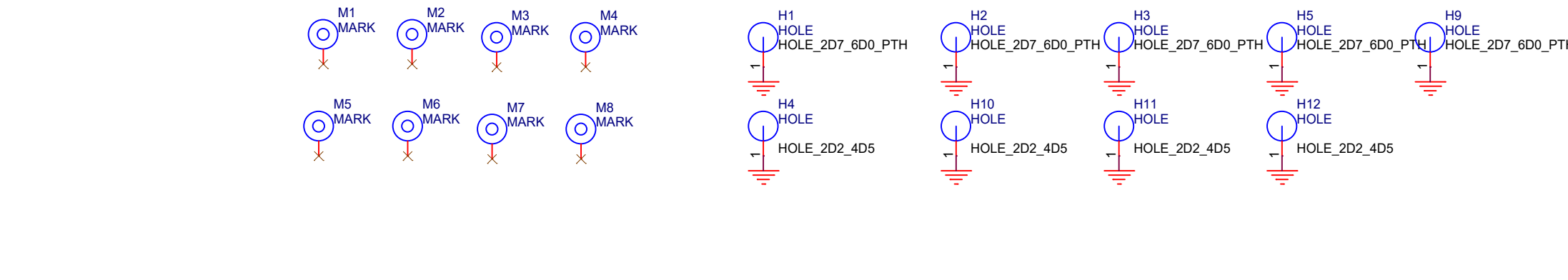
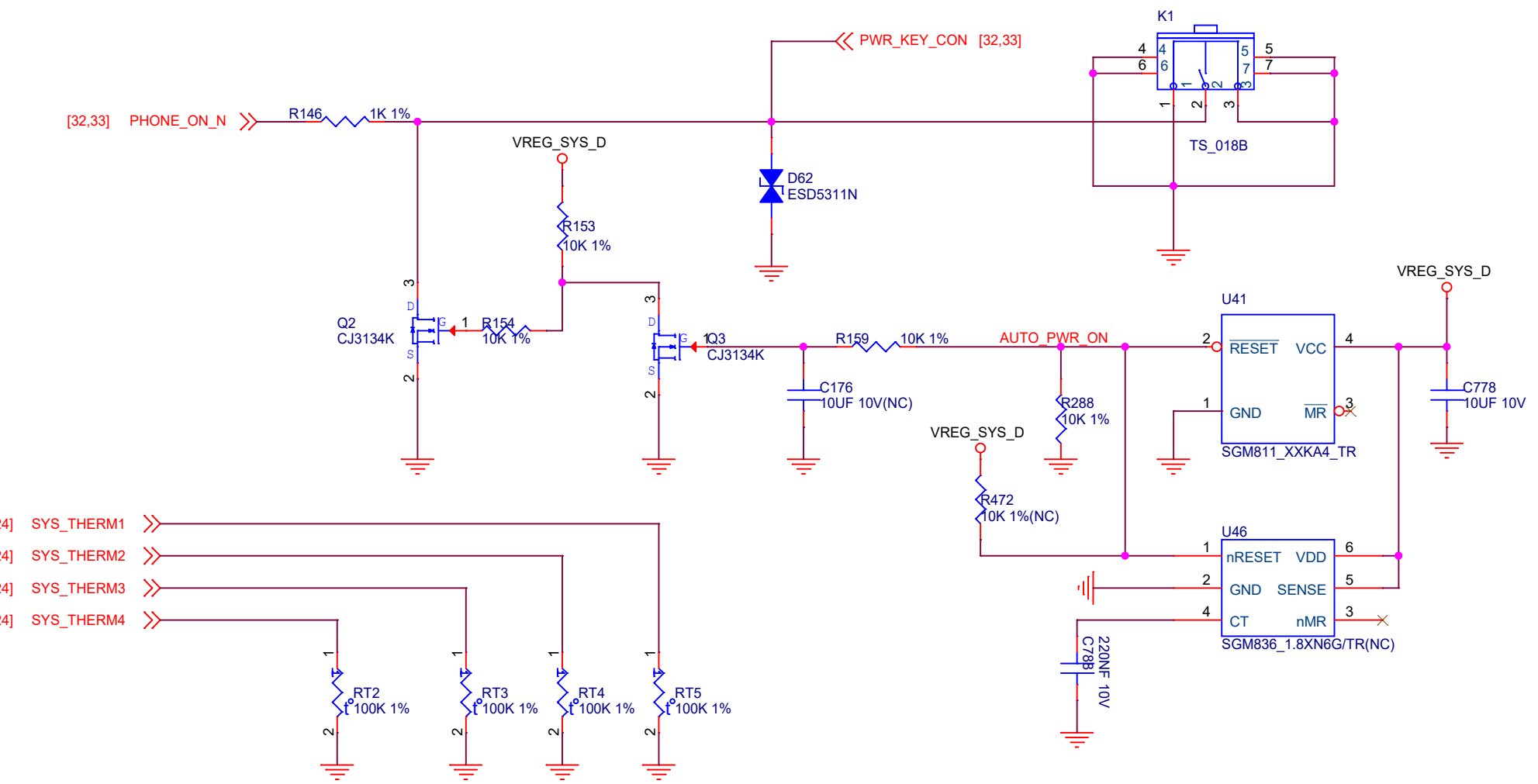
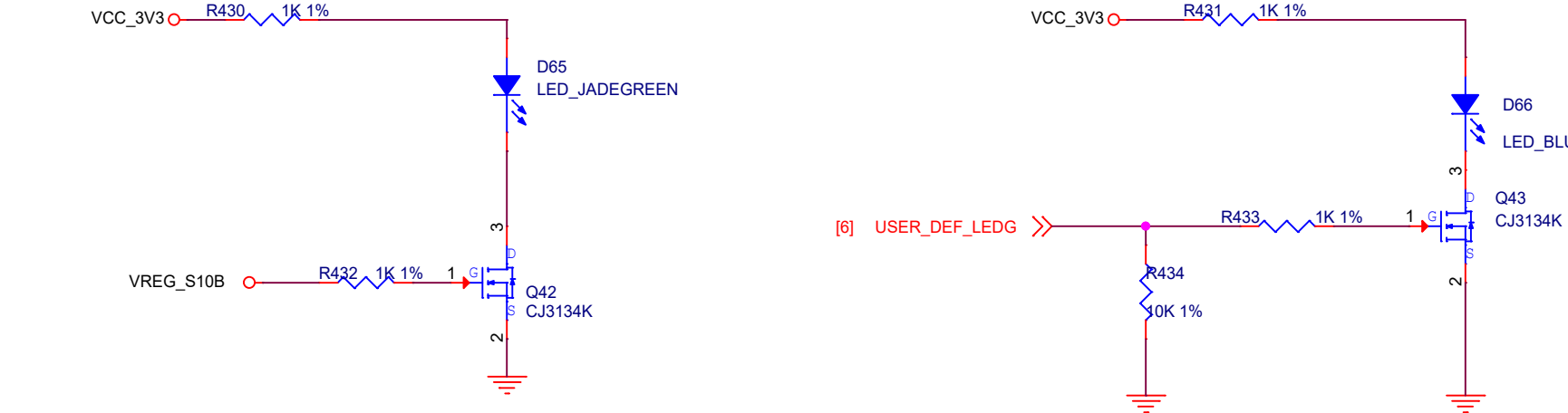
BAT_SEN



BATT THERM:
 --100 kΩ pull-down to GND
 (to fake good battery temperature)

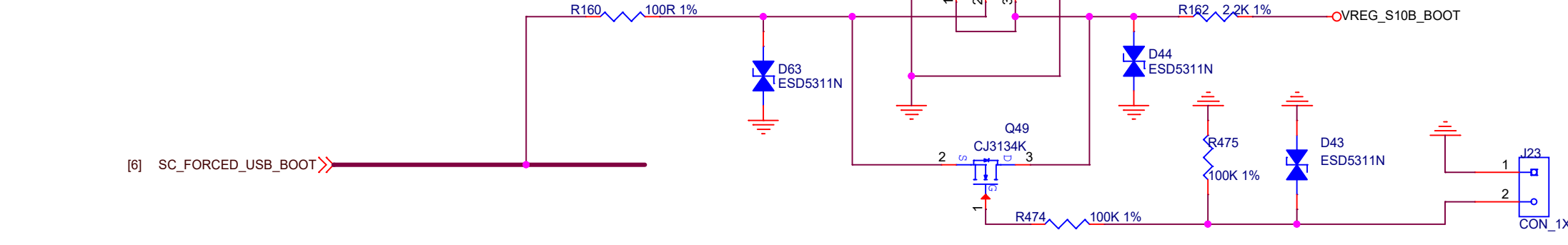


BATT ID:
 When "Fake Battery" detection is enabled, a nominal resistance of 7.5 k to PGND applied to the BAT_ID pin indicates that an external power supply is present instead of a Li+ cell. In this case, the user can prevent charging an external power supply. This feature is useful in a debug environment when using a real battery is difficult.

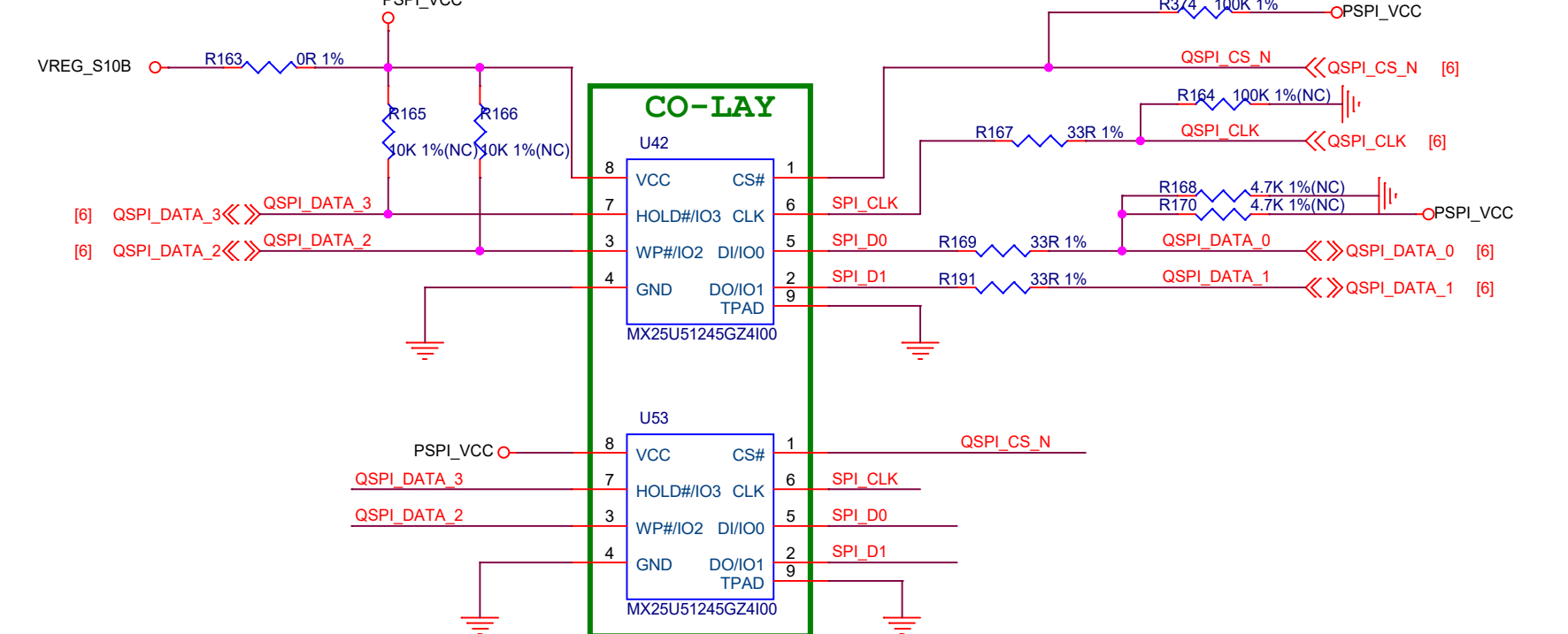


The BOOT_CONFIG GPIOs [GPIOs 50, 48, 47, 15] are used to select the boot device, when fuses are not blown.
 BOOT_CONFIG[4:1]
 0b0000 - Default : UFS0 -> eDL(USB0)
 0b0001 - SDC2 -> eDL(USB0)
 0b0010 - QSPI -> eDL(USB0)
 0b0011 - SPI -> eDL(USB0)
 0b0100 - UFS0 -> eDL(USB0)
 0b0101 - USB only

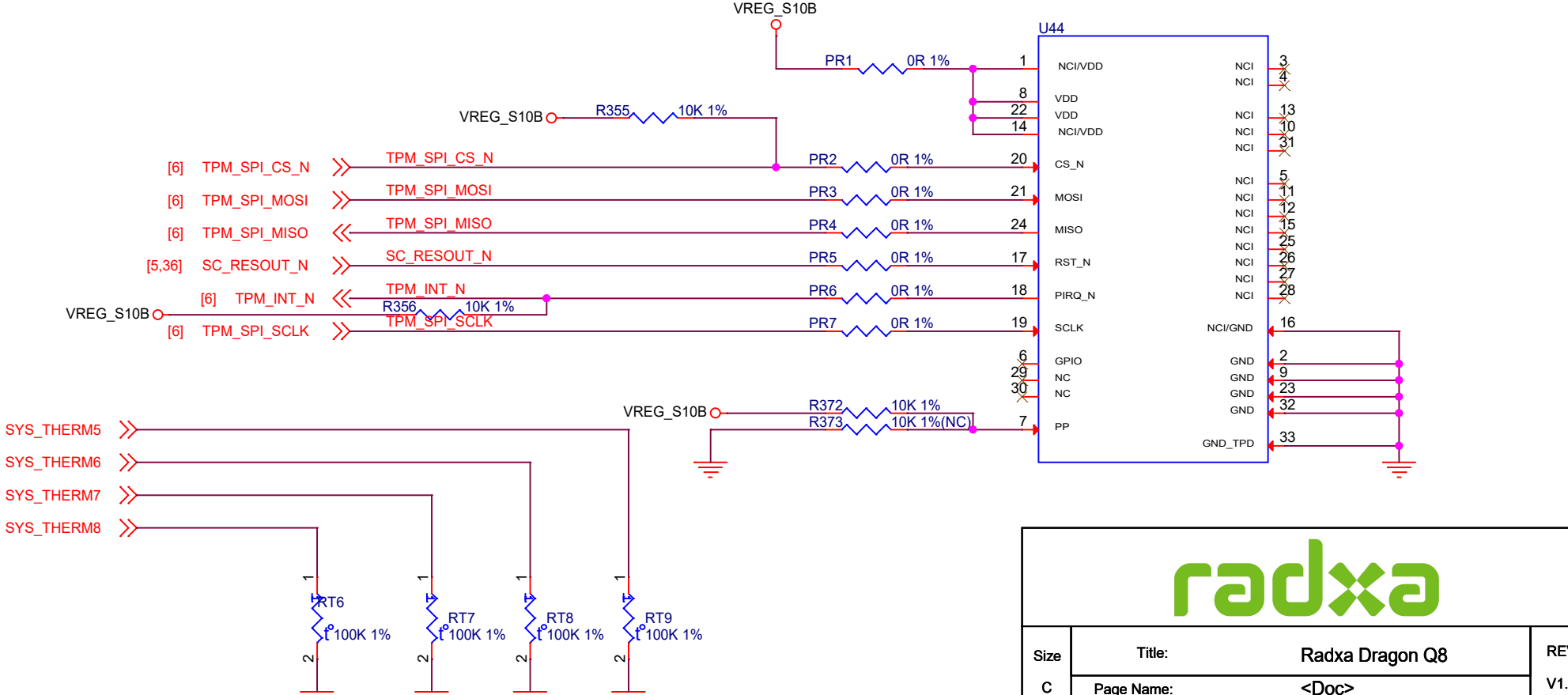
The boot configuration function of this GPIO is only active at the time of boot, before RESOUT_N is deasserted.

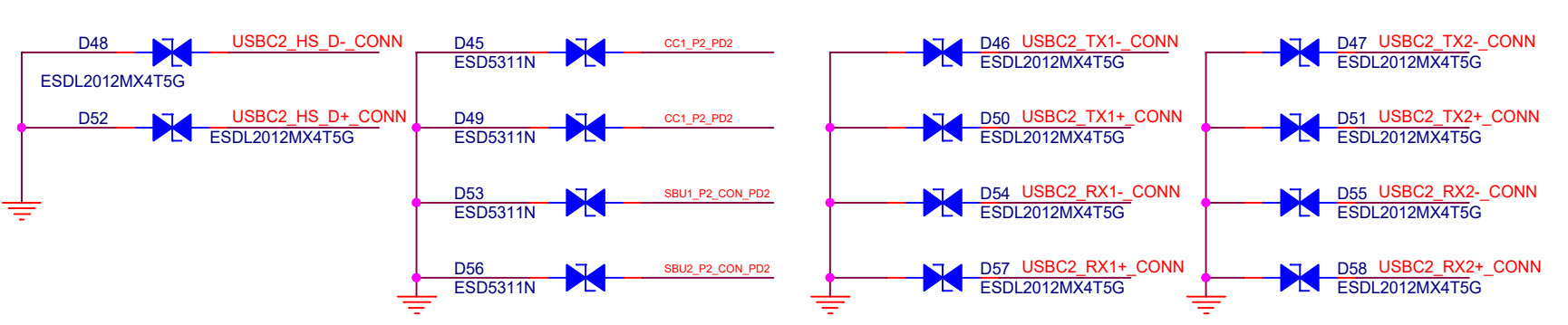
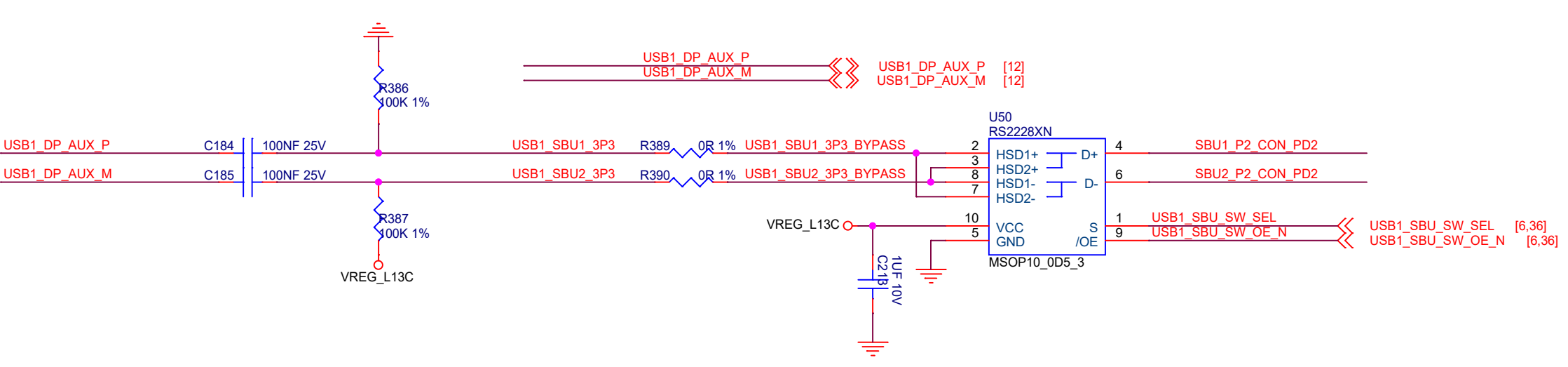
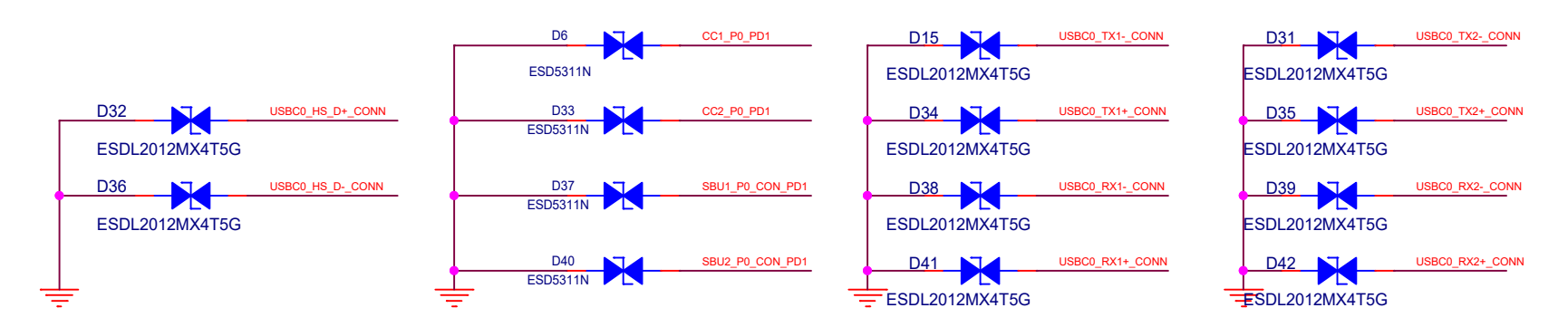
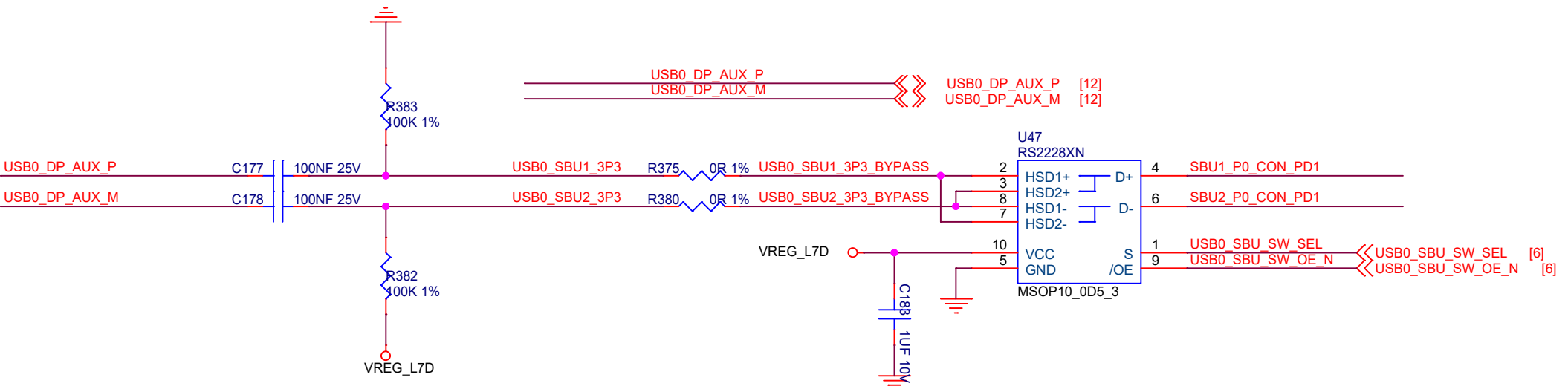
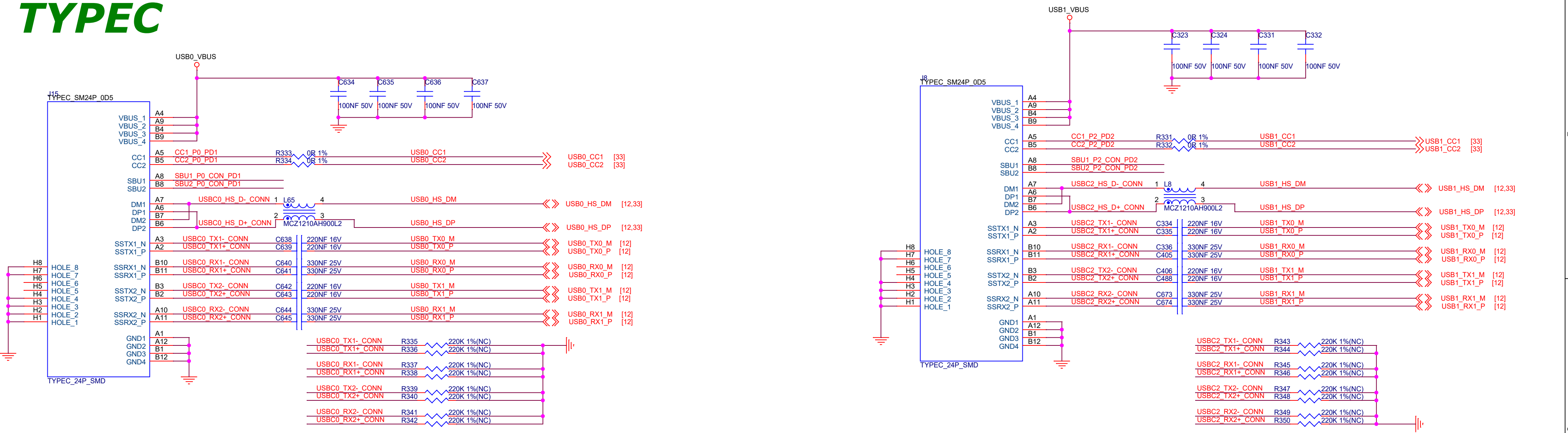


QSPI FLASH FOR BOOT

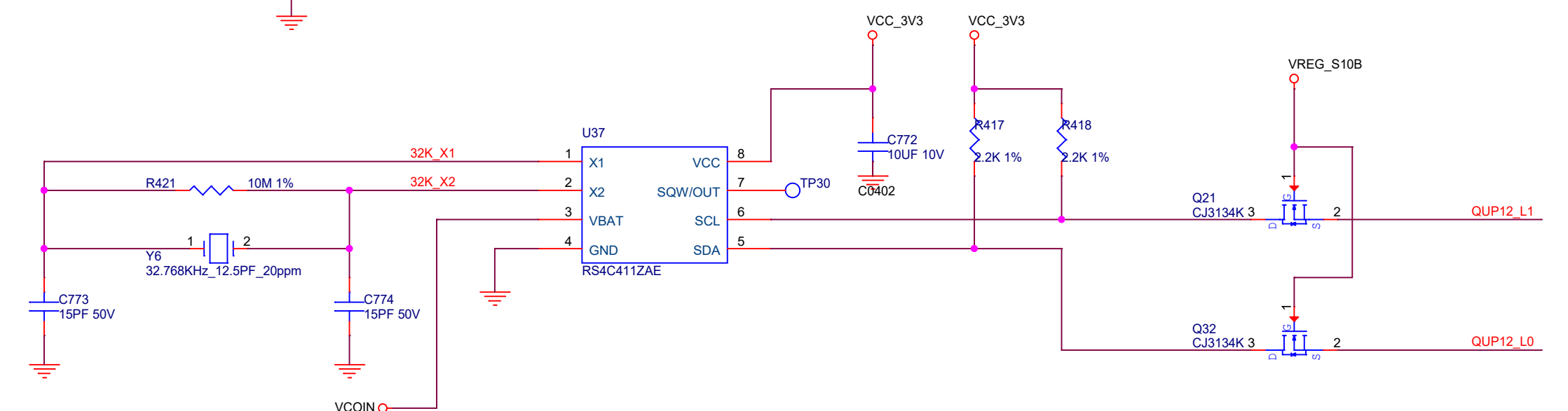
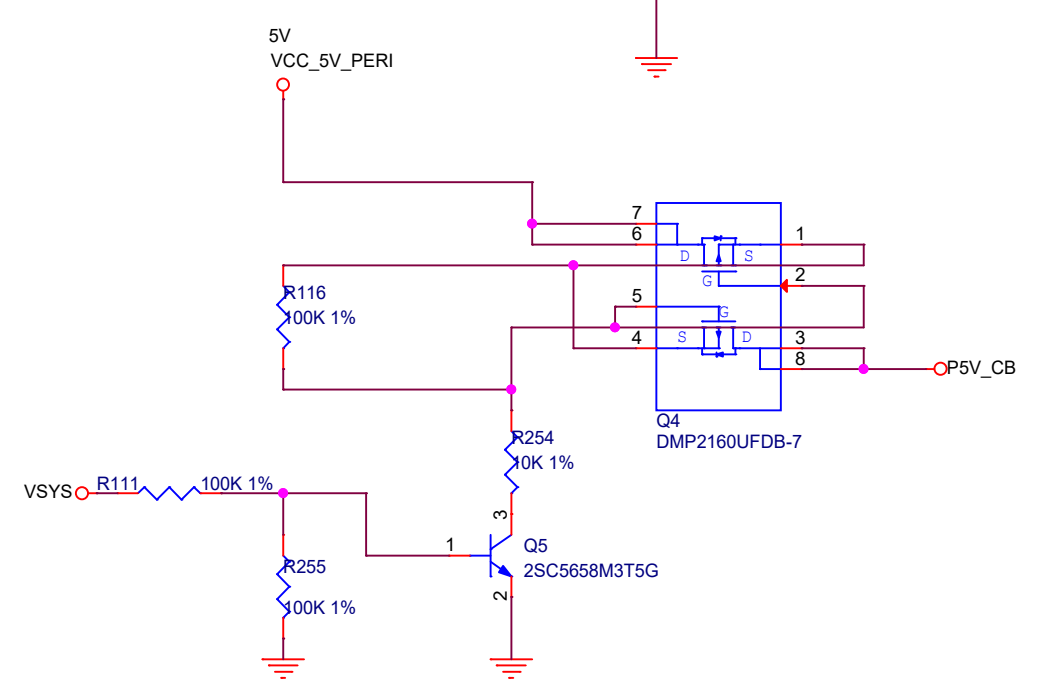
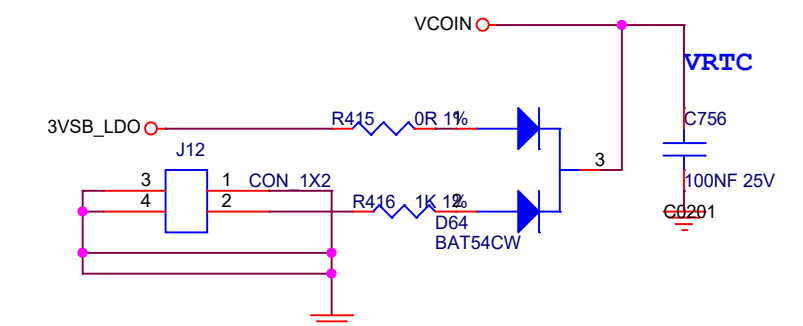
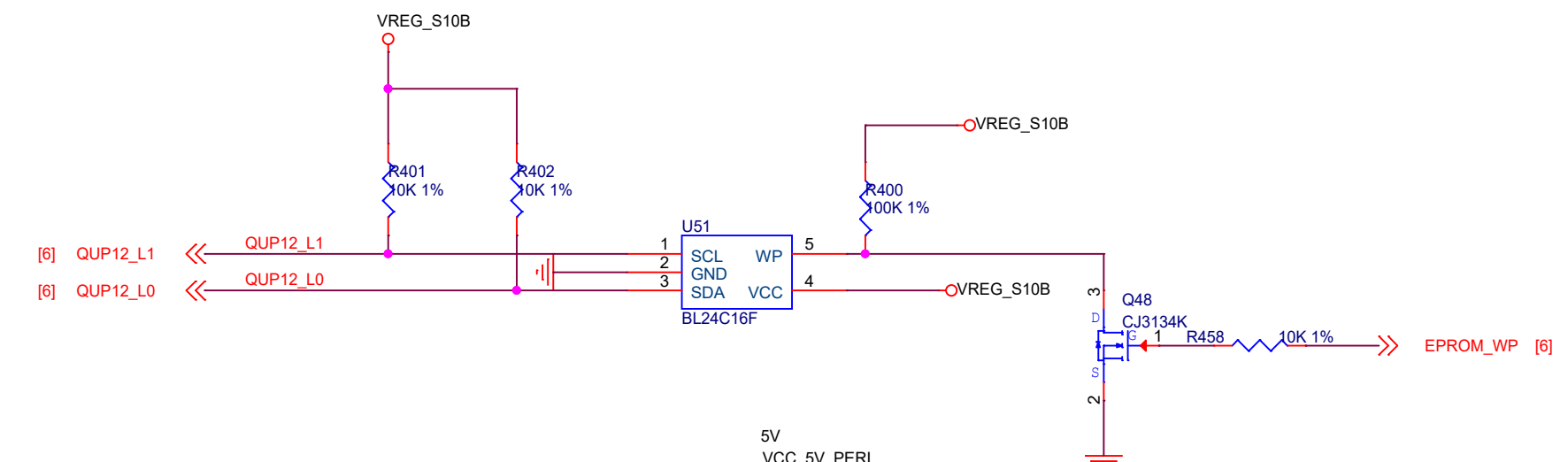
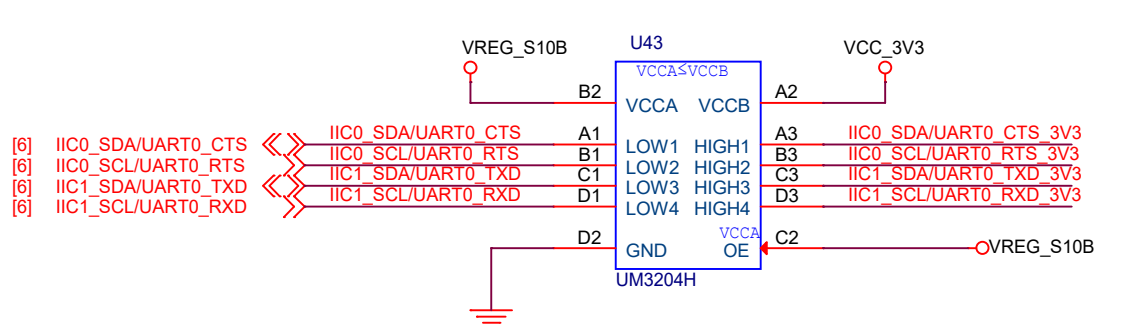
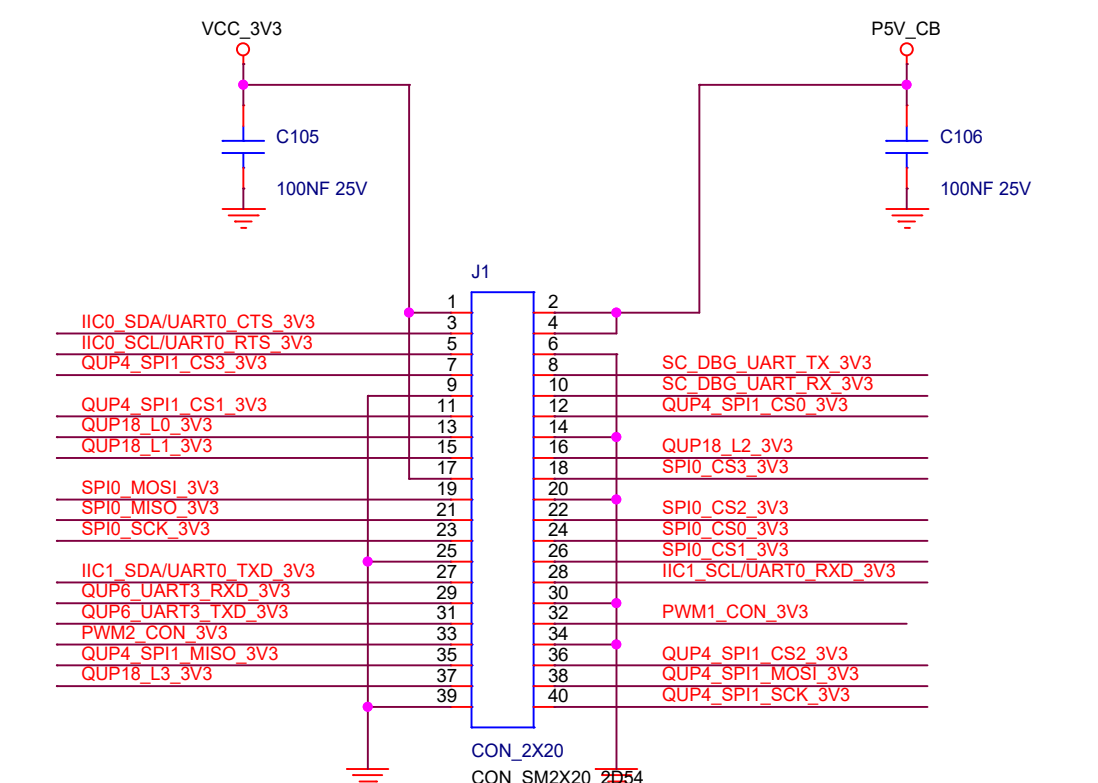
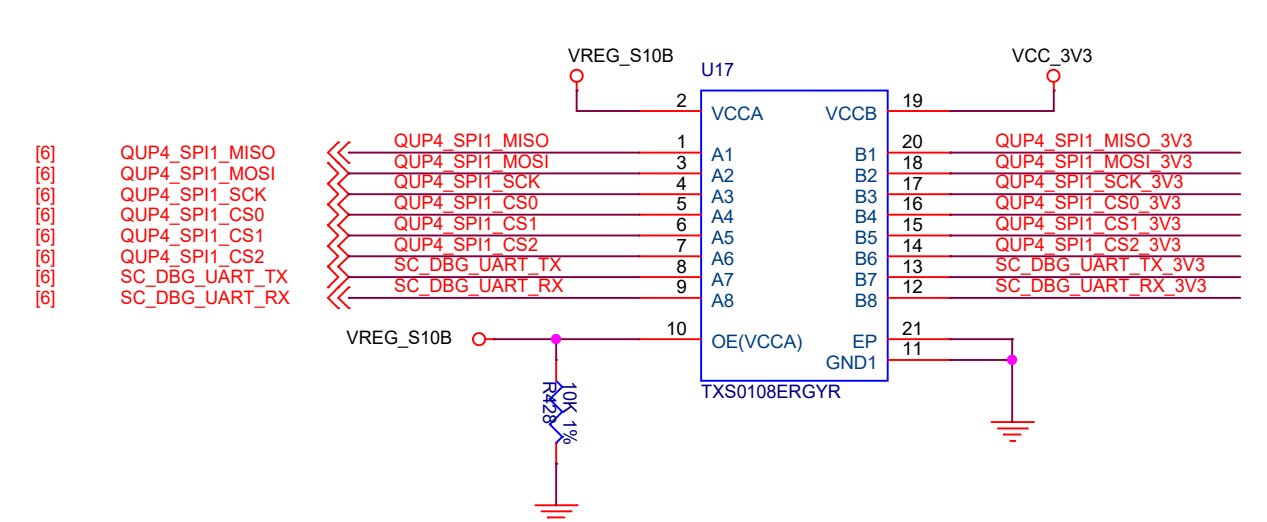
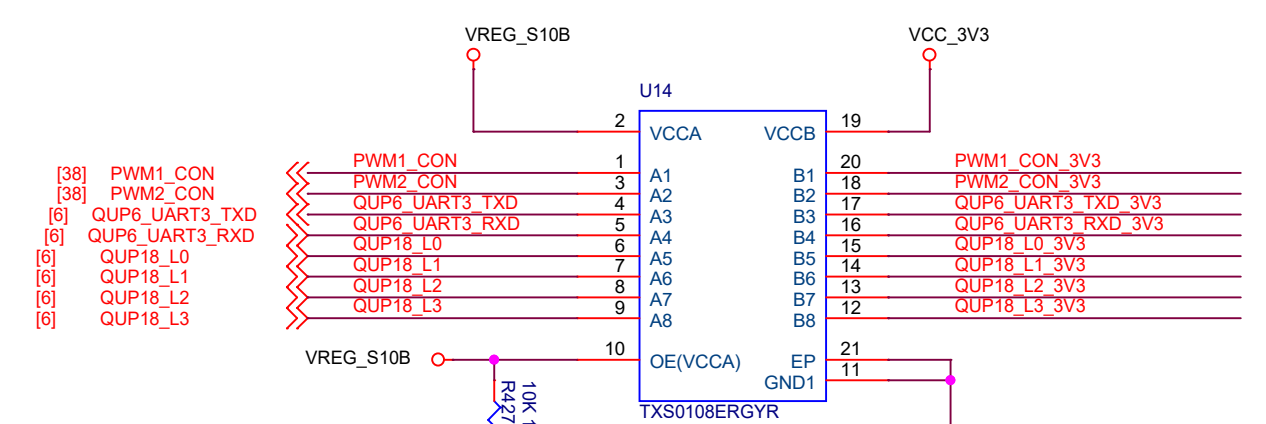
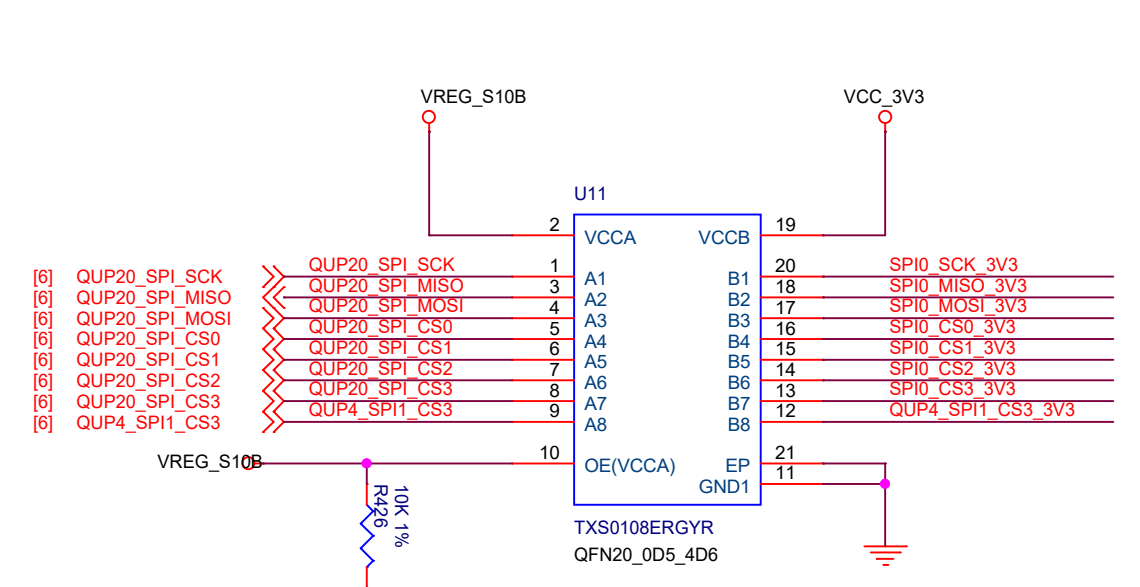


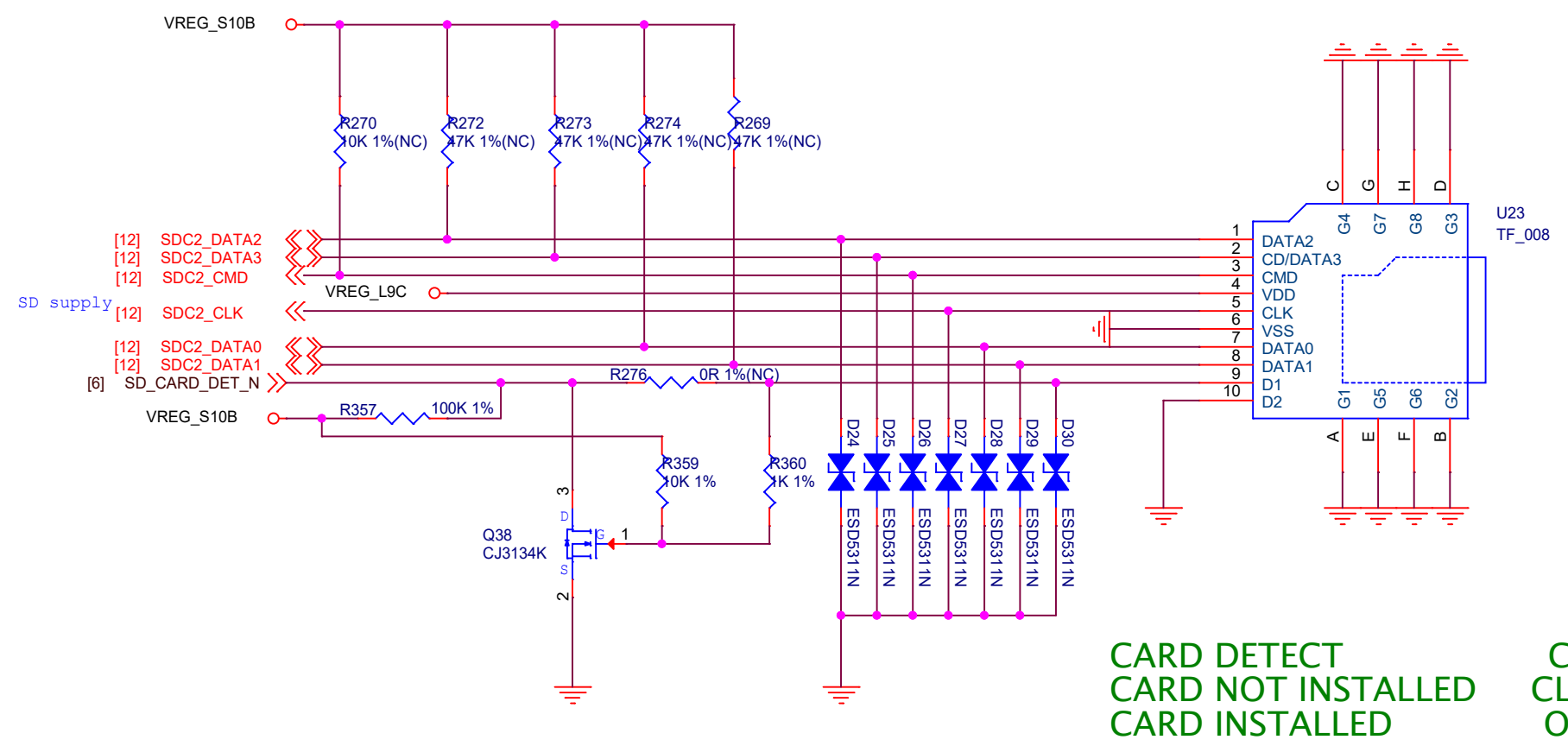
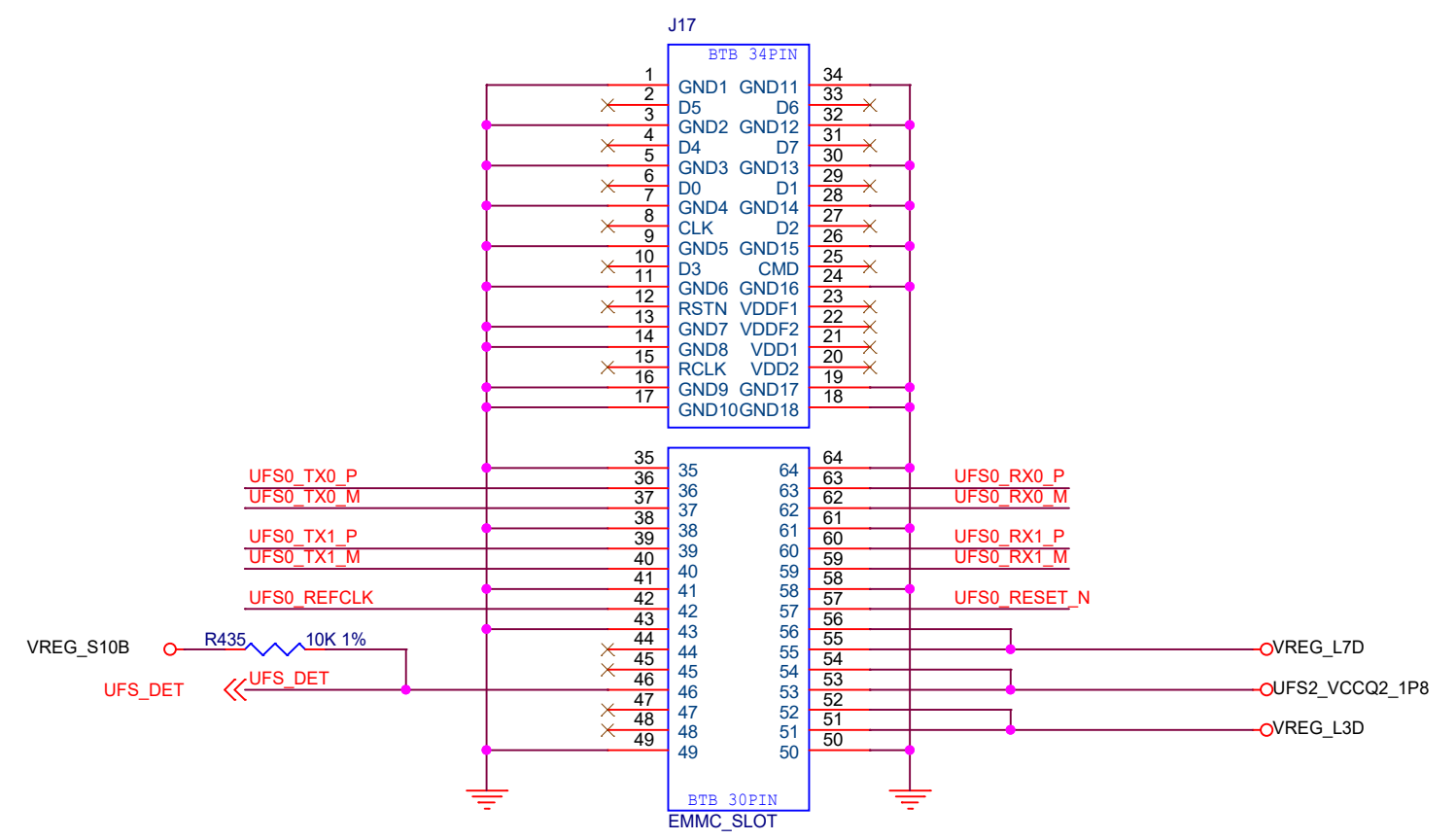
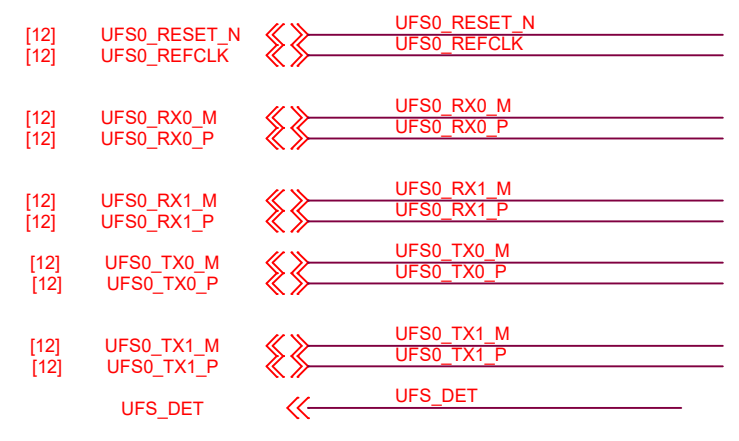
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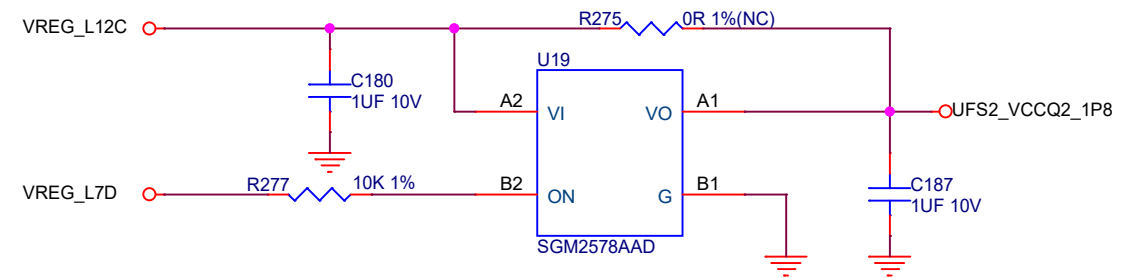
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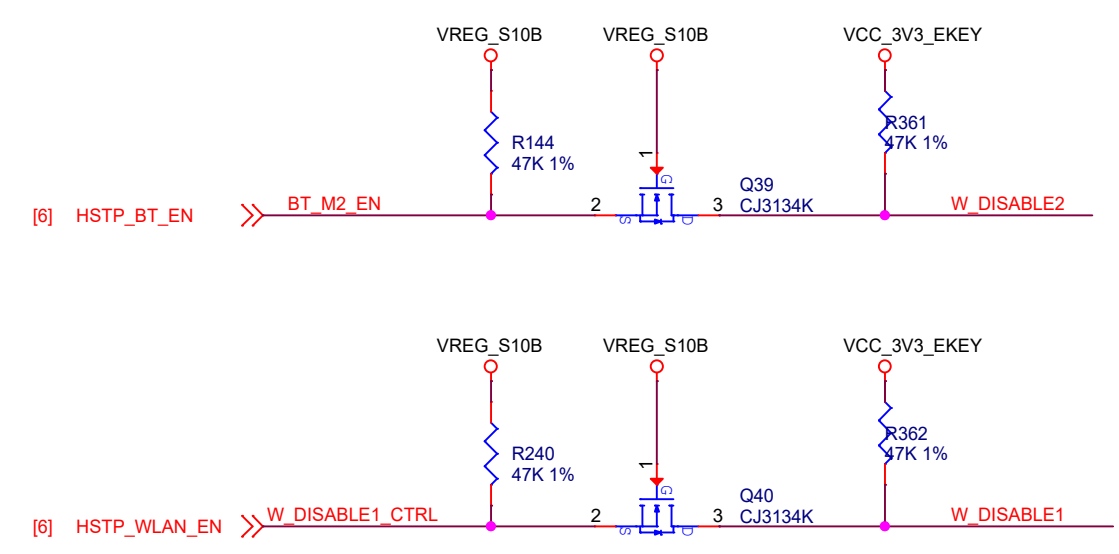
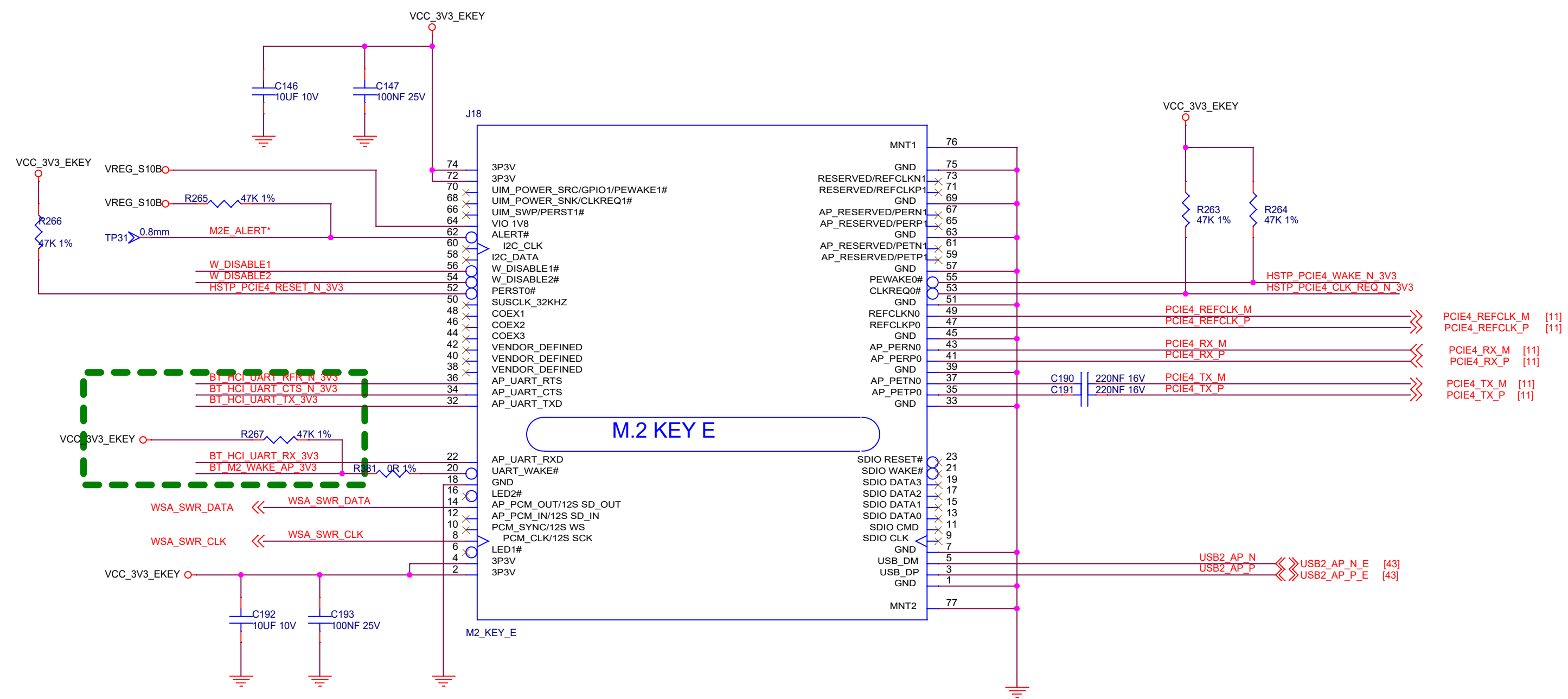


CARD DETECT
CARD NOT INSTALLED
CARD INSTALLED

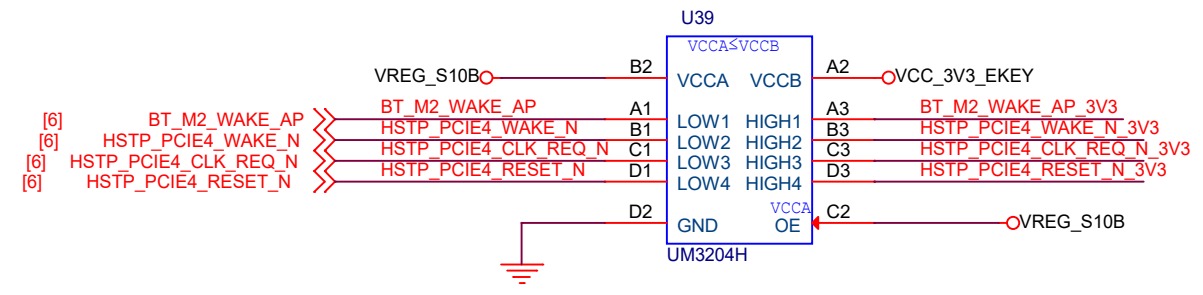
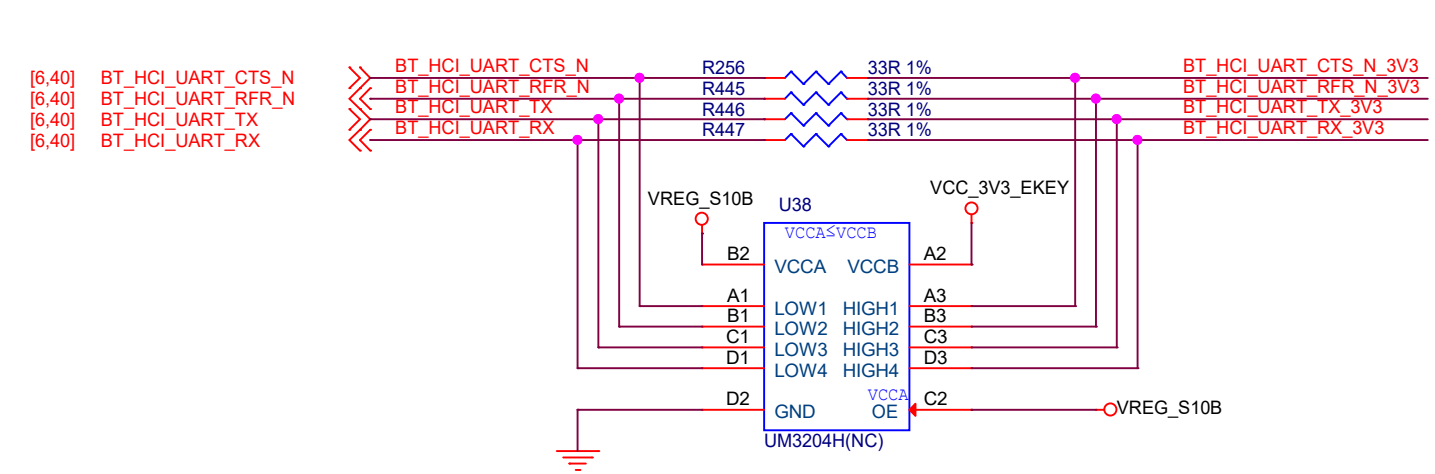
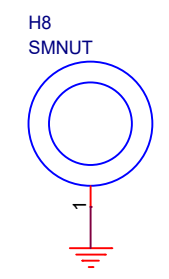
CASE
CLOSED (GND)
OPEN (FLOATING)



M.2 EKEY

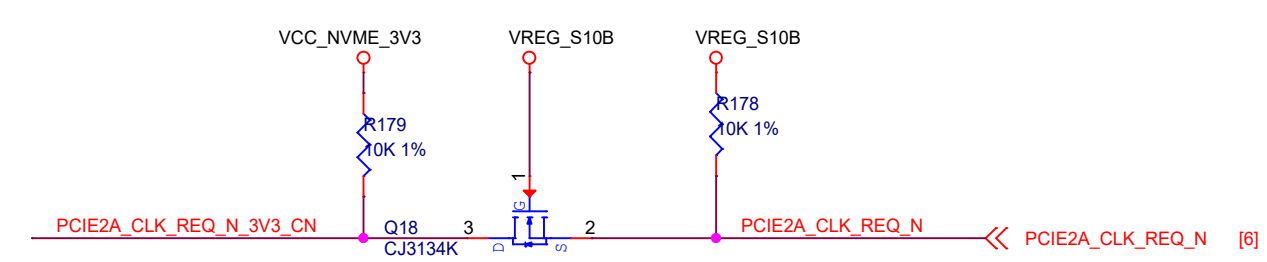
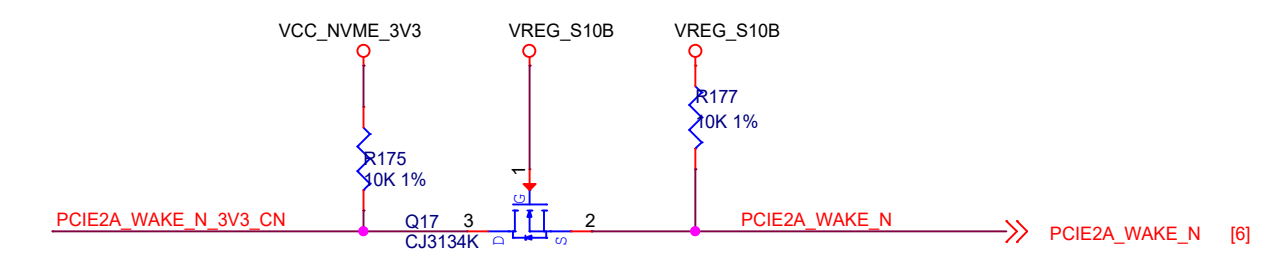
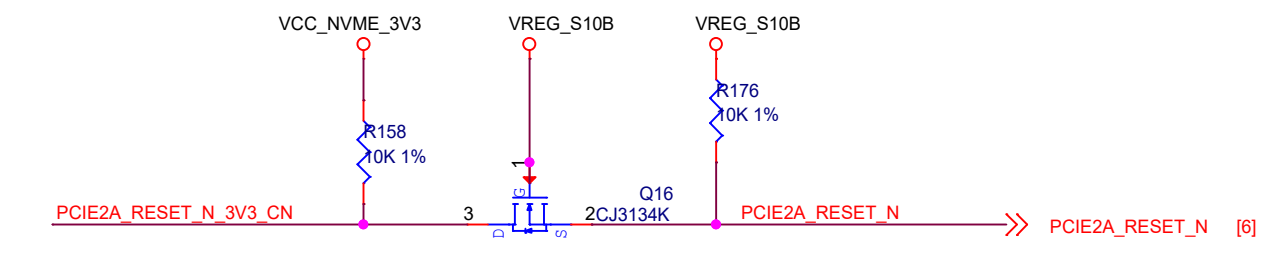
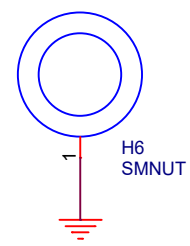
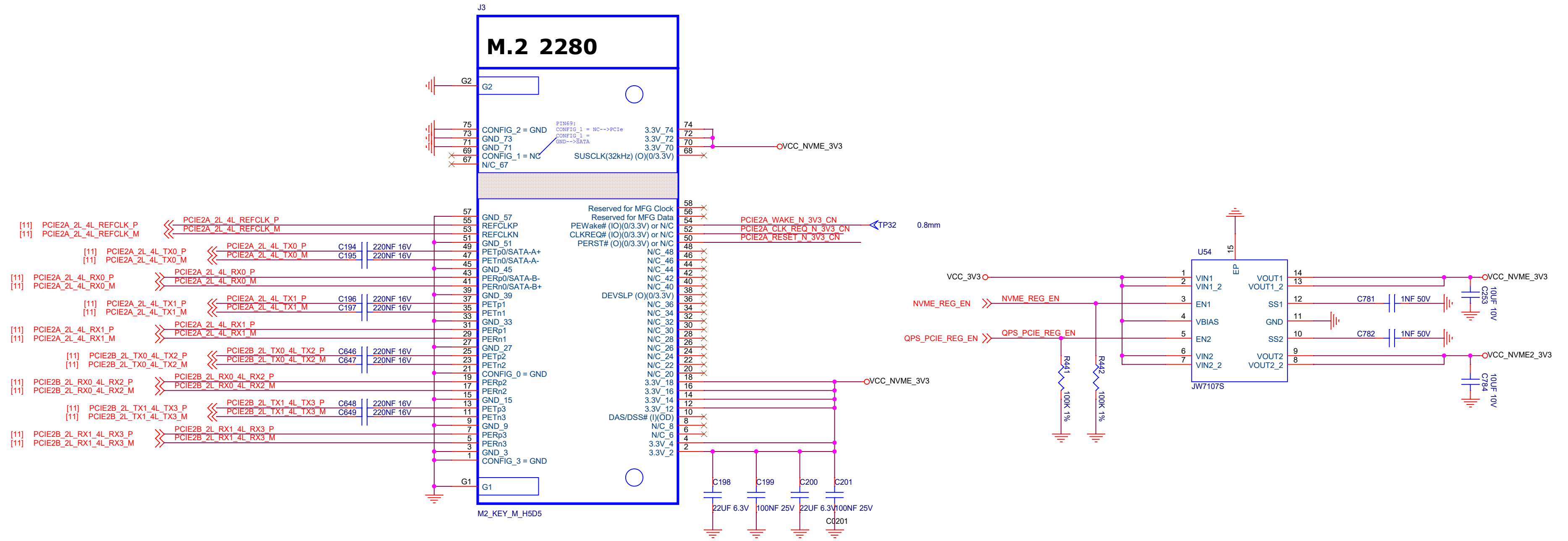


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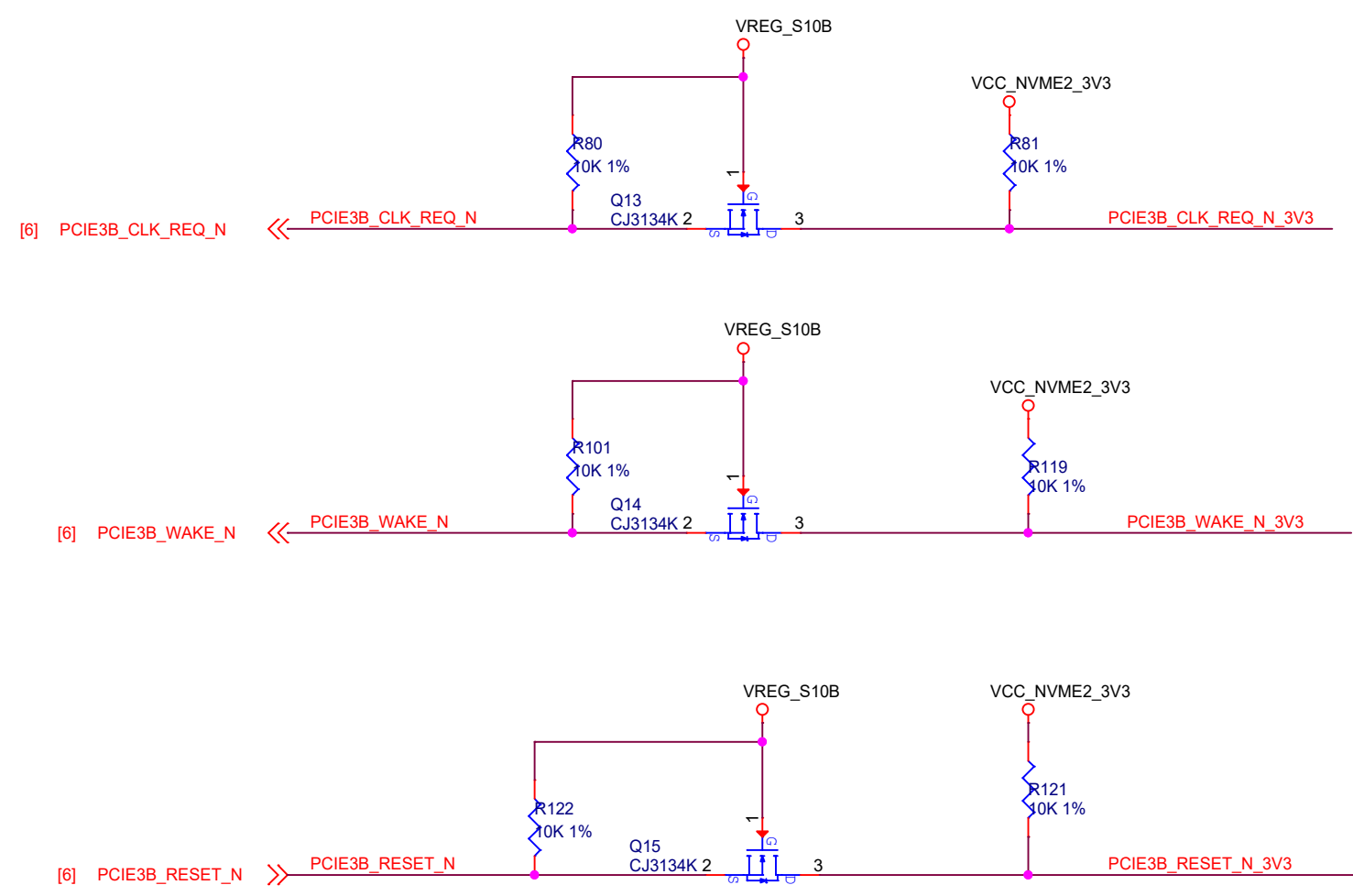
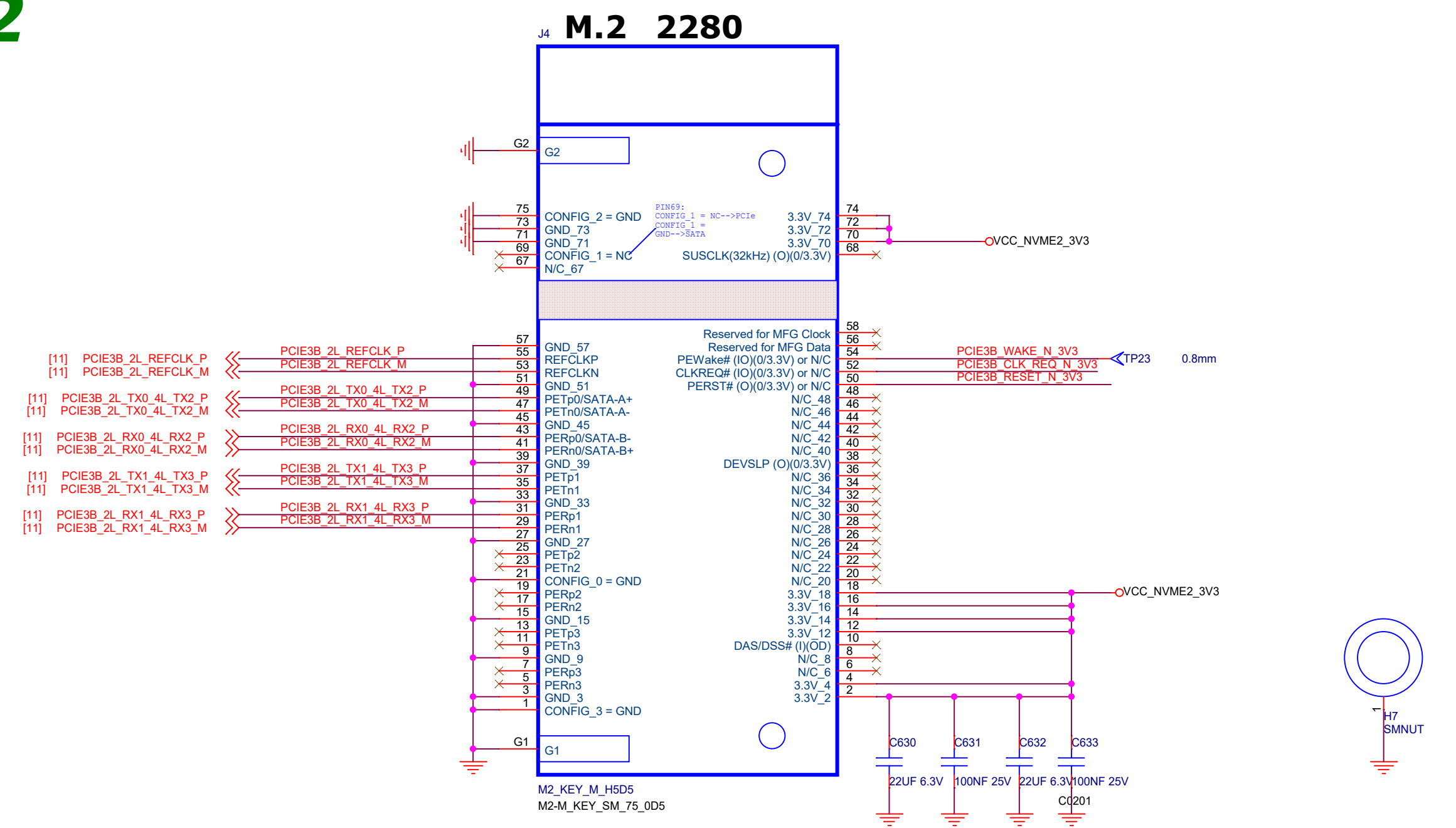


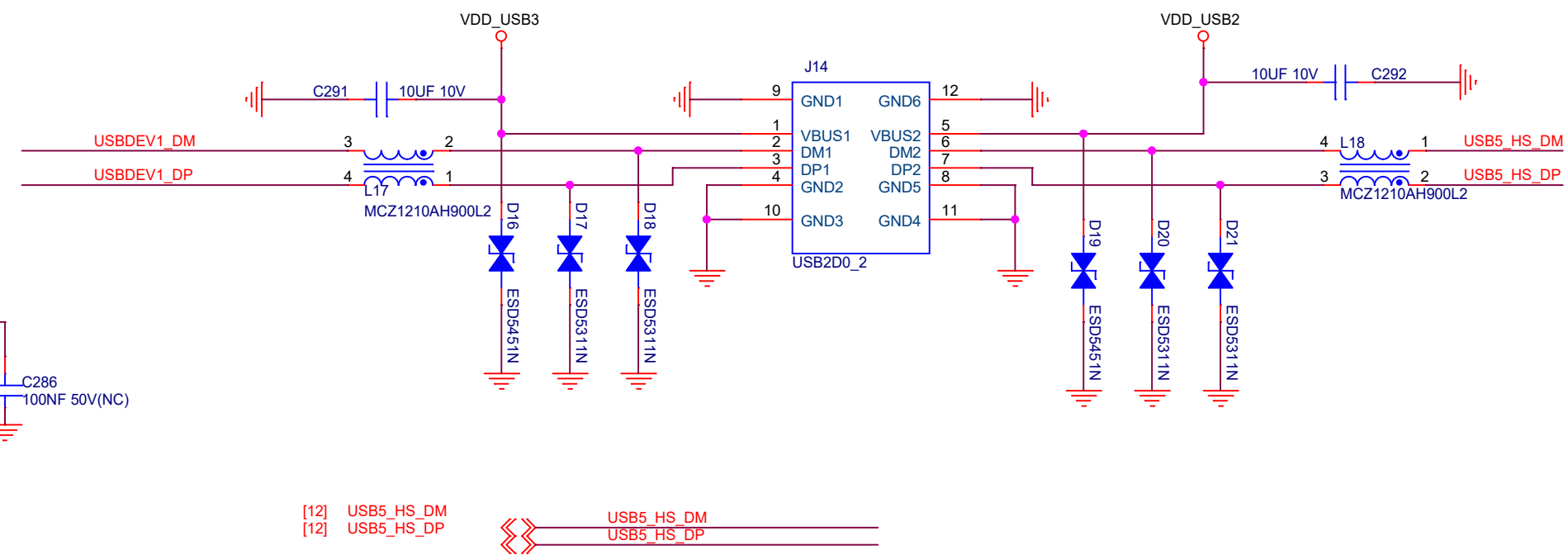
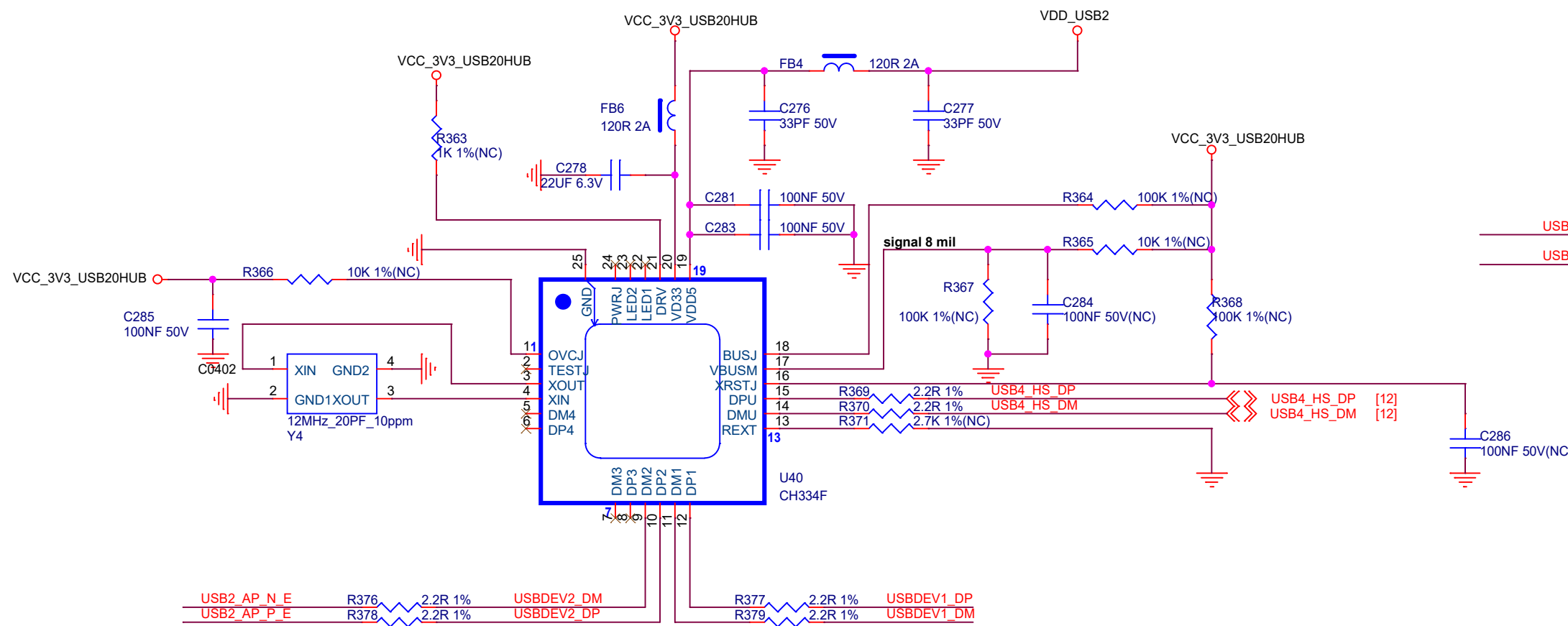
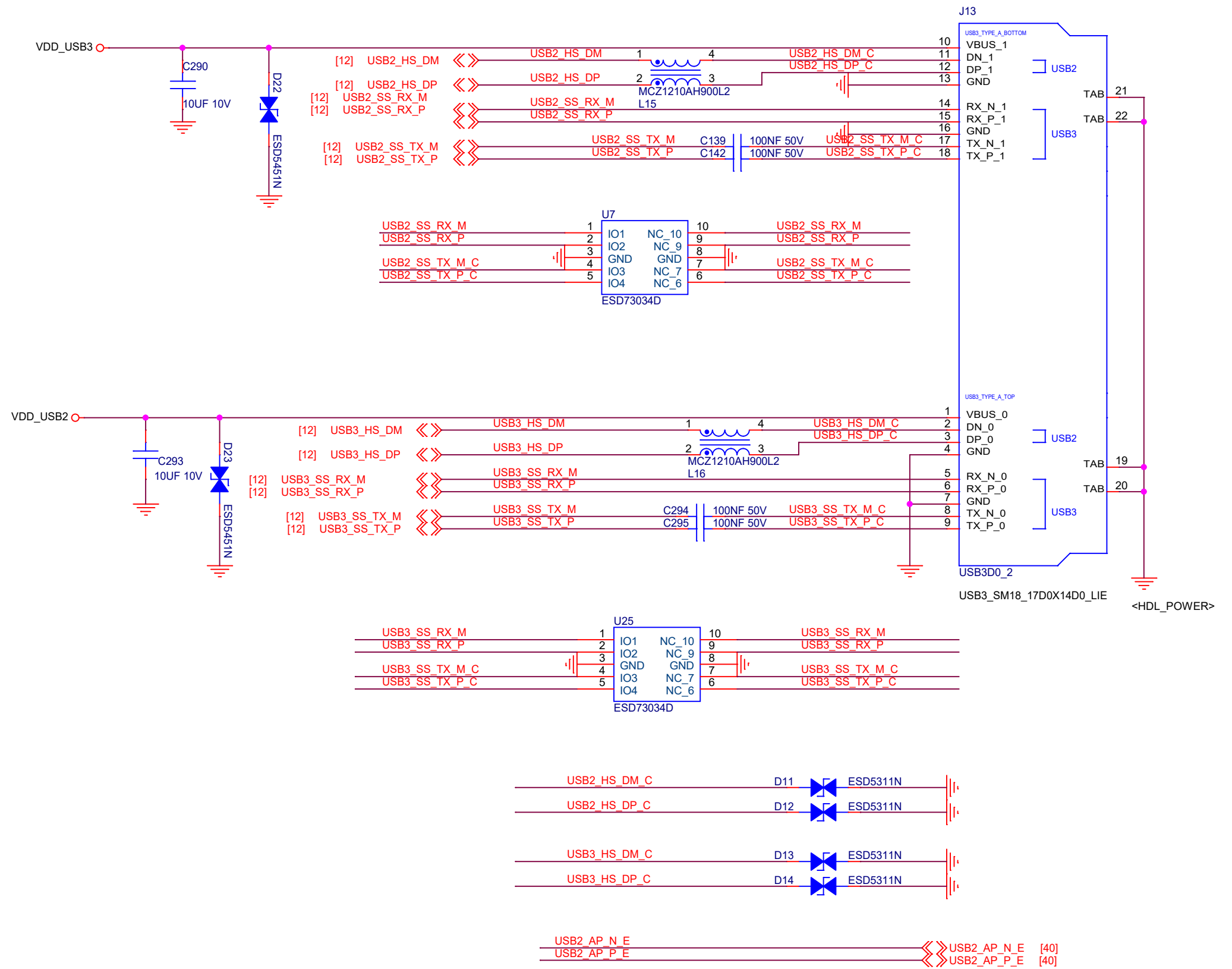
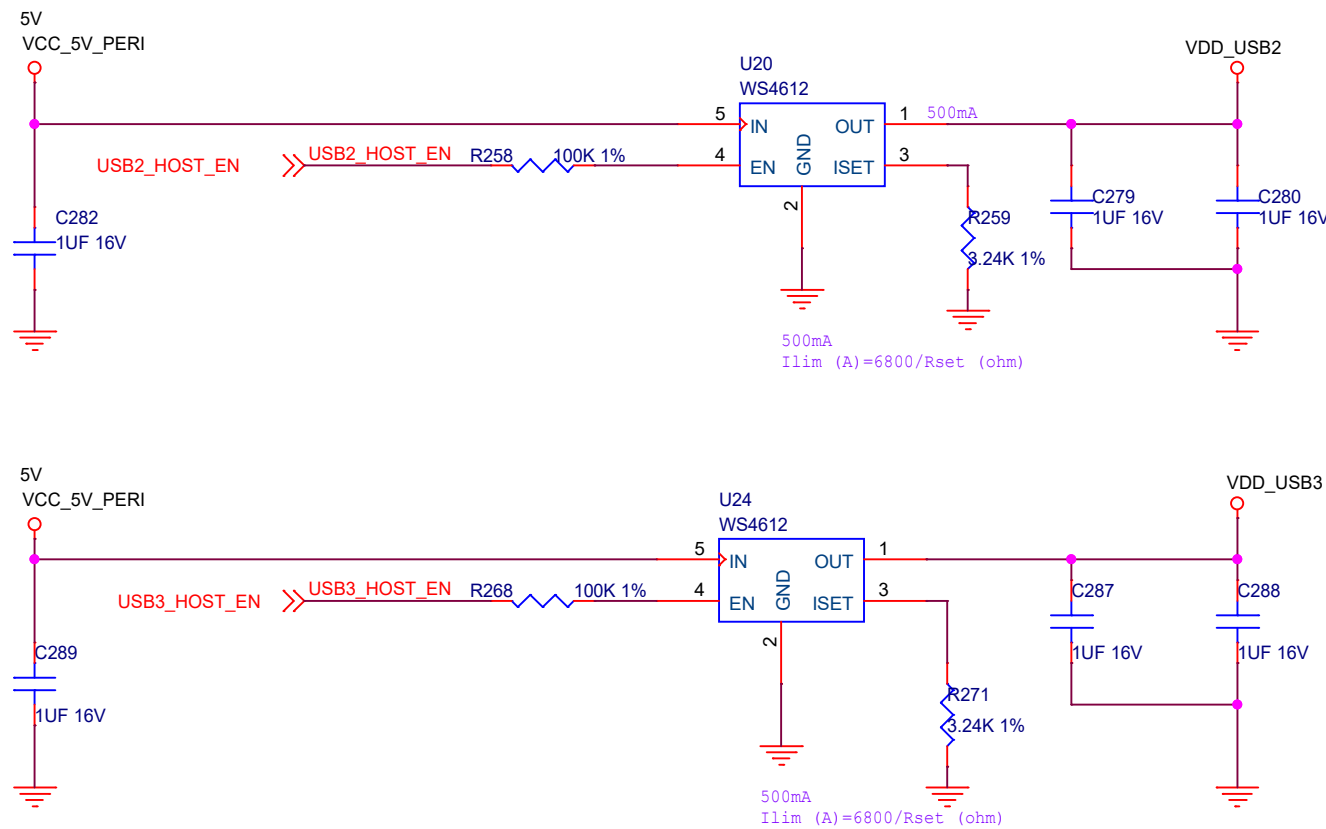
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M.2 M KEY



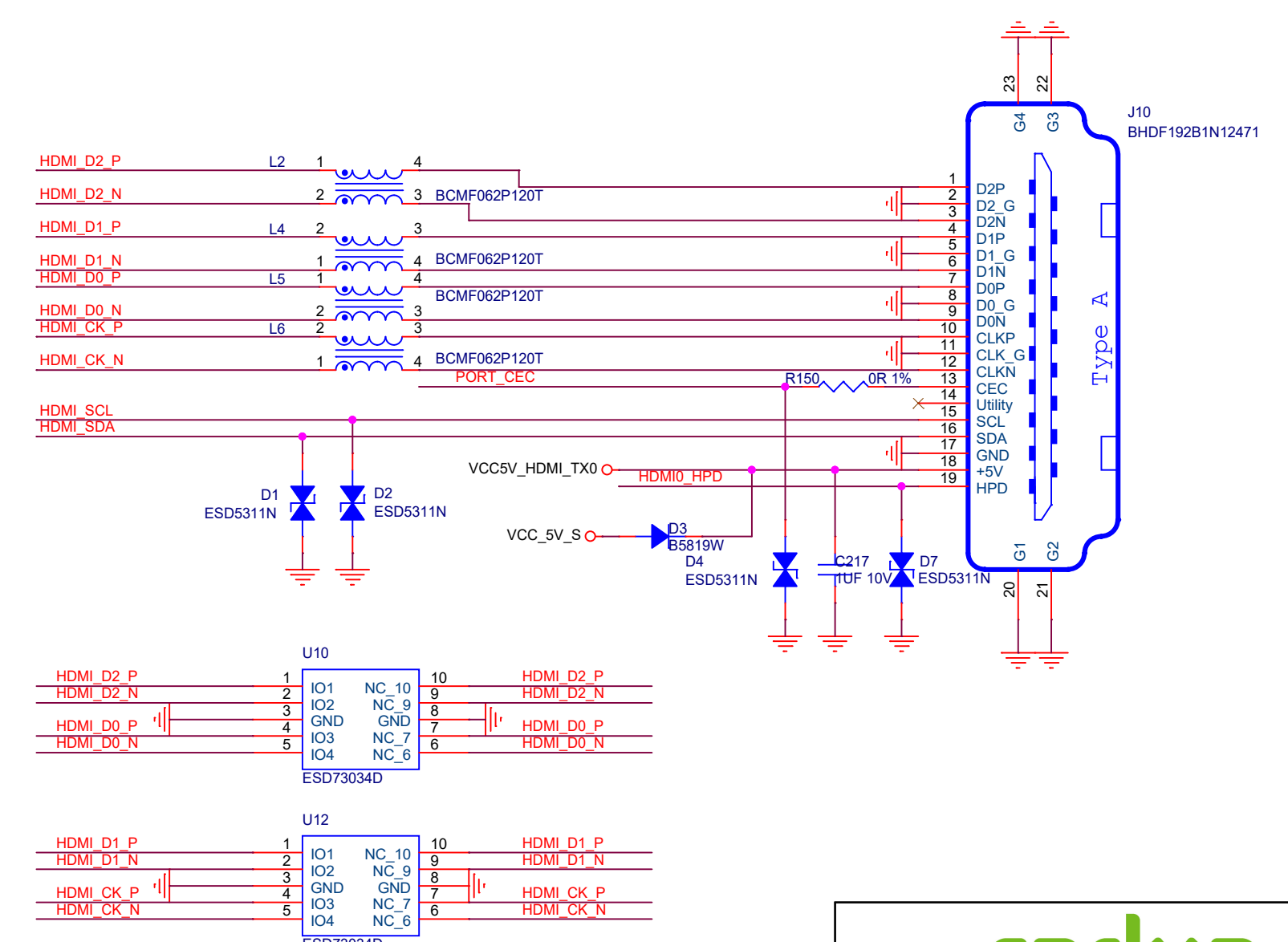
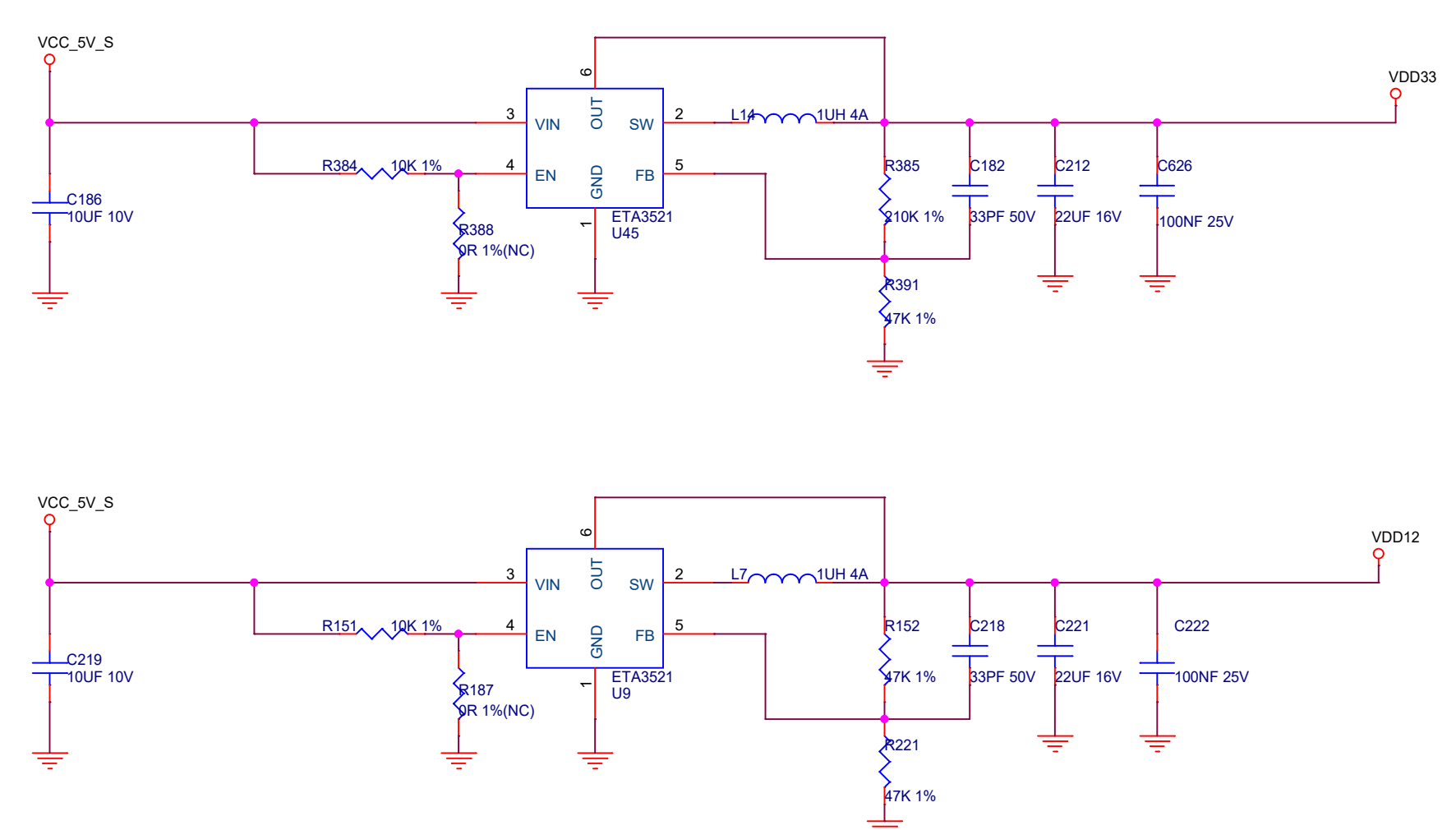
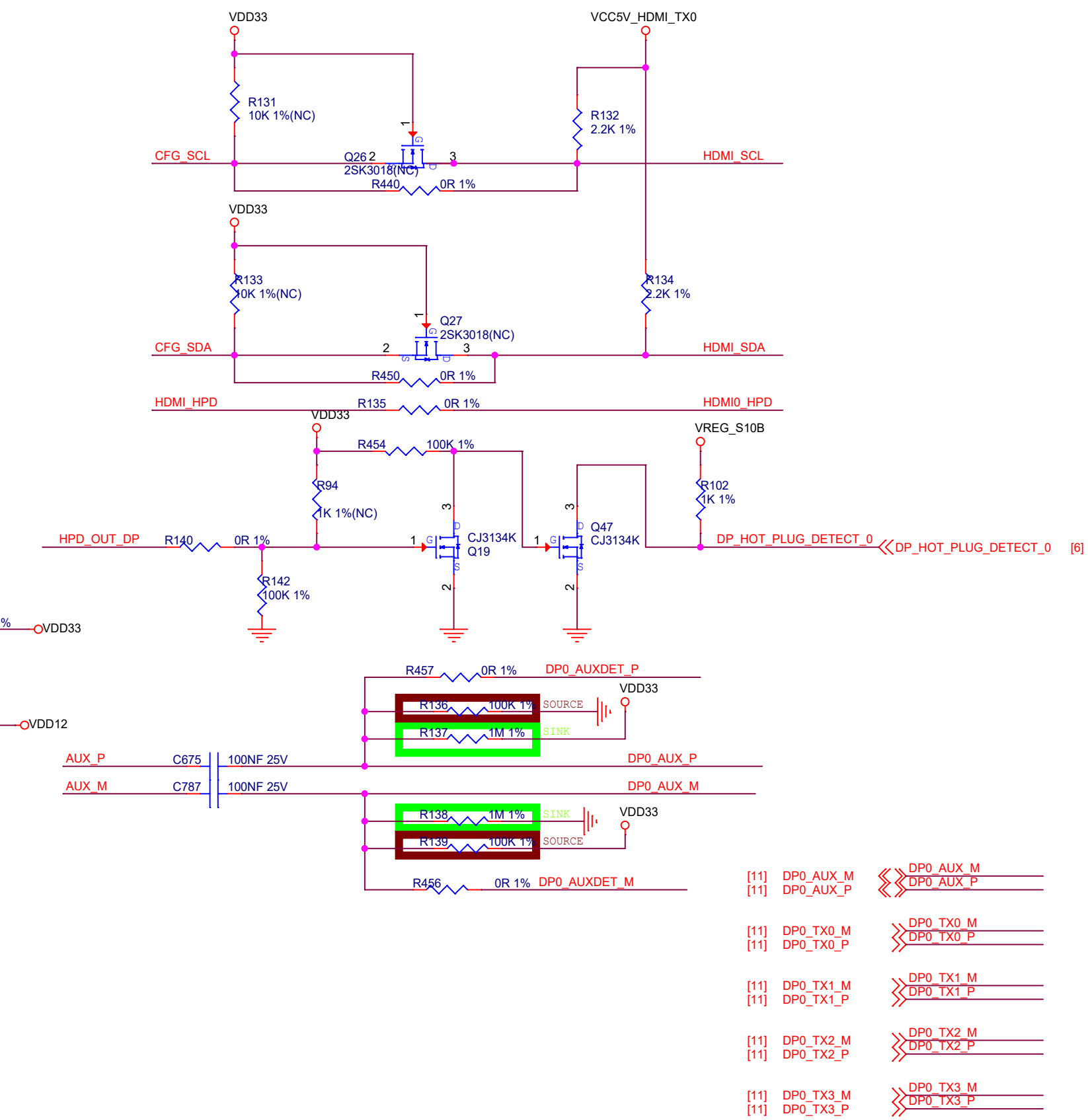
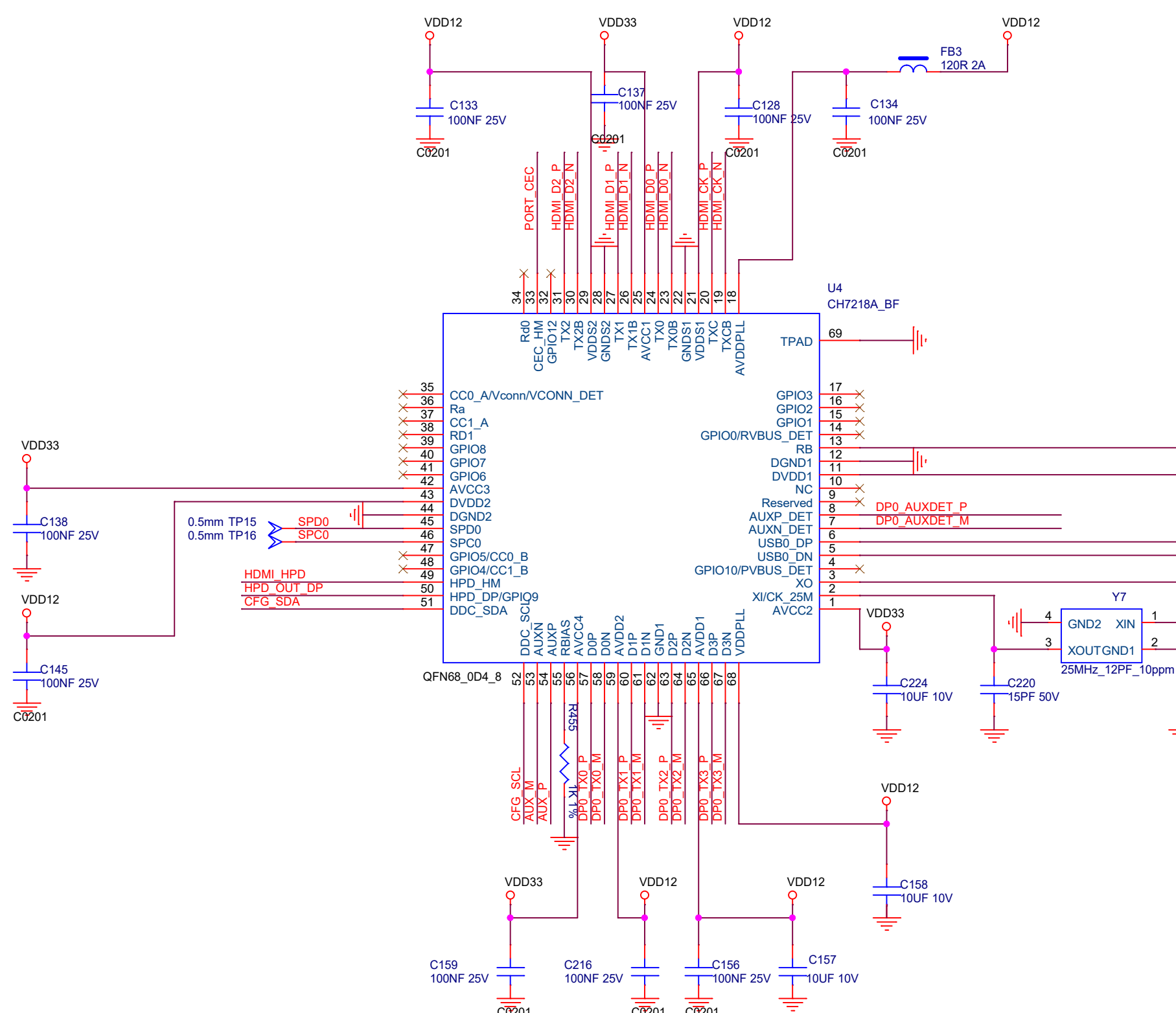
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DP TO HDMI

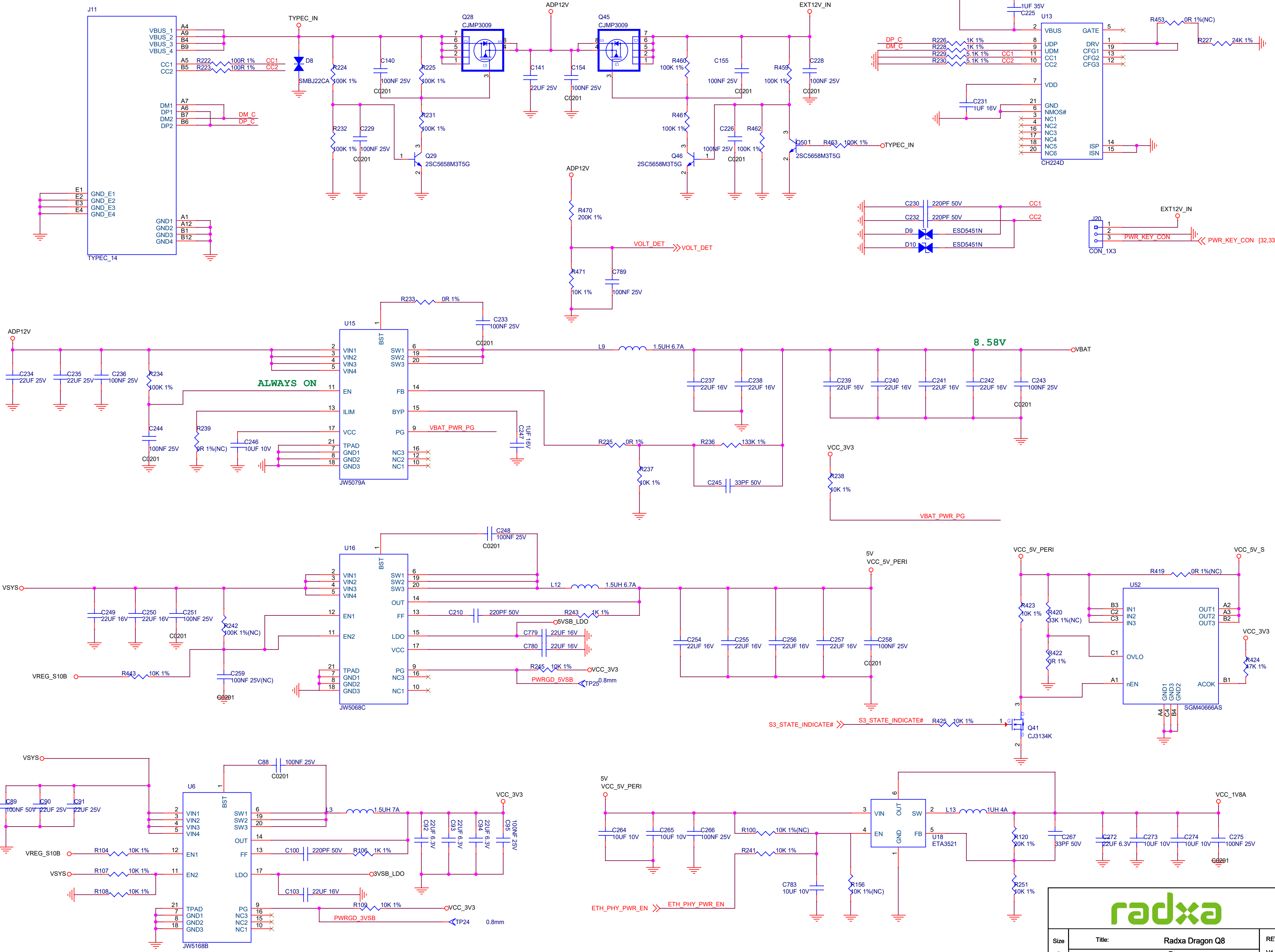


radxa

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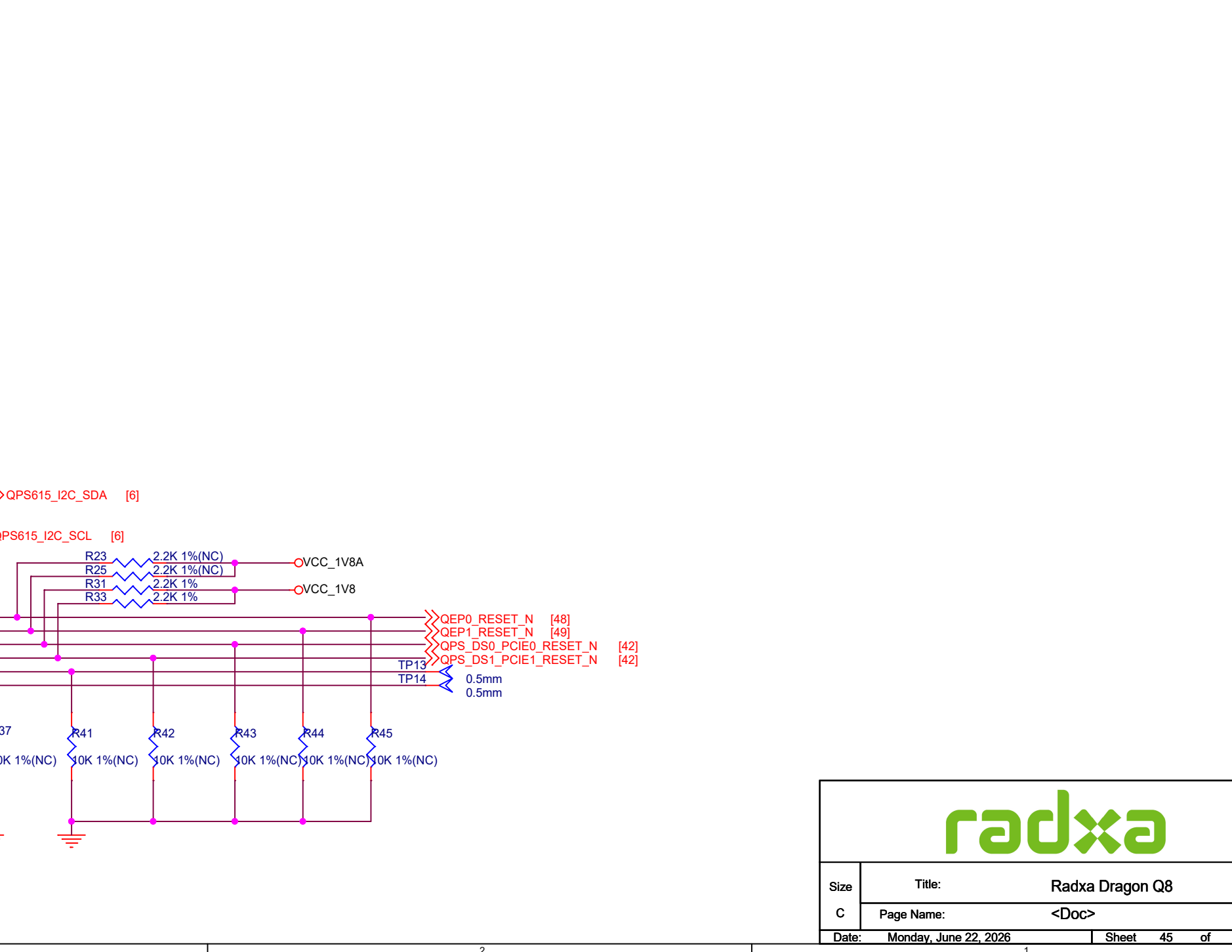
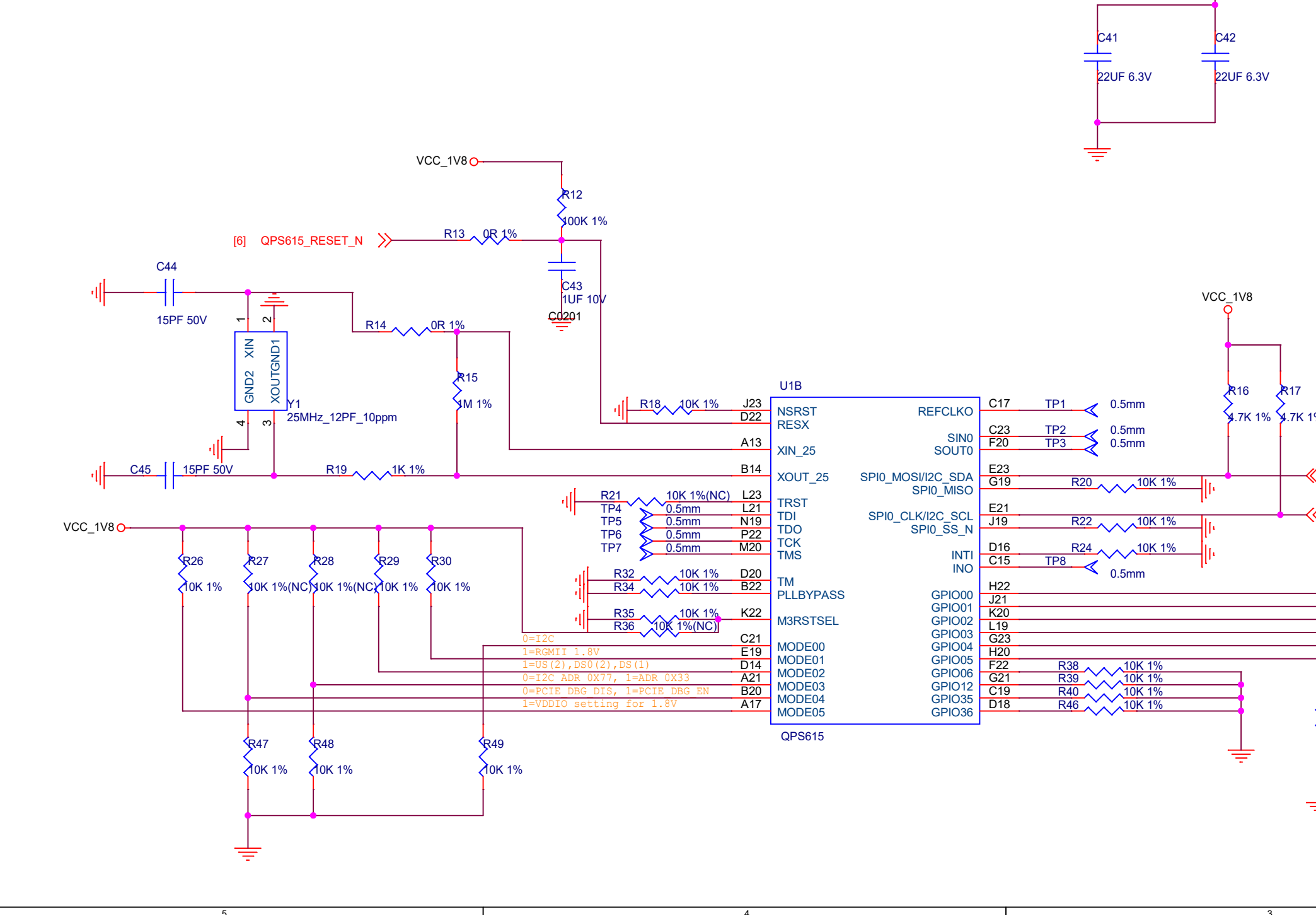
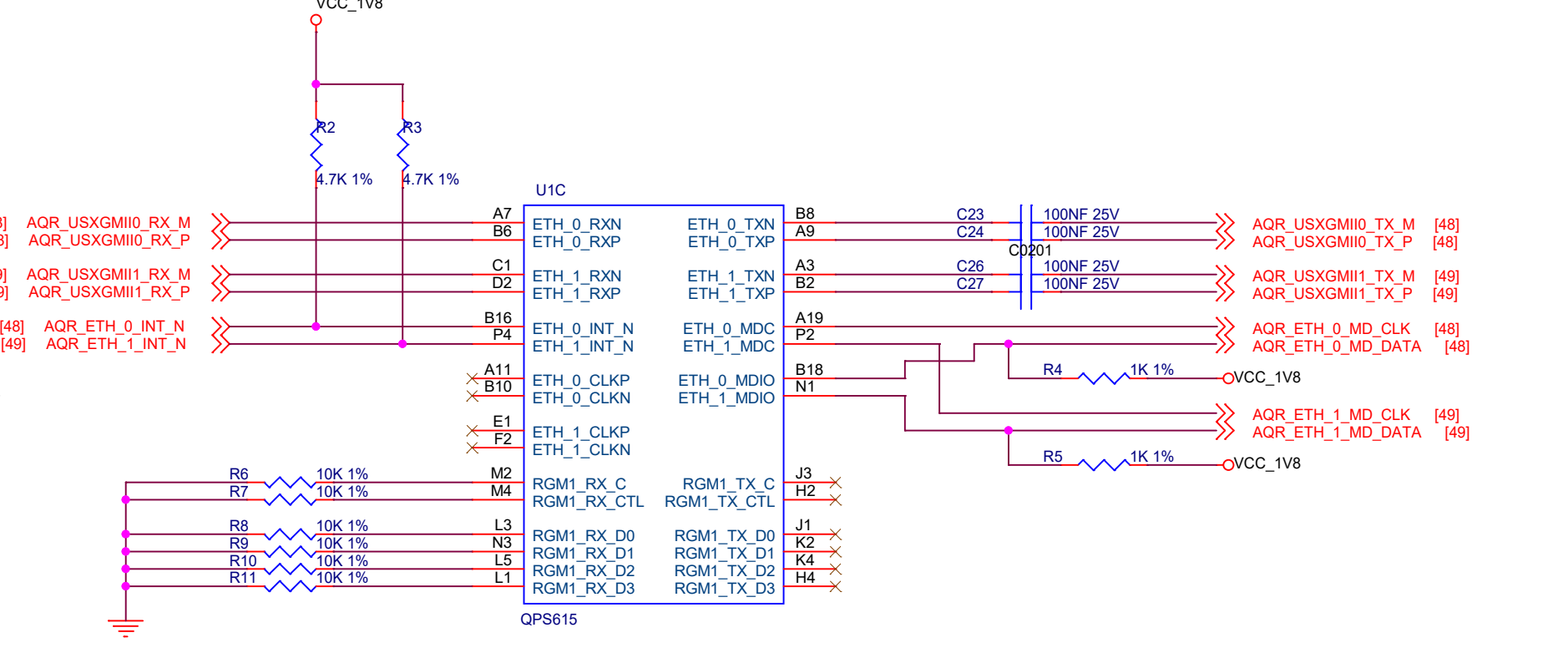
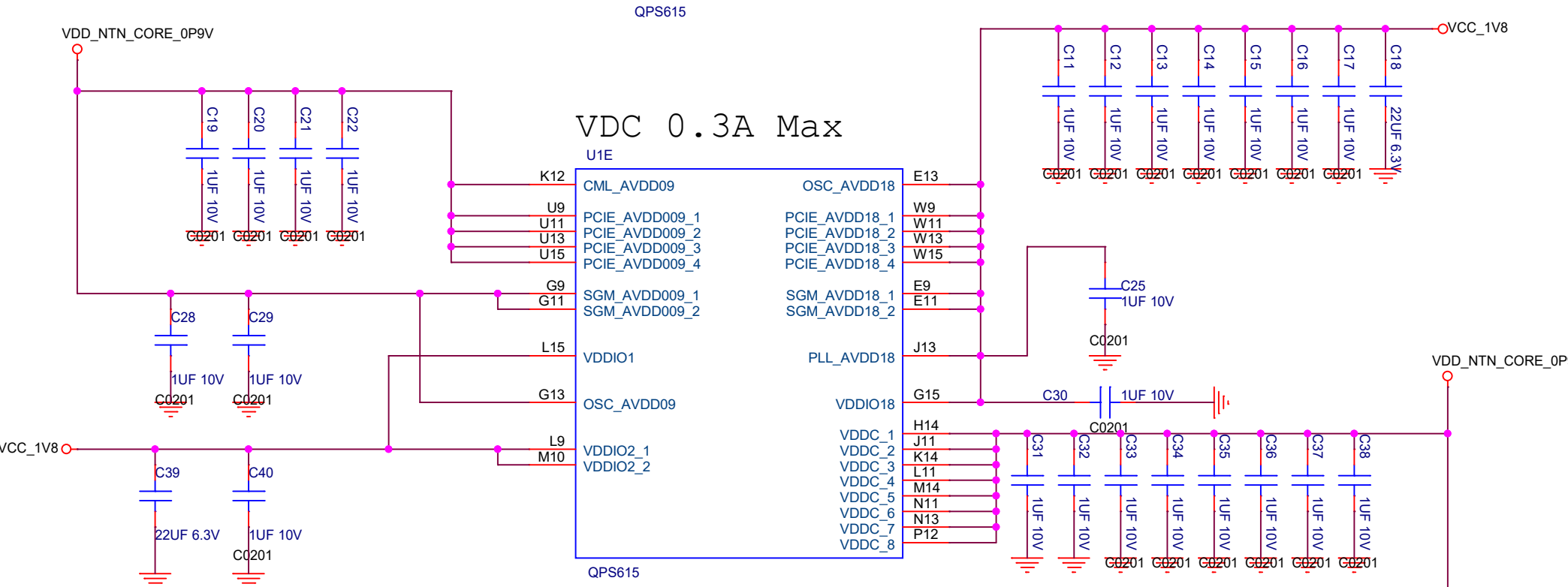
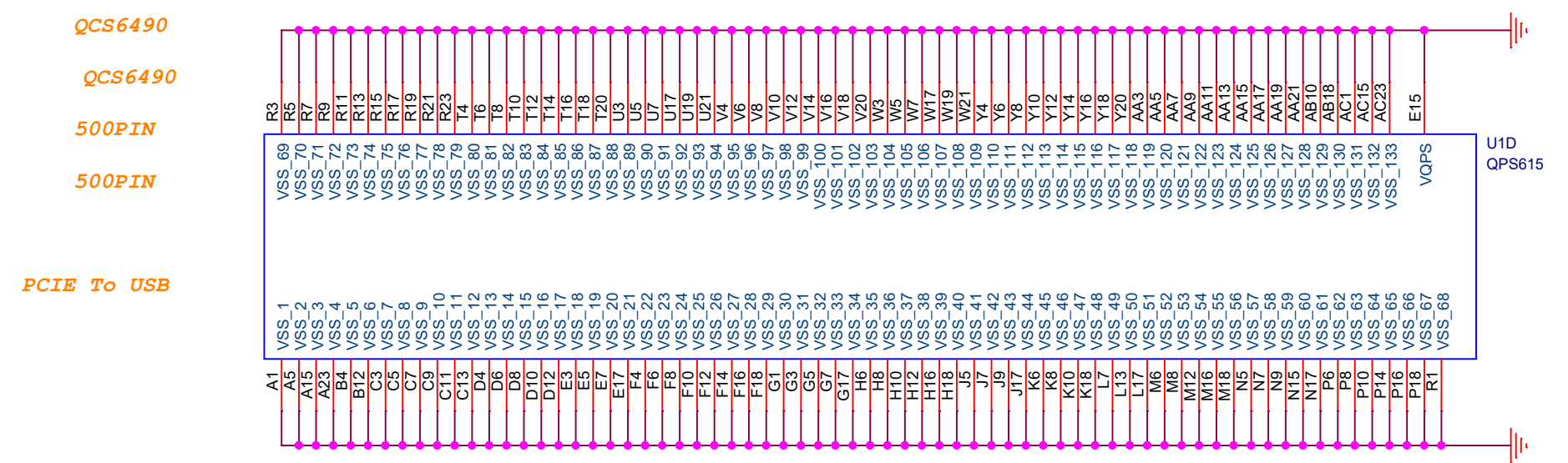
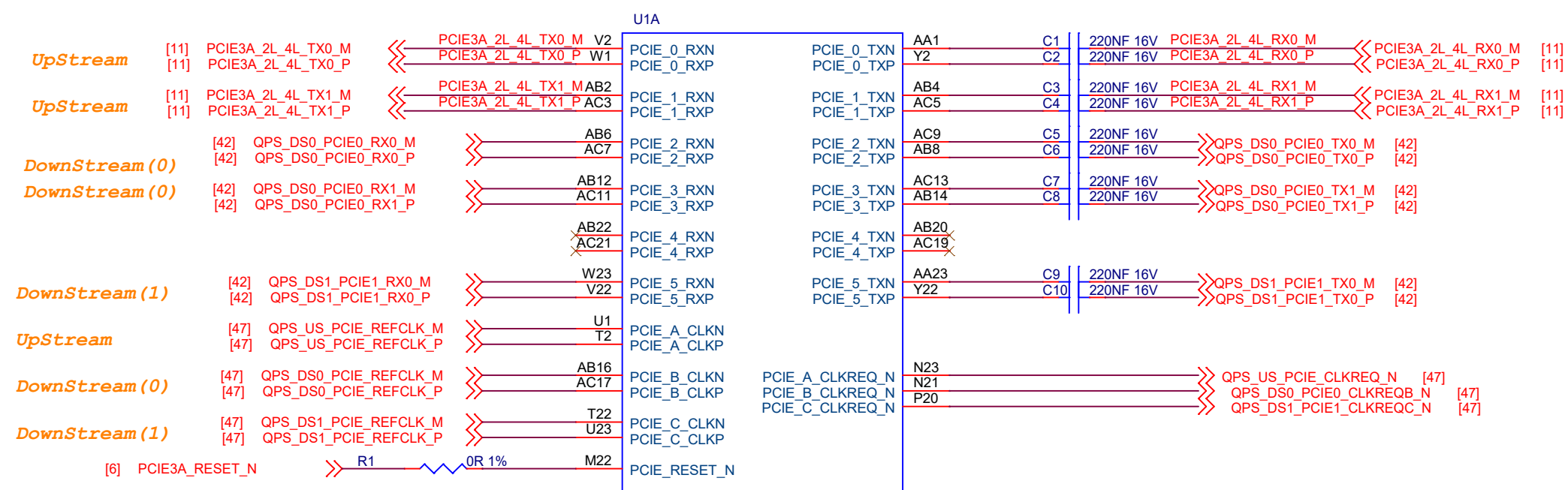
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24K	12V
56K	15V
NC	20V



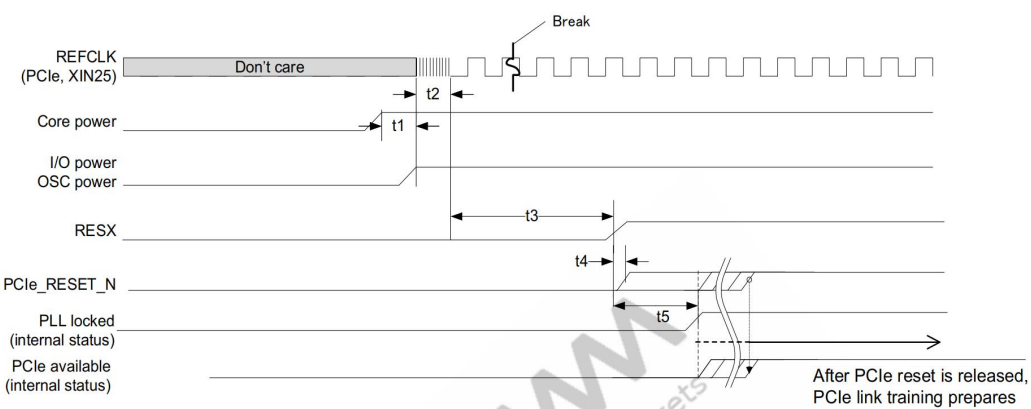
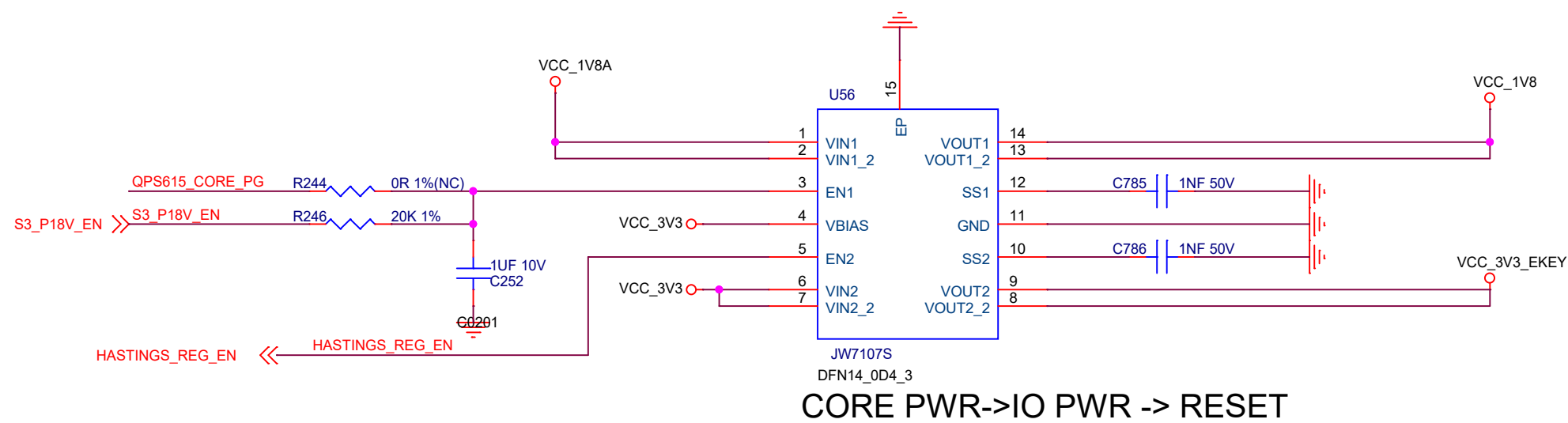
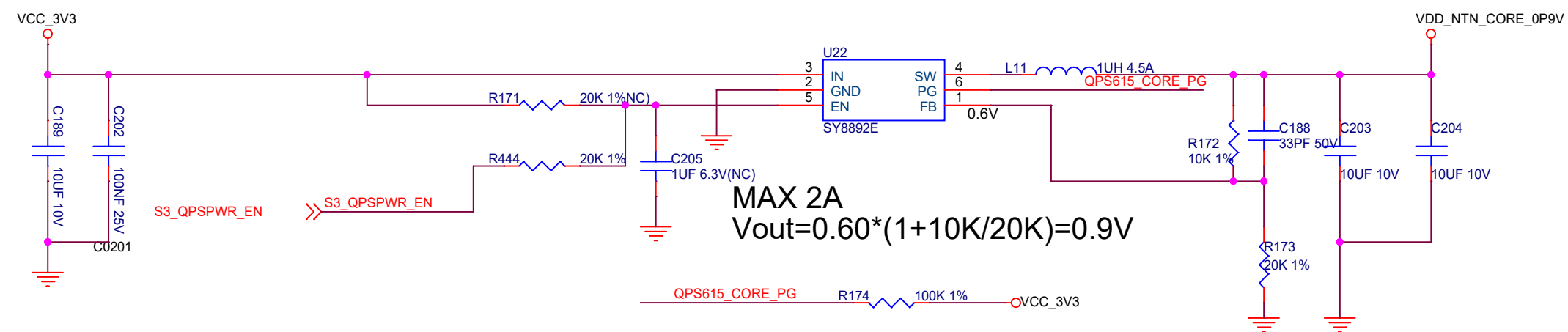
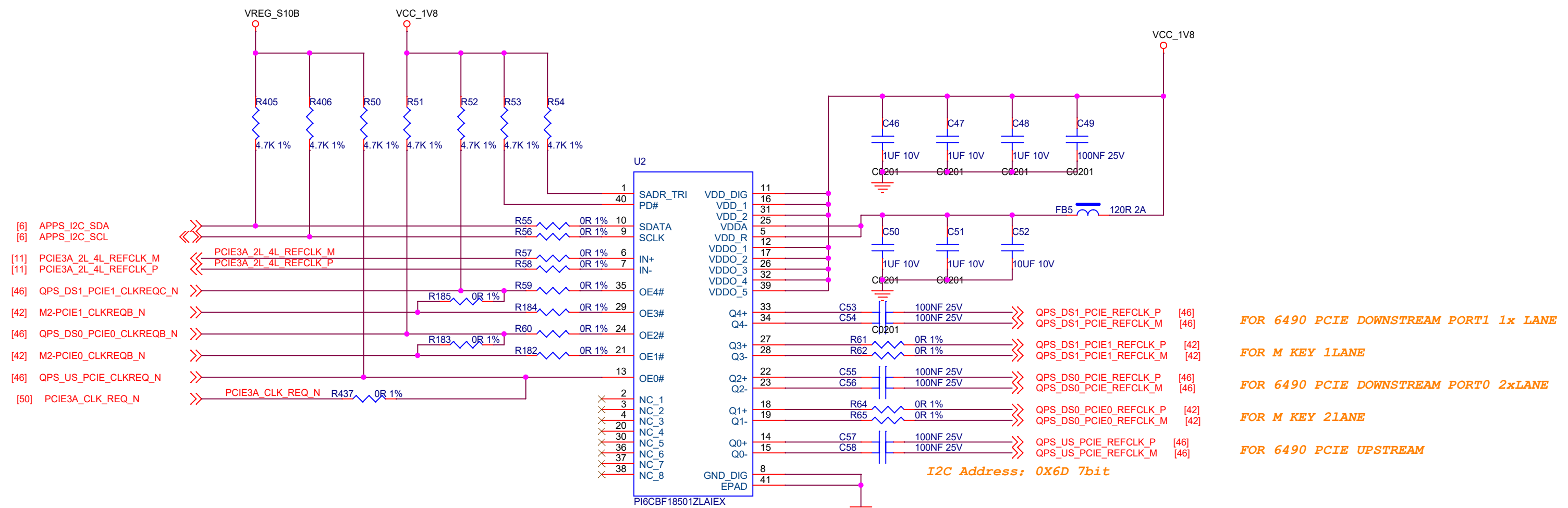
radxa

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PCIE BRIDGE



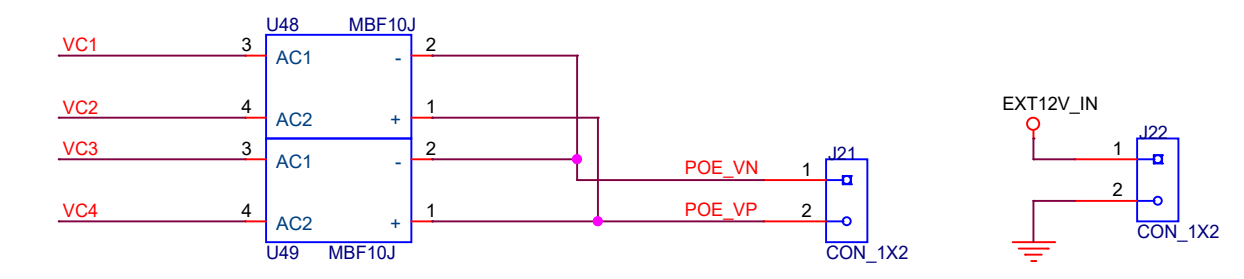
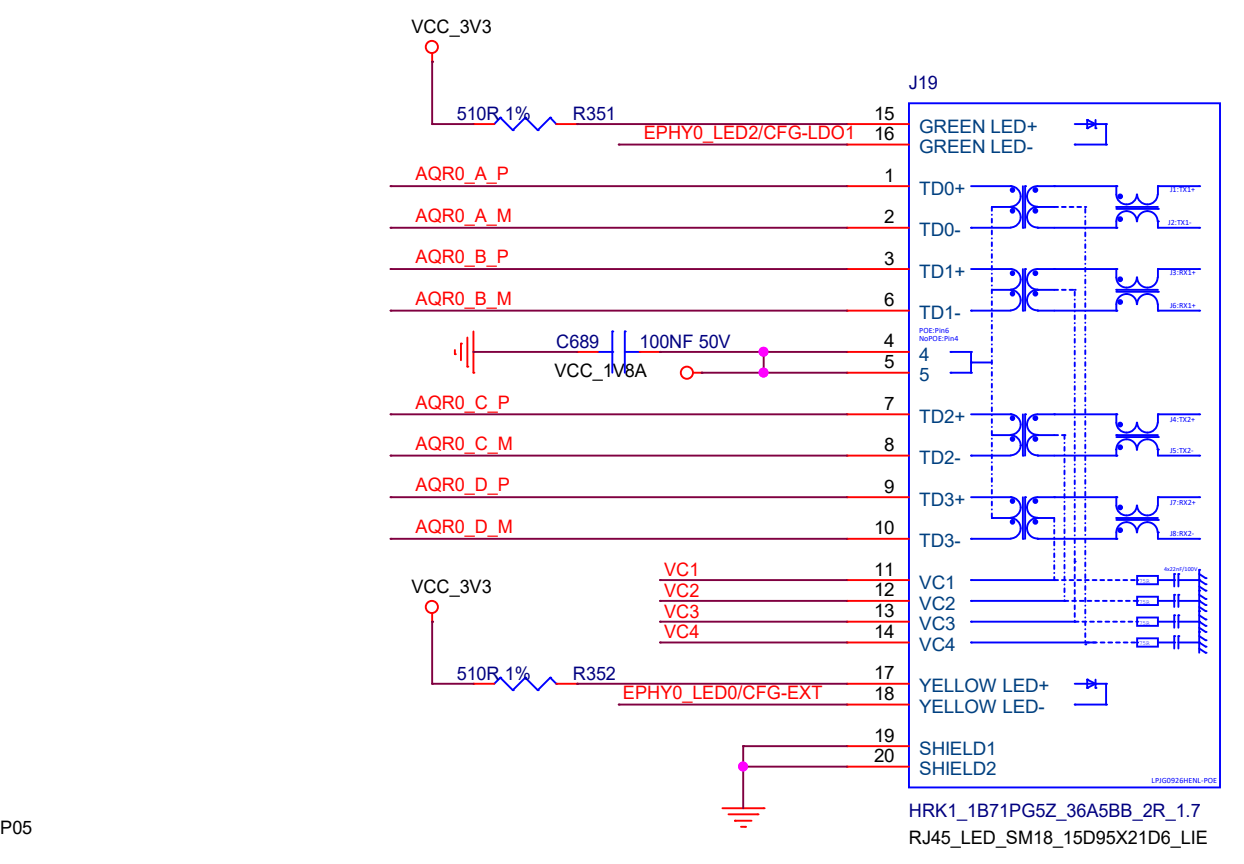
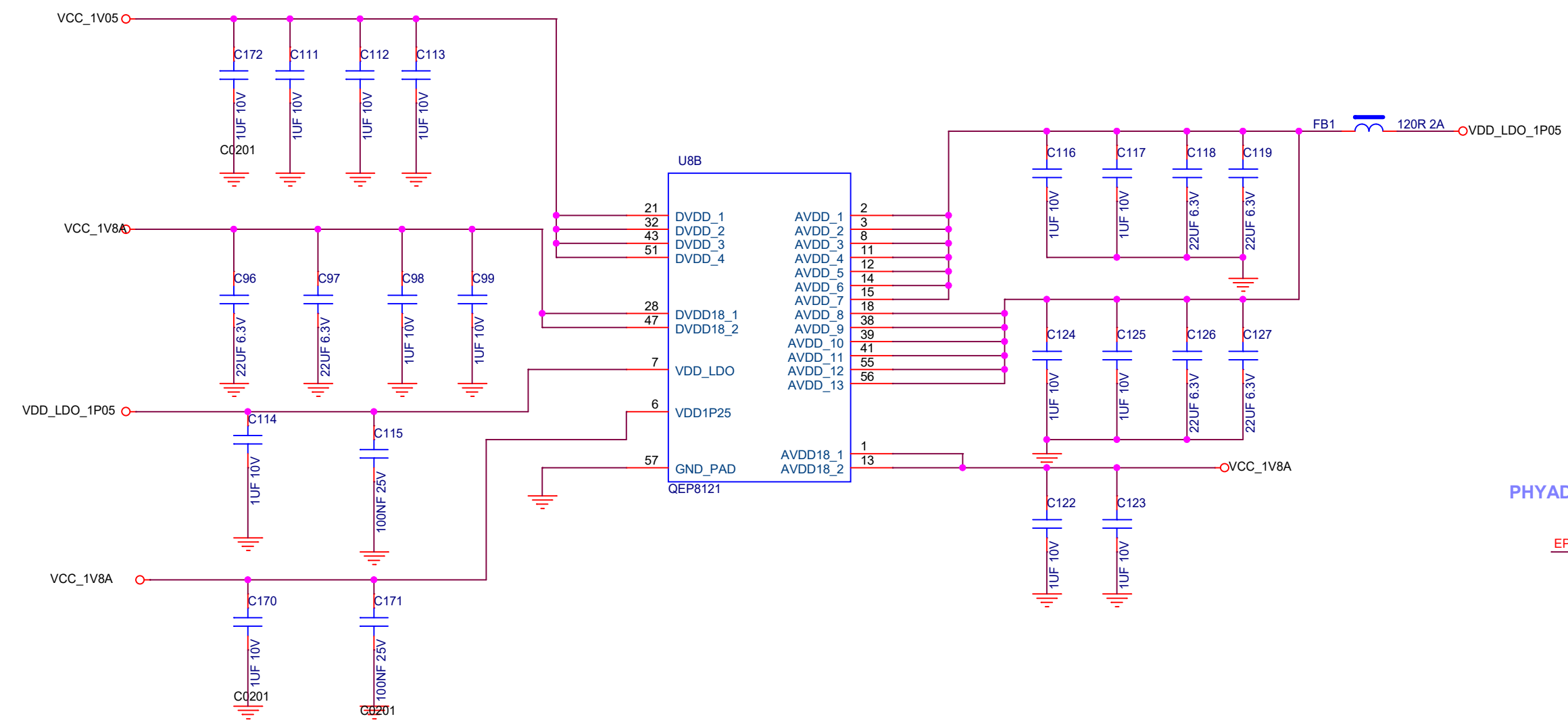
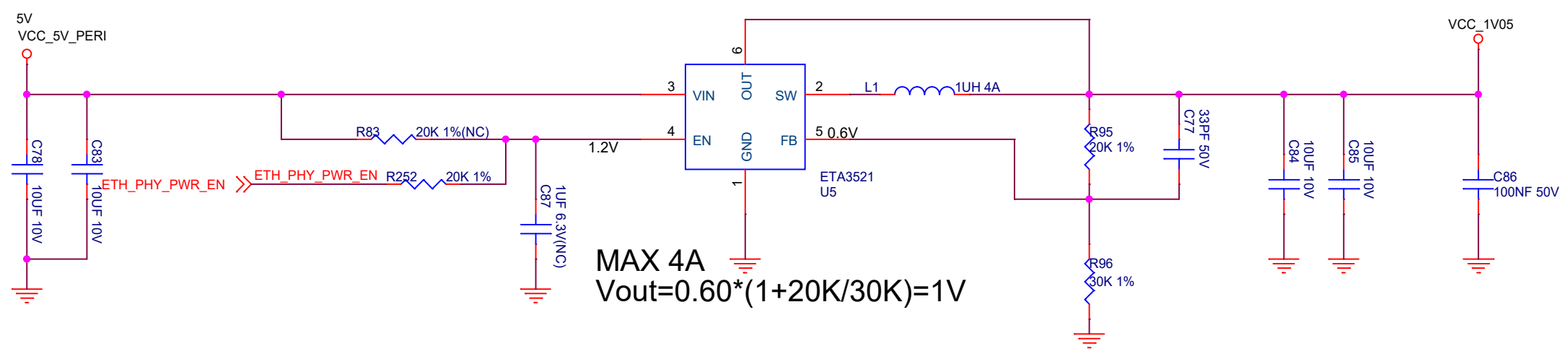
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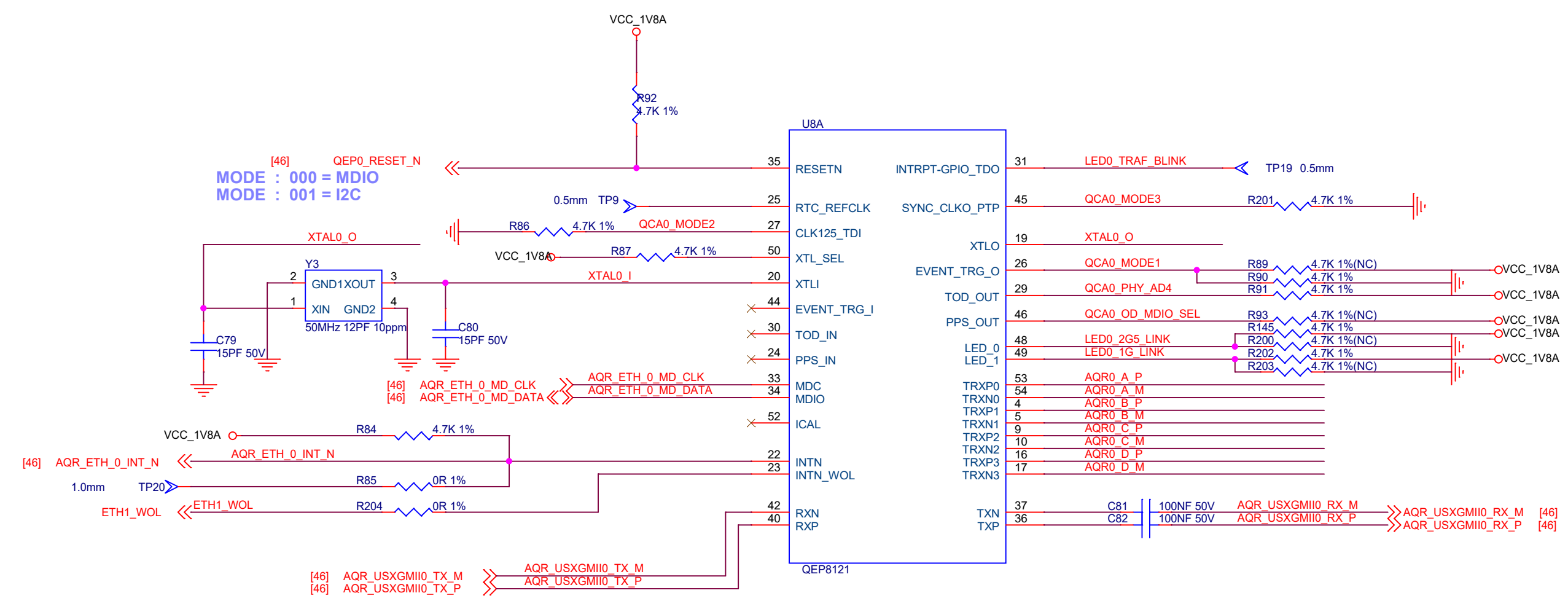
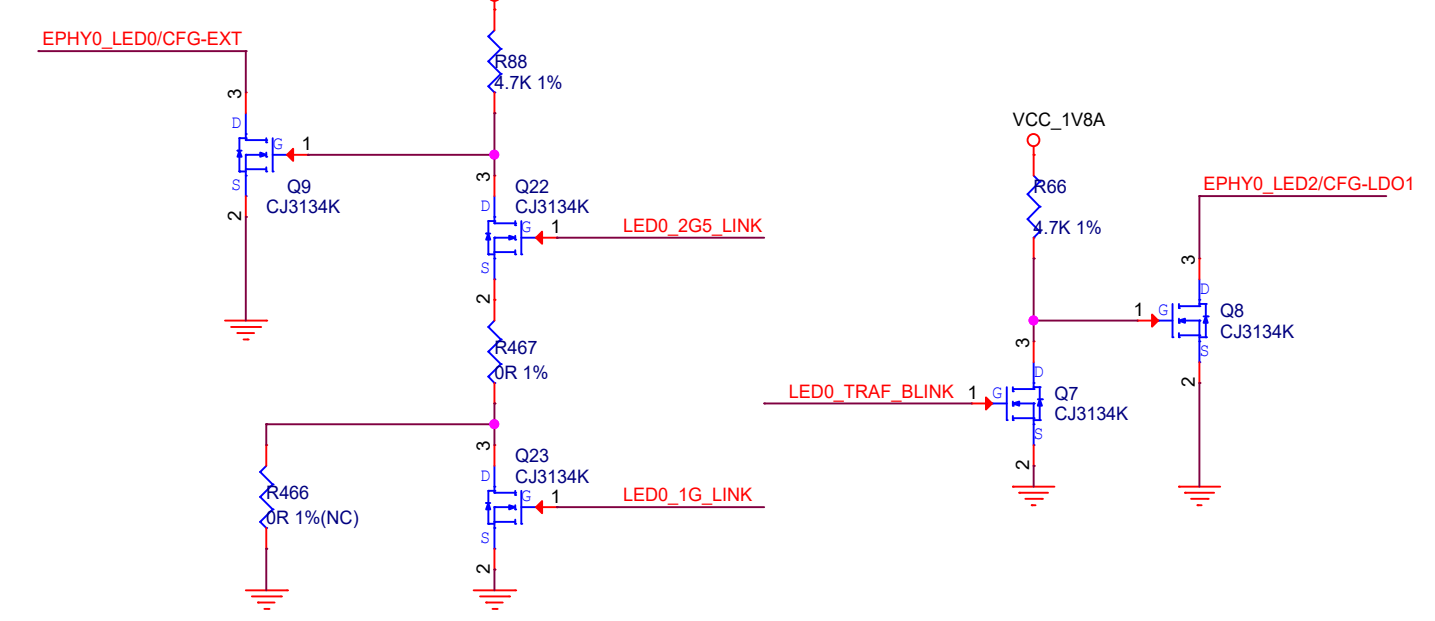
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ETH_2.5G_0



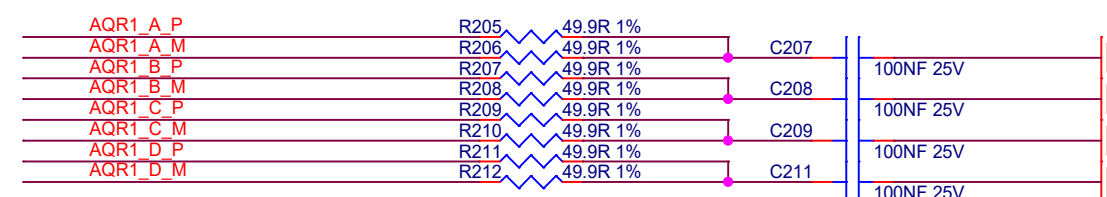
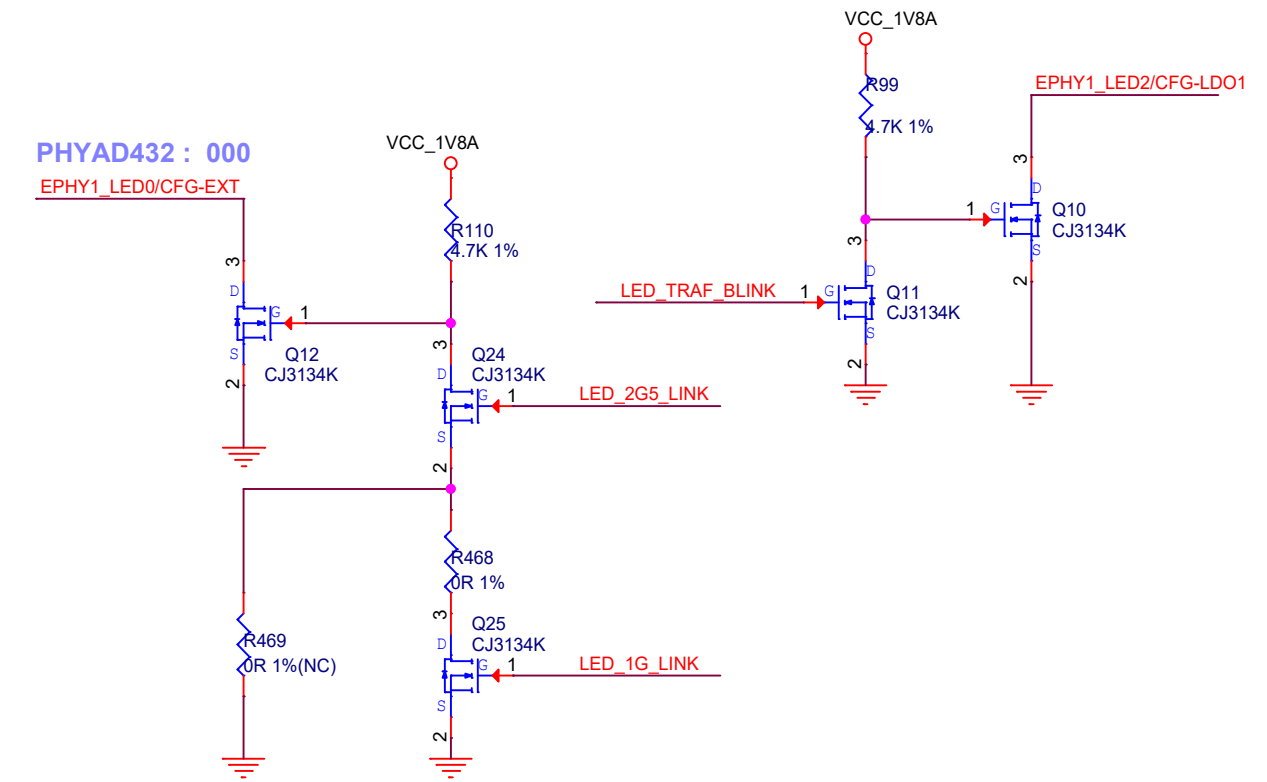
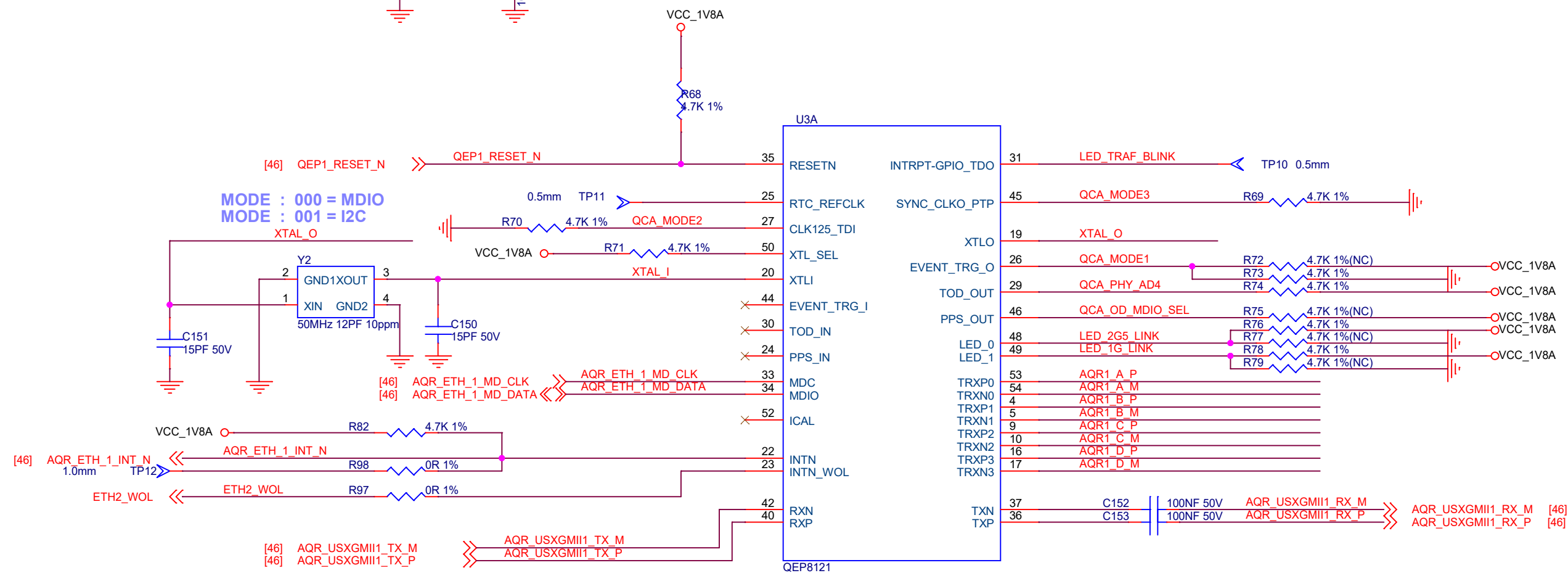
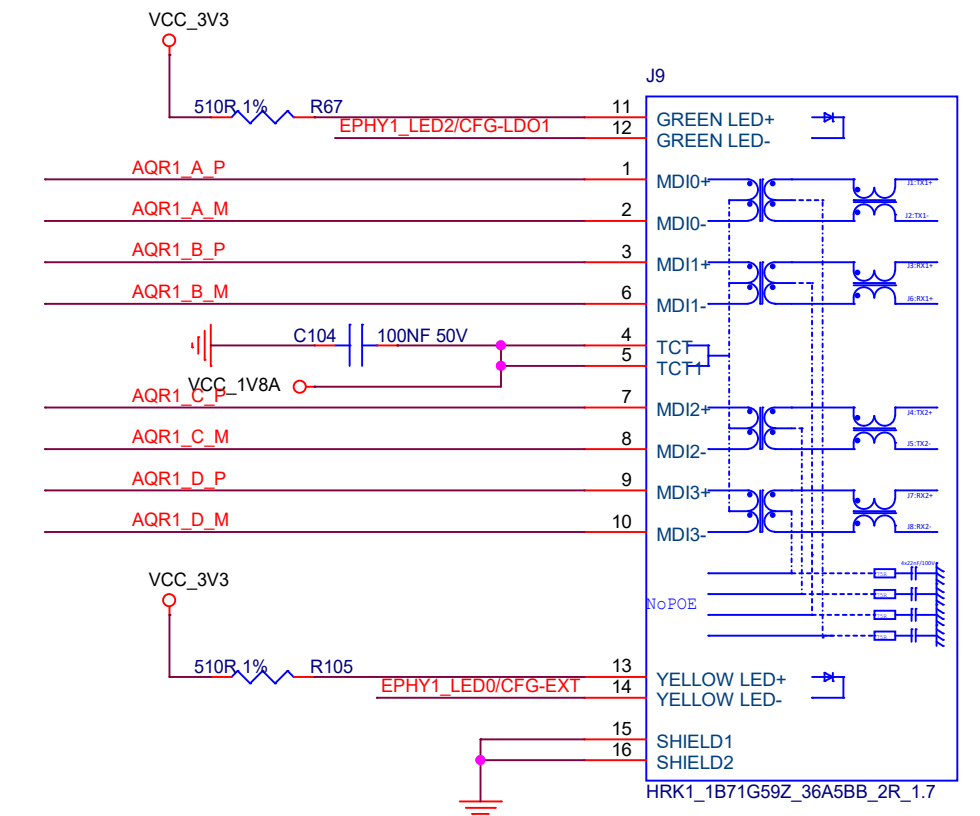
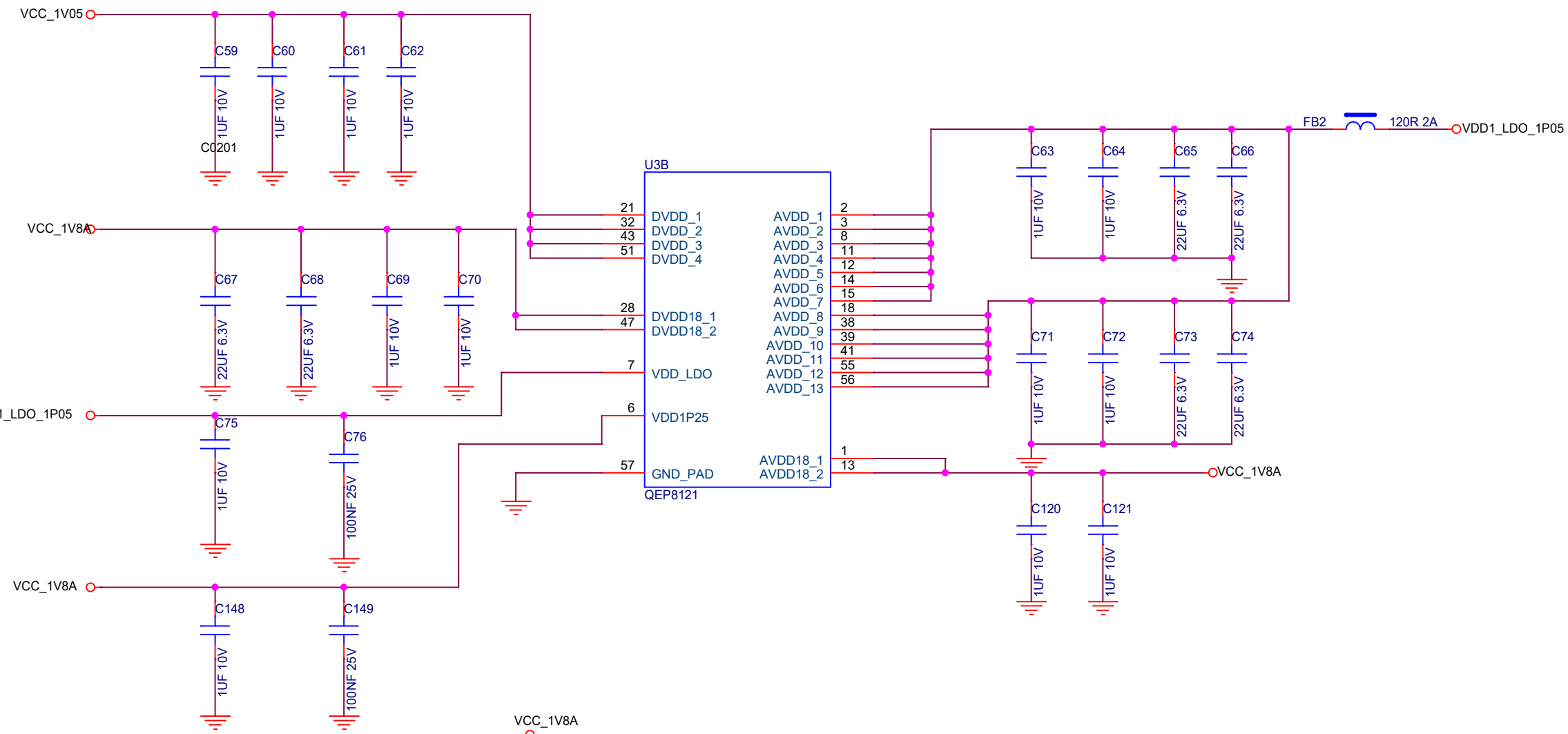
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AQR0_B_P	R114	49.9R 1%		
AQR0_B_M	R147	49.9R 1%	C174	100NF 25V
AQR0_C_P	R148	49.9R 1%		
AQR0_C_M	R149	49.9R 1%	C175	100NF 25V
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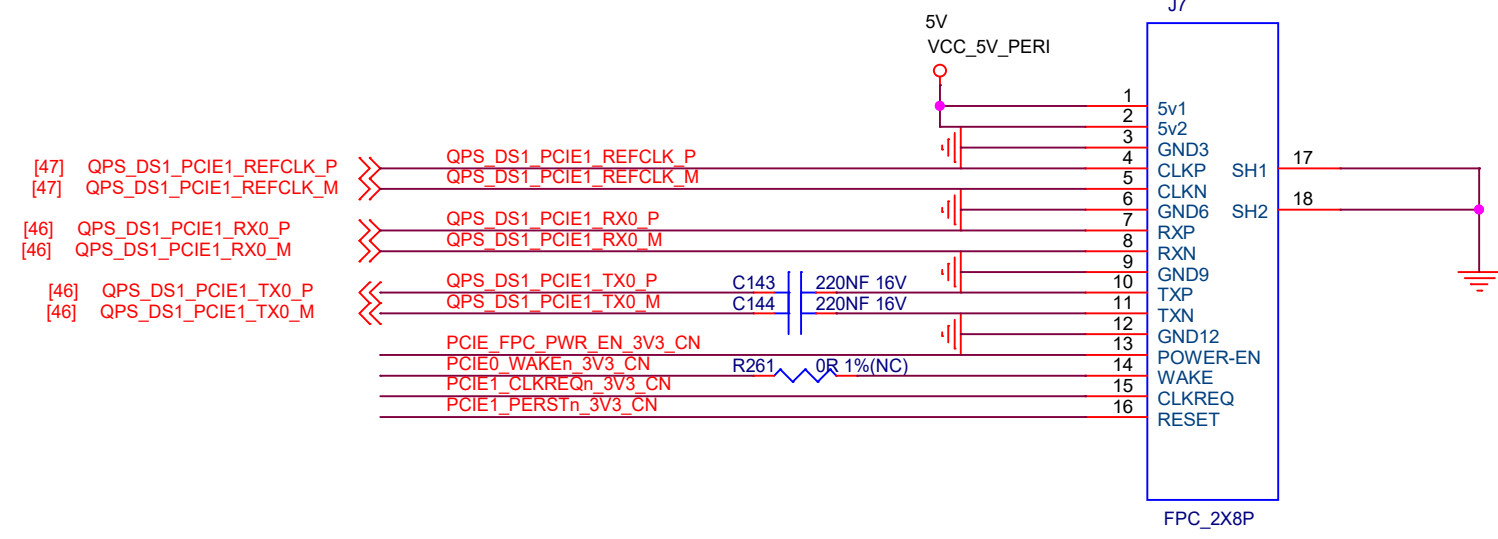
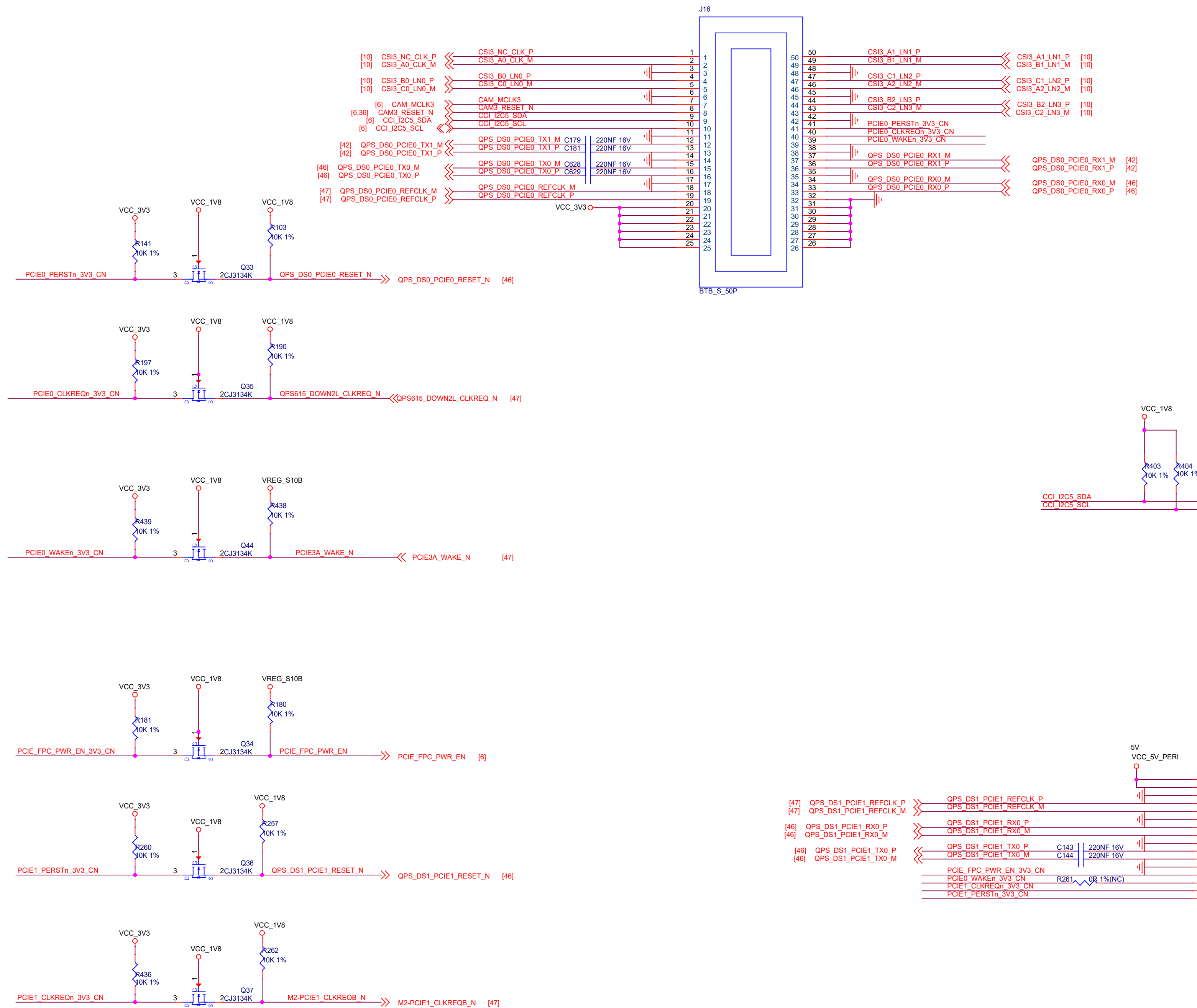


ETH_2.5G_1



radxa

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AUDIO

